

# ΕΡΓΑΣΤΗΡΙΟ ΣΥΓΧΡΟΝΗΣ ΑΡΧΙΤΕΚΤΟΝΙΚΗΣ

Τριαντάφυλλος Πράππας ΑΜ:1067504

## ΕΡΓΑΣΤΗΡΙΑΚΗ ΑΣΚΗΣΗ 5

BOOTST RAP	BRA	BI N	CO N	I	I	I	APO RT	BPO RT	DDA TA	SH ~	SE LB	MW E~	MARC LK	MSTAT US	LD S~	PC E~	CARR YE~	MD E~	DDAT AE~	ADDR ESS
	(4:0)	(2:0)	(2:0)	(2:0)	(5:3)	(8:6)	(3:0)	(3:0)	(1:0)											
SW+0- >PC,MAR	XXX XX	00 0	XX X	11 1	00 0	01 1	XXX X	1000	XX	0	1	1	1	0	1	0	1	1	1	m00
NEXT(PC )	XXX XX	00 0	XX X	00 0	00 0	00 1	XXX X	XXX X	XX	0	0	1	0	0	0	0	0	0	0	m01

LOAD R, \$K	BRA	BI N	CO N	I	I	I	APO RT	BPO RT	DDA TA	SH ~	SE LB	MW E~	MARC LK	MSTAT US	LD S~	PC E~	CARR YE~	MD E~	DDATA E~	ADDRE SS
	(4:0)	(2:0)	(2:0)	(2:0)	(5:3)	(8:6)	(3:0)	(3:0)	(1:0)											
PC+1- >PC,M AR	XXX XX	00 0	XX X	10 1	00 0	01 1	1000	1000	01	0	1	1	1	0	1	1	1	1	0	m02
MDR+0 ->X	XXX XX	00 0	XX X	11 1	00 0	01 1	XXX X	1010	XX	0	1	1	0	0	1	1	1	0	1	m03
PC+1- >PC,M AR	XXX XX	00 0	XX X	10 1	00 0	01 1	1000	1000	01	0	1	1	1	0	1	1	1	1	0	m04
MDR+0 ->MAR	XXX XX	00 0	XX X	11 1	00 0	00 1	XXX X	XXX X	XX	0	1	1	1	0	1	1	1	0	1	m05
X+0 - >NOP	XXX XX	00 0	XX X	10 0	00 0	00 1	1010	XXX X	XX	0	1	1	0	0	1	1	1	1	0	m06
MDR+0 ->RAMF	XXX XX	00 0	XX X	10 0	00 0	00 1	XXX X	XXX X	XX	0	0	1	0	0	1	1	1	0	1	m07
PC+1- >PC,M AR	XXX XX	00 0	XX X	10 1	00 0	01 1	1000	1000	01	0	1	1	1	0	1	1	1	1	0	m08
NEXT( PC)	XXX XX	00 0	XX X	00 0	00 0	00 1	XXX X	XXX X	XX	0	0	1	0	0	0	0	0	0	0	m09

PC + 1 -> PC,MAR >> Το όνομα του R στο MDR

MDR + 0 -> X >> Το όνομα του R στο X

PC + 1 -> PC,MAR >> Δείχνει στην επόμενη θέση μνήμης

MDR + 0 -> NOP, MAR >> MAR <-K, MDR<-(K)

X + 0 -> NOP >> Το όνομα του R στον feedback register.

MDR + 0 -> RAMF >> Στον MDR βρίσκονται τα περιεχόμενα της θέσης μνήμης με διεύθυνση K. Τροφοδοτούμε το PortB με την τιμή του FeedbackReg που είναι ο R και κάνουμε εγγραφή σε αυτόν τα περιεχόμενα της διεύθυνσης K από τον MDR.

NEXT(PC) >> φόρτωση στον μPC του περιεχομένου της διεύθυνσης μνήμης την οποία υποδεικνύει ο Mapper

STORE R, \$K	BRA	BI N	CO N	I	I	I	APO RT	BPO RT	DDA TA	SH ~	SE LB	MW E~	MARC LK	MSTAT US	LD S~	PC E~	CARR YE~	MD E~	DDATA E~	ADDRE SS
	(4:0)	(2: 0)	(2: 0)	(2: 0)	(5: 3)	(8: 6)	(3:0)	(3:0)	(1:0)											
PC+1- >PC,M AR	XXX XX	00 0	XX X	10 1	00 0	01 1	1000	1000	01	0	1	1	1	0	1	1	1	1	0	m0a
MDR+0 ->X	XXX XX	00 0	XX X	11 1	00 0	01 1	XXX X	1010	XX	0	1	1	0	0	1	1	1	0	1	m0b
PC+1- >PC,M AR	XXX XX	00 0	XX X	10 1	00 0	01 1	1000	1000	01	0	1	1	1	0	1	1	1	1	0	m0c
MDR+0 ->MAR	XXX XX	00 0	XX X	11 1	00 0	00 1	XXX X	XXX X	XX	0	1	1	1	0	1	1	1	0	1	m0d
X+0 - >NOP	XXX XX	00 0	XX X	10 0	00 0	00 1	1010	XXX X	XX	0	1	1	0	0	1	1	1	1	0	m0e
portB + 0 -> MDR	XXX XX	00 0	XX X	01 1	00 0	00 1	XXX X	XXX X	XX	0	0	0	0	0	1	1	1	1	1	m0f
PC+1- >PC,M AR	XXX XX	00 0	XX X	10 1	00 0	01 1	1000	1000	01	0	1	1	1	0	1	1	1	1	0	m10
NEXT( PC)	XXX XX	00 0	XX X	00 0	00 0	00 1	XXX X	XXX X	XX	0	0	1	0	0	0	0	0	0	0	m11

ADD R1, \$K	BRA	BI N	CO N	I	I	I	APO RT	BPO RT	DDA TA	SH ~	SE LB	MW E~	MARC LK	MSTAT US	LD S~	PC E~	CARR YE~	MD E~	DDATA E~	ADDRE SS
	(4:0)	(2: 0)	(2: 0)	(2: 0)	(5: 3)	(8: 6)	(3:0)	(3:0)	(1:0)											
PC+1- >PC,M AR	XXX XX	00 0	XX X	10 1	00 0	01 1	1000	1000	01	0	1	1	1	0	1	1	1	1	0	m12
MDR+0 ->X	XXX XX	00 0	XX X	11 1	00 0	01 1	XXX X	1010	XX	0	1	1	0	0	1	1	1	0	1	m13
PC+1- >PC,M AR	XXX XX	00 0	XX X	10 1	00 0	01 1	1000	1000	01	0	1	1	1	0	1	1	1	1	0	m14
MDR+0 ->MAR	XXX XX	00 0	XX X	11 1	00 0	00 1	XXX X	XXX X	XX	0	1	1	1	0	1	1	1	0	1	m15
X+0 - >NOP	XXX XX	00 0	XX X	10 0	00 0	00 1	1010	XXX X	XX	0	1	1	0	0	1	1	1	1	0	m16
Bport + 0 -> Q	XXX XX	00 0	XX X	01 1	00 0	00 0	XXX X	XXX X	XX	0	0	1	0	0	1	1	1	1	0	m17
X+0 - >NOP	XXX XX	00 0	XX X	10 0	00 0	00 1	1010	XXX X	XX	0	1	1	0	0	1	1	1	1	0	m18
MDR+ Q- >Bport	XXX XX	00 0	XX X	11 0	00 0	01 1	XXX X	XXX X	XX	0	0	1	0	0	1	1	1	0	1	m19
PC+1- >PC,M AR	XXX XX	00 0	XX X	10 1	00 0	01 1	1000	1000	01	0	1	1	1	0	1	1	1	1	0	m1a
NEXT( PC)	XXX XX	00 0	XX X	00 0	00 0	00 1	XXX X	XXX X	XX	0	0	1	0	0	0	0	0	0	0	m1b

SUB R1, \$K	BRA	BI N	CO N	I	I	I	APO RT	BPO RT	DDA TA	SH ~	SE LB	MW E~	MARC LK	MSTAT US	LD S~	PC E~	CARR YE~	MD E~	DDATA E~	ADDRE SS
	(4:0)	(2: 0)	(2: 0)	(2: 0)	(5: 3)	(8: 6)	(3:0)	(3:0)	(1:0)											
PC+1- >PC,M AR	XXX XX	00 0	XX X	10 1	00 0	01 1	1000	1000	01	0	1	1	1	0	1	1	1	1	0	m1c
MDR+0 ->X	XXX XX	00 0	XX X	11 1	00 0	01 1	XXX X	1010	XX	0	1	1	0	0	1	1	1	0	1	m1d
PC+1- >PC,M AR	XXX XX	00 0	XX X	10 1	00 0	01 1	1000	1000	01	0	1	1	1	0	1	1	1	1	0	m1e
MDR+0 ->MAR	XXX XX	00 0	XX X	11 1	00 0	00 1	XXX X	XXX X	XX	0	1	1	1	0	1	1	1	0	1	m1f
X+0 - ->NOP	XXX XX	00 0	XX X	10 0	00 0	00 1	XXX X	XXX X	XX	0	1	1	0	0	1	1	1	1	0	m20
Bport + 0-> Q	XXX XX	00 0	XX X	01 1	00 0	00 0	XXX X	XXX X	XX	0	0	1	0	0	1	1	1	1	0	m21
X+0 - ->NOP	XXX XX	00 0	XX X	10 0	00 0	00 1	1010	XXX X	XX	0	1	1	0	0	1	1	1	1	0	m22
Q-MDR ->Bport	XXX XX	00 0	XX X	11 0	00 1	01 1	XXX X	XXX X	XX	0	0	1	0	0	1	1	1	0	1	m23
PC+1- ->PC,M AR	XXX XX	00 0	XX X	10 1	00 0	01 1	1000	1000	01	0	1	1	1	0	1	1	1	1	0	m24
NEXT( PC)	XXX XX	00 0	XX X	00 0	00 0	00 1	XXX X	XXX X	XX	0	0	1	0	0	0	0	0	0	0	m25

ADD R1, R2	BRA	BI N	CO N	I	I	I	APO RT	BPO RT	DDA TA	SH ~	SE LB	MW E~	MARC LK	MSTAT US	LD S~	PC E~	CARR YE~	MD E~	DDATA E~	ADDRE SS
	(4:0)	(2: 0)	(2: 0)	(2: 0)	(5: 3)	(8: 6)	(3:0)	(3:0)	(1:0)											
PC+1- ->PC,M AR	XXX XX	00 0	XX X	10 1	00 0	01 1	1000	1000	01	0	1	1	1	0	1	1	1	1	0	m26
MDR+0 ->X	XXX XX	00 0	XX X	11 1	00 0	01 1	XXX X	1010	XX	0	1	1	0	0	1	1	1	0	1	m27
PC+1- ->PC,M AR	XXX XX	00 0	XX X	10 1	00 0	01 1	1000	1000	01	0	1	1	1	0	1	1	1	1	0	m28
MDR+0 ->NOP	XXX XX	00 0	XX X	11 1	00 0	00 1	XXX X	XXX X	XX	0	1	1	1	0	1	1	1	0	1	m29
Bport+ 0->Q	XXX XX	00 0	XX X	01 1	00 0	00 0	XXX X	XXX X	XX	0	0	1	0	0	1	1	1	1	0	m2a
Q+0 ->Y	XXX XX	00 0	XX X	01 0	00 0	01 1	XXX X	0000	XX	0	1	1	0	0	1	1	1	1	1	m2b
X+0 - ->NOP	XXX XX	00 0	XX X	10 0	00 0	00 1	1010	XXX X	XX	0	1	1	0	0	1	1	1	1	0	m2c
Bport + Y-> Bport	XXX XX	00 0	XX X	00 1	00 0	01 1	0000	XXX X	XX	0	0	1	0	0	1	1	1	1	0	m2d
PC+1- ->PC,M AR	XXX XX	00 0	XX X	10 1	00 0	01 1	1000	1000	01	0	1	1	1	0	1	1	1	1	0	m2e
NEXT( PC)	XXX XX	00 0	XX X	00 0	00 0	00 1	XXX X	XXX X	XX	0	0	1	0	0	0	0	0	0	0	m2f

SHL R	BRA	BI N	CO N	I	I	I	APO RT	BPO RT	DDA TA	SH ~	SE LB	MW E~	MARC LK	MSTAT US	LD S~	PC E~	CARR YE~	MD E~	DDATA E~	ADDRE SS
	(4:0)	(2:0)	(2:0)	(2:0)	(5:3)	(8:6)	(3:0)	(3:0)	(1:0)											
PC+1->PC, MAR	XXX XX	00 0	XX X	10 1	00 0	01 1	1000	1000	01	0	1	1	1	0	1	1	1	1	0	m30
MDR+0->NOP	XXX XX	00 0	XX X	11 1	00 0	00 1	XXX X	XXX X	XX	0	1	1	1	0	1	1	1	0	1	m31
SHLA enable	XXX XX	00 0	XX X	01 1	00 0	11 1	XXX X	XXX X	XX	0	0	1	0	0	1	1	1	1	1	m32
PC+1->PC, MAR	XXX XX	00 0	XX X	10 1	00 0	01 1	1000	1000	01	0	1	1	1	0	1	1	1	1	0	m33
NEXT(PC)	XXX XX	00 0	XX X	00 0	00 0	00 1	XXX X	XXX X	XX	0	0	1	0	0	0	0	0	0	0	m34

DEC	BRA	BI N	CO N	I	I	I	APO RT	BPO RT	DDA TA	SH ~	SE LB	MW E~	MARC LK	MSTAT US	LD S~	PC E~	CARR YE~	MD E~	DDATA E~	ADDRE SS
	(4:0)	(2:0)	(2:0)	(2:0)	(5:3)	(8:6)	(3:0)	(3:0)	(1:0)											
PC+1->PC, MAR	XXX XX	00 0	XX X	10 1	00 0	01 1	1000	1000	01	0	1	1	1	0	1	1	1	1	0	m35
MDR+0->X	XXX XX	00 0	XX X	11 1	00 0	01 1	XXX X	1010	XX	0	1	1	0	0	1	1	1	0	1	m36
X+0->NOP	XXX XX	00 0	XX X	10 0	00 0	00 1	1010	XXX X	XX	0	1	1	0	0	1	1	1	1	0	m37
Bport+0->Q	XXX XX	00 0	XX X	01 1	00 0	00 0	XXX X	XXX X	XX	0	0	1	0	0	1	1	1	1	0	m38
X+0->NOP	XXX XX	00 0	XX X	10 0	00 0	00 1	1010	XXX X	XX	0	1	1	0	0	1	1	1	1	0	m39
Q-1->Bport	XXX XX	00 0	XX X	11 0	00 1	01 1	XXX X	XXX X	XX	0	0	1	0	0	1	1	1	1	0	m3a
PC+1->PC, MAR	XXX XX	00 0	XX X	10 1	00 0	01 1	1000	1000	01	0	1	1	1	0	1	1	1	1	0	m3b
NEXT(PC)	XXX XX	00 0	XX X	00 0	00 0	00 1	XXX X	XXX X	XX	0	0	1	0	0	0	0	0	0	0	m3c

HALT	BRA	BI N	CO N	I	I	I	APO RT	BPO RT	DDA TA	SH ~	SE LB	MW E~	MARC LK	MSTAT US	LD S~	PC E~	CARR YE~	MD E~	DDATA E~	ADDRE SS
	(4:0)	(2:0)	(2:0)	(2:0)	(5:3)	(8:6)	(3:0)	(3:0)	(1:0)											
SW+0->PC, MAR	XXX XX	00 0	XX X	10 0	00 0	01 1	1000	1000	XX	0	0	1	0	0	1	1	1	1	1	m3d
NEXT(PC)	XXX XX	00 0	XX X	00 0	00 0	00 1	XXX X	XXX X	XX	0	0	1	0	0	0	0	0	0	0	m3e

MAPPER		
Κώδικας Εντολής	Opcode/Θέση	Περιεχόμενα
LOAD R, \$K	00	02
STORE R, \$K	01	0a
ADD R1, \$K	02	12
SUB R1, \$K	03	1c
ADD R1, R2	04	26
SHL R	05	30
DEC R	06	35
HALT	07	3d

MAIN MEMORY		
Κώδικας Εντολής	Θέση	Περιεχόμενο
LOADBR #K	00	00/opcode
	01	28/έντελο
	02	A0/Y
ADD R1, \$K	03	02/opcode

	04	0b/έντελο
	05	A1/X
SUB R1, \$K	06	03/opcode
	07	0b
	08	A2/Z
DEC	09	06
	0A	0b
SHL R1	0B	05
	0C	0b
SHL R1	0D	05
	0E	0b
LOAD	0F	00
	10	0C
	11	A0
ADD	12	02
	13	0C
	14	A1
DEC	15	06
	16	0C
SHL R2	17	05
	18	0C
ADD R1, R2	19	04
	1A	0b
	1B	0c
STORE	1C	01
	1D	0B
	1E	A3
HALT	1F	07
ΔΕΔΟΜΕΝΑ	A0	\X
	A1	\Y
	A2	\Z
	A3	00\W