CA Lab

Computer Architecture Lab

Lab 15: Homework 15

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Task Description

1)MIPS PROCESSOR

1. Branch condition evaluation unit

Solution

1. created a thing

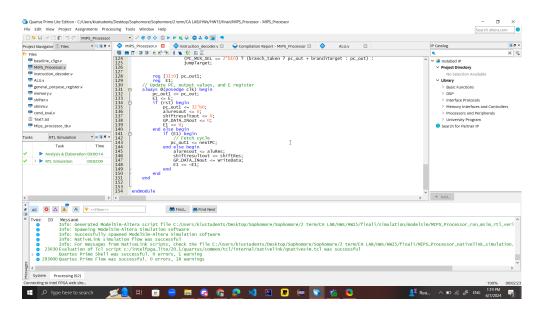


Figure 1:

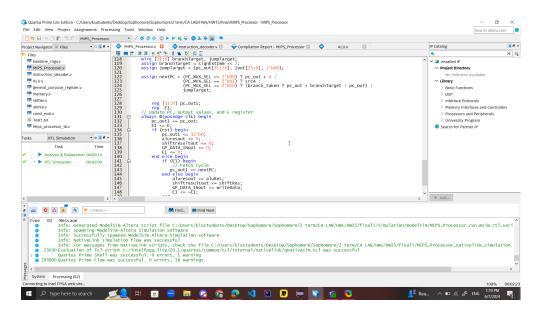


Figure 2:

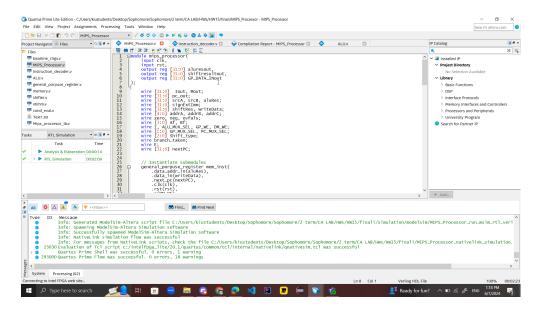


Figure 3:

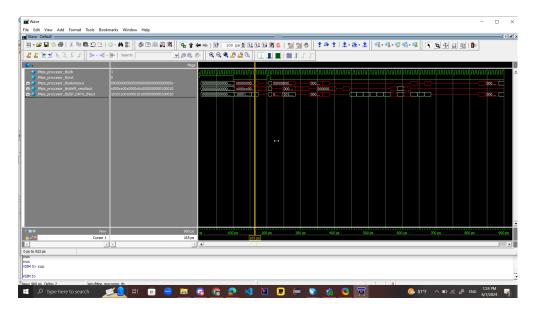


Figure 4:

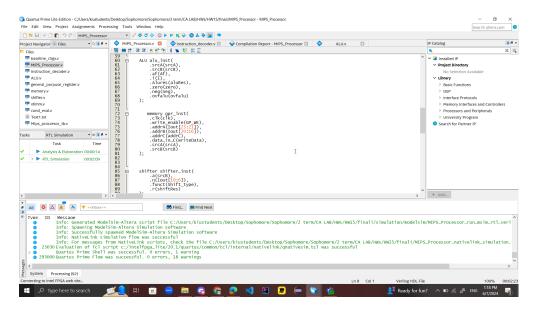


Figure 5:

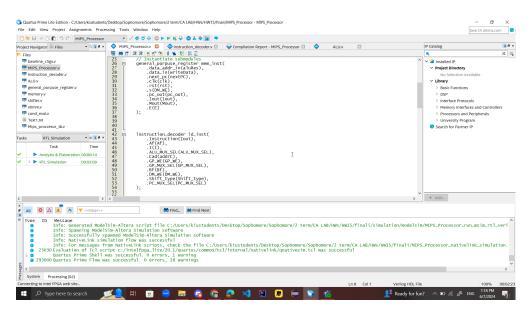


Figure 6:

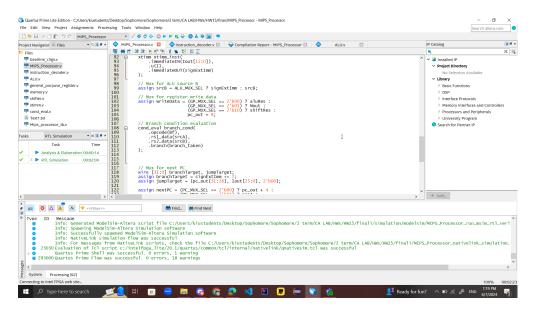


Figure 7:

MODIFIED INSTRUCTION DECODER

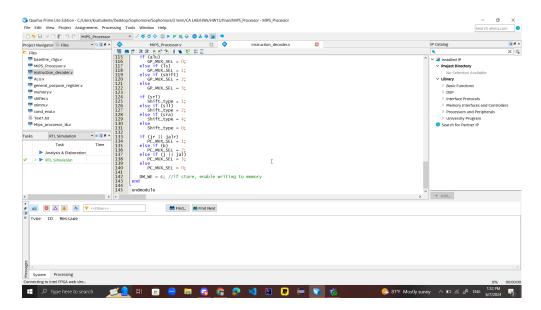


Figure 8:

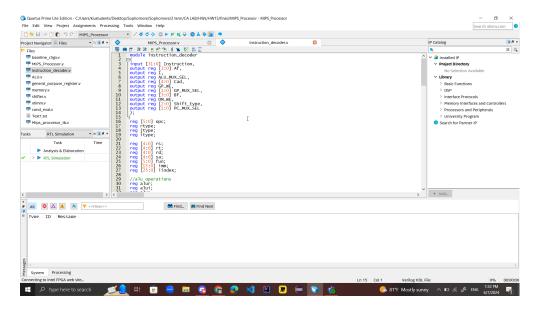


Figure 9:

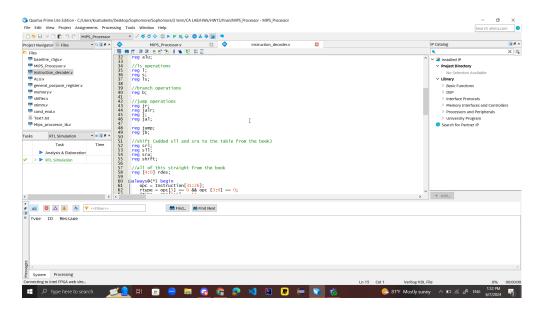


Figure 10:

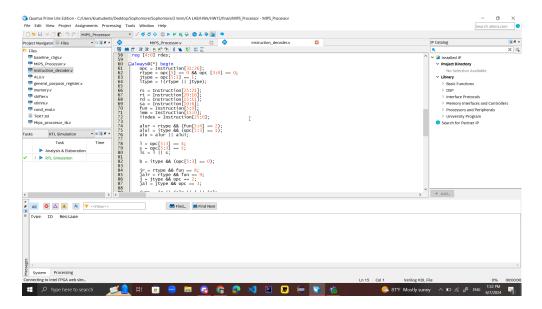


Figure 11:

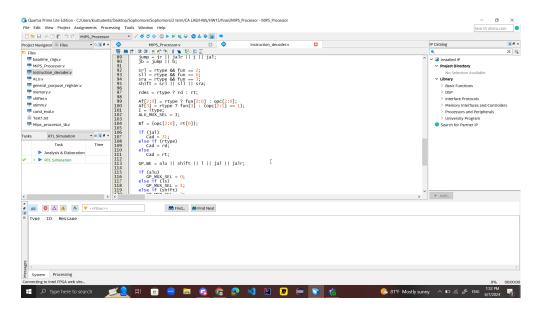


Figure 12:

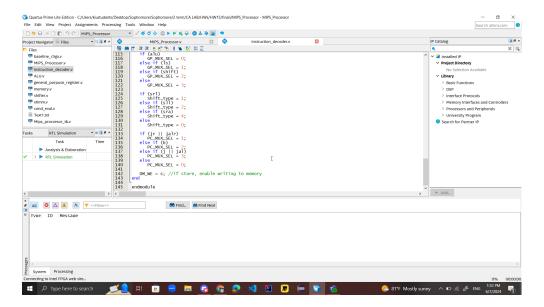


Figure 13:

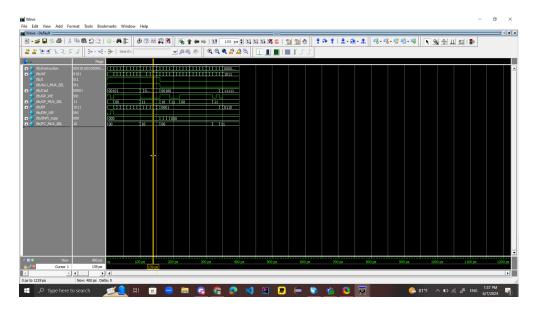


Figure 14:

Conclusion (optional)

combined all the modules, Mout works as inteneded, alures have problem, in the transition phase from one module(memory) to other (alu) alures are not computed correctly, shiftresultout works when a shift operation is made in other case it doesnt do anything,

Reference

created procesor