### TFE4152 - Lecture 3

### PN Junctions

Source

### Goal for today

- Refresh what you should aready know about PN
- Carrier concentrations
- Built in voltage
- Reverse bias
- Forward bias
- Small signal models

### Want to go deeper

Feynman lectures on physics

MIT 8.04 Quantum Mechanics I

MIT 8.05 Quantum Mechanics II

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$$q = 1.6 imes 10^{-19} [C] \ k = 1.38 imes 10^{-23} [J/K]$$

$$\mu_0 = rac{2lpha}{q^2}rac{h}{c} = 1.26 imes 10^{-6}[H/m]$$

$$\epsilon_0 = rac{1}{\mu_0 c^2} = 8.854 imes 10^{-12} [F/m]$$

where q is unit charge, k is Boltzmann's constant, h is Plancks constant, c is speed of light and alpha is the fine structure constant

```
from scipy import constants
import numpy as np
h = constants.physical_constants["Planck constant"][0]
k = constants.Boltzmann
q = constants.physical_constants["elementary charge"][0]
c = constants.c
alpha = constants.physical_constants["fine-structure constant"][0]
mu = 2* alpha*h/(c*np.power(q,2))
print("Permiability of free space = %g" % mu)
epsilon = 1/(mu* c**2)
print("Permitivity of free space = %g" % epsilon)
wulffalectures python 13 constants.py
Permiability of free space = 1.25664e-06
Permitivity of free space = 8.85419e-12
```

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### Computer models

http://bsim.berkeley.edu/models/bsim4/

http://bsim.berkeley.edu/BSIM4/BSIM480.zip

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$$n_i pprox 1 imes 10^{16} [1/m^3] = 1 imes 10^{10} [1/cm^3]$$
 at 300 K

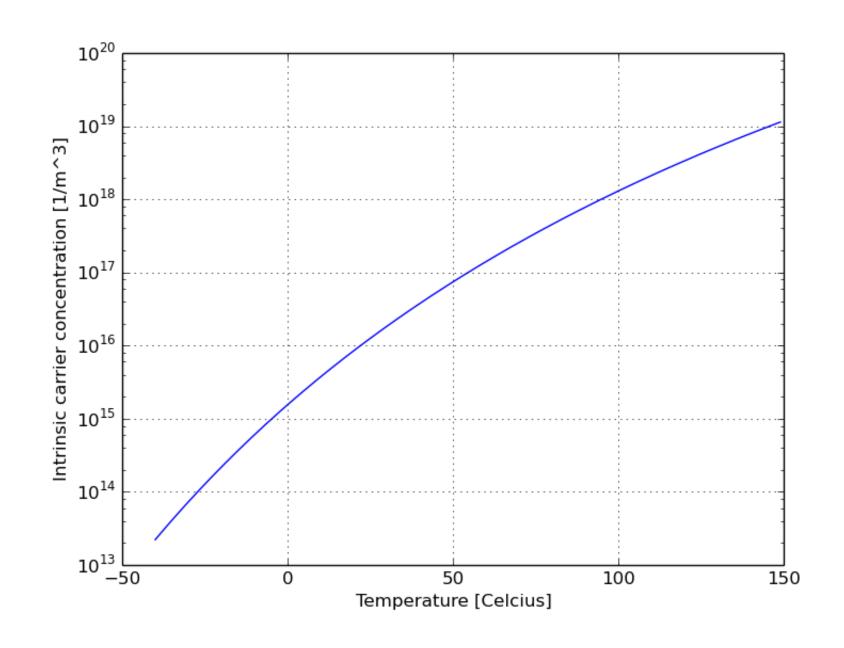
$$n_i^2=n_0p_0$$

$$n_i = \sqrt{N_C N_V} e^{rac{-E_g}{2kT}}$$

$$N_C=2igg(rac{2\pi m_n^*kT}{h^2}igg)^{3/2}$$

$$N_V=2igg(rac{2\pi m_p^*kT}{h^2}igg)^{3/2}$$

https://github.com/wulffern/dic2021/blob/main/2021-07-08\_diodes/intrinsic.py



### Solid state physics:

$$n_i = \sqrt{N_C N_V} e^{rac{-E_g}{2kT}}$$

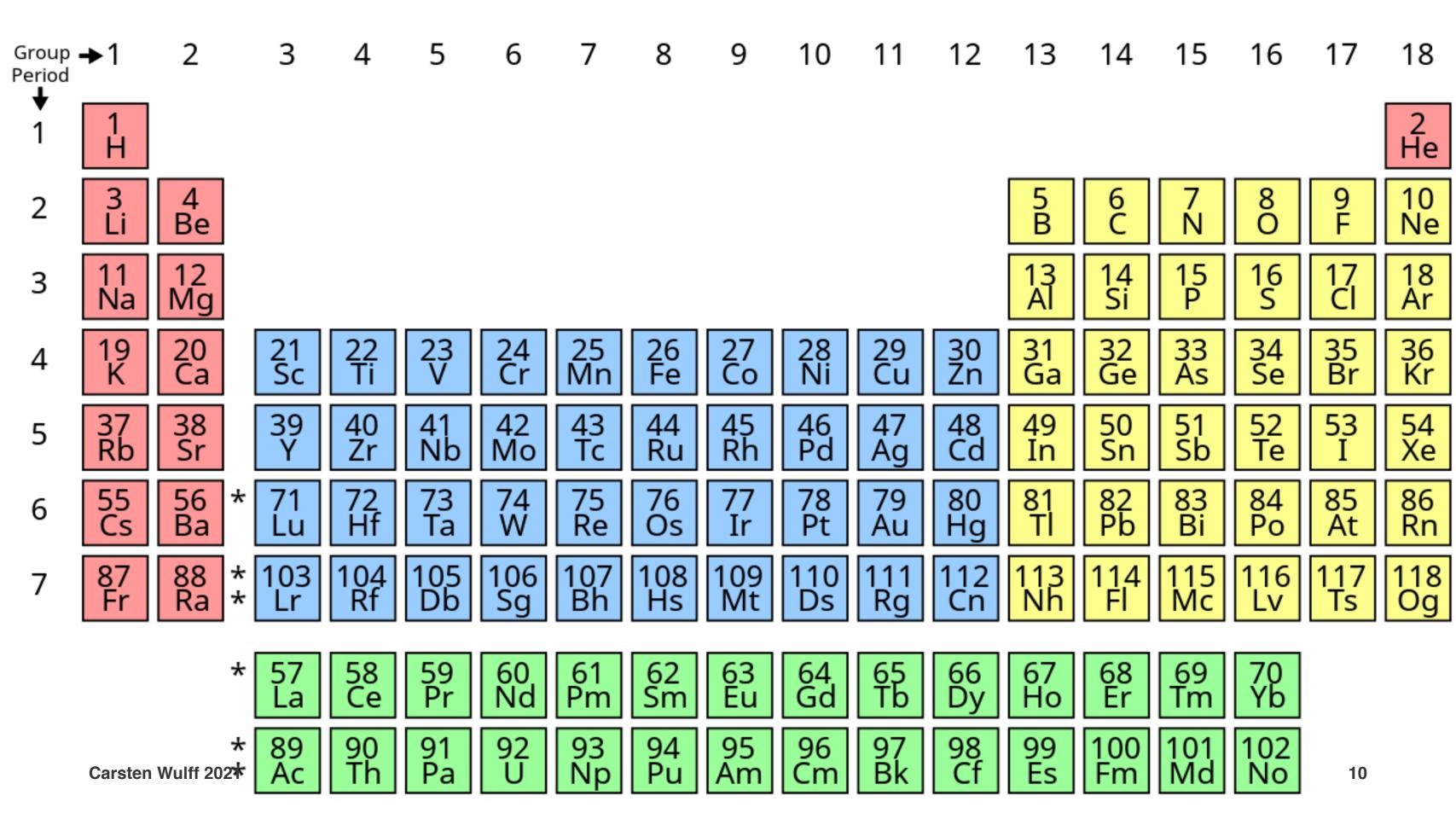
BSIM 4.8, Intrinsic carrier concentration (page 122)

$$n_i = 1.45e10rac{TNOM}{300.15}\sqrt{rac{T}{300.15}}exp\left[21.5565981 - rac{qE_g(TNOM)}{2k_bT}
ight]$$

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### How do charge carriers in intrinsic silicon move?

Drift and Diffusion



### n-type

$$n_npprox N_D$$

$$p_n = rac{n_i^2}{N_D}$$

### p-type

$$p_ppprox N_A$$

$$p_n = rac{n_i^2}{N_A}$$

## PN junctions

### Symbol

How carriers move

Carrier distribution

Depletion zone

Current characteristics

Avalanche

### Built in voltage

Comes from Fermi-Dirac statistics

$$\Phi_0 = V_T ln \left(rac{N_A N_D}{n_i^2}
ight)$$

$$V_T=rac{kT}{q}$$

$$rac{n_n}{n_p}=e^{rac{q\Phi_0}{kT}}$$

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### Depletion widths

$$x_n = \left(rac{2K_s\epsilon_0(\Phi_0+V_R)}{q}rac{N_A}{N_D(N_A+N_D)}
ight)^{l_1}$$

where,  $K_s=11.8$  is the relative perimittivity of silicon, and  $l_1\approx 0.5$  to almost unity, depending on the doping profile

For  $x_p$  replace  $N_A=N_D, N_D=N_A$ 

#### Junction capacitance of reversed bias junctions

CJM, single sided diode:

$$C_j = C_{j0} \left[1 + rac{V_R}{\Phi_0}
ight]^{-1/2}$$

BSIM 4.8:

$$C_{jbs} = CJS(T)igg(1-rac{V_{BS}}{PBS(T)}igg)^{-MJS}$$

$$CJS(T) = CJS(TNOM) + TCJ(T - TNOM)$$

$$PBS(T) = PBS(TNOM) - TPB(T - TNOM)$$

### Small-Signal model under forward bias

Large signal current 
$$I_D = I_s(e^{V_D/V_T}-1)$$

Small signal conductance 
$$g_d = \frac{dI_D}{dV_D} = \frac{I_D}{V_T} = \frac{1}{r_d}$$

### Capacitance of forward bias

 $C_j$  is similar (but not same) as reverse bias (page 106 in BSIM 4.8)

Additional term due to the increased minority carrier on the "other side"

$$C_d = au_T rac{I_D}{V_T} = rac{ au_T}{r_d}$$

$$C_T = C_j + C_d$$

where  $\tau_T$  is the transit time of the diode

# Thanks!