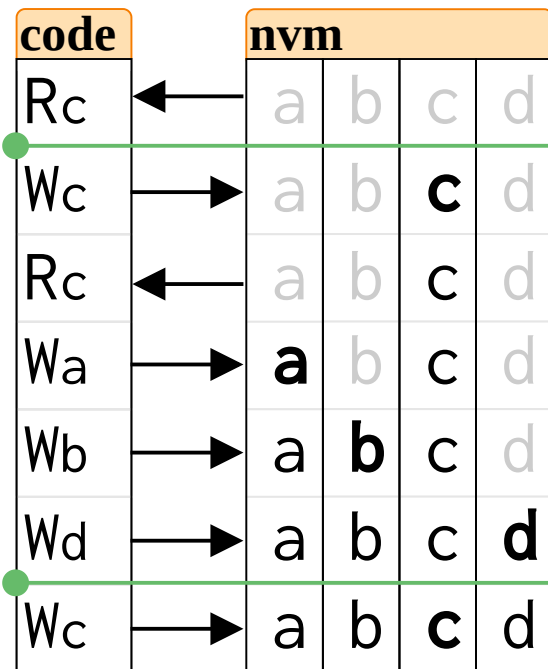
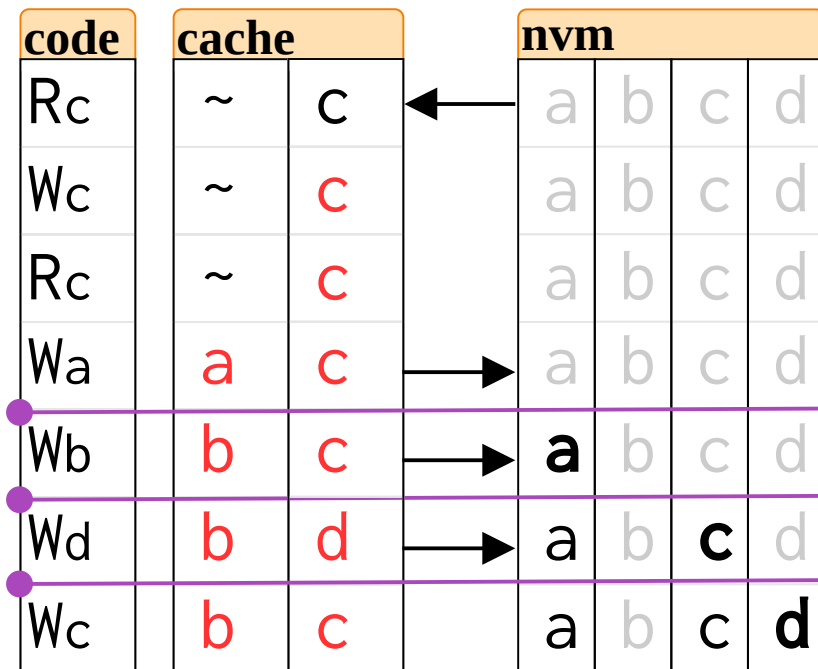


No data cache



Checkpoints: 2
NVM Reads: 2
NVM Writes: 5

With a data cache



Checkpoints: 0
NVM Reads: 2 Cannot be intermittent
NVM Writes: 5

Legend

- Checkpoint
- Cache eviction
- R_xC Possible WAR bit
- Modified Instruction
- ↔ NVM reads/writes
- a Clean NVM mem block
- a** NVM block just written to
- a Dirty NVM block