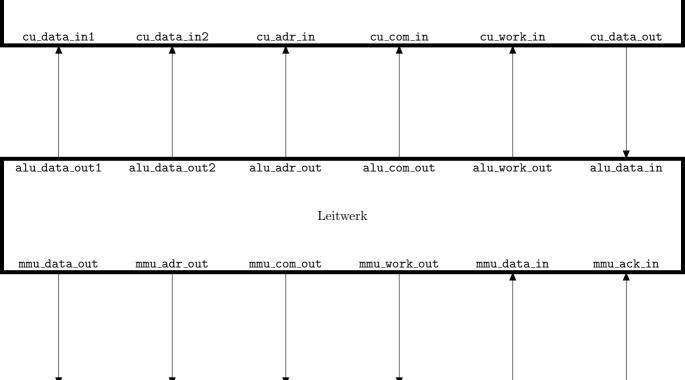
## ALU



## MMU

work\_in

data\_out

ack\_out

cmd\_in

data\_in

addr\_in