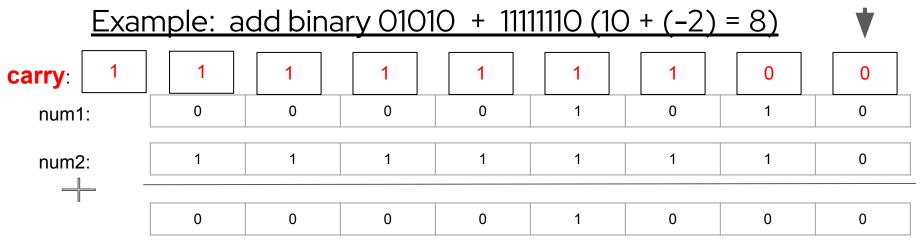
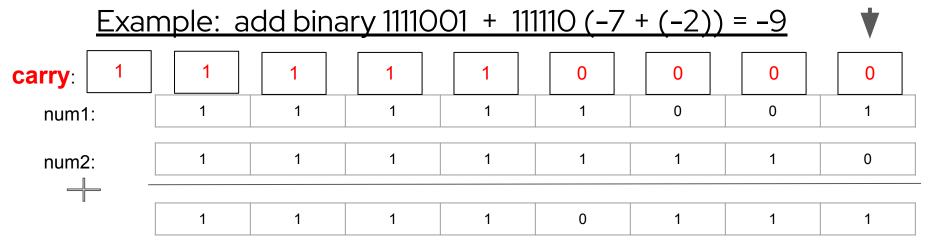
- When adding two signed numbers of different signs, ignore the carry-out bit.
- When adding two signed numbers of the same sign, the sign bit of the sum should match the numbers.
- If the two numbers are negative and the MSB is 0, then there is an overflow with a carry-out bit of 1. The carry-out bit must become the sign bit. (if adding a bit is allowed)
- If the two numbers are positive and the MSB is 1, then there is an overflow with a carry-out bit of 0.
   The carry-out bit must become the sign bit. (if adding a bit is allowed)

  Start at LSB



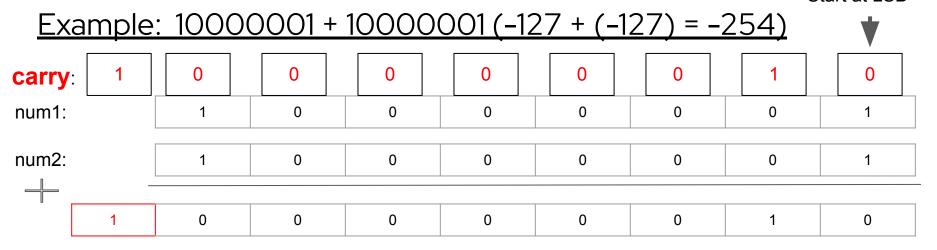
- When adding two signed numbers of different signs, ignore the carry-out bit.
- When adding two signed numbers of the same sign, the sign bit of the sum should match the numbers.
- If the two numbers are negative and the MSB is 0, then there is an overflow with a carry-out bit of 1. The carry-out bit must become the sign bit. (if adding a bit is allowed)
- If the two numbers are positive and the MSB is 1, then there is an overflow with a carry-out bit of 0. The carry-out bit must become the sign bit. (if adding a bit is allowed)

  Start at LSB



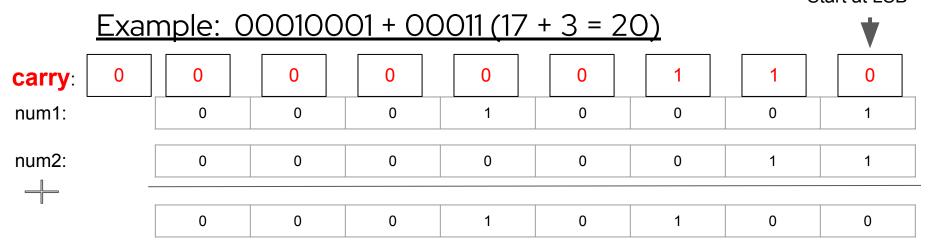
- When adding two signed numbers of different signs, ignore the carry-out bit.
- When adding two signed numbers of the same sign, the sign bit of the sum should match the numbers.
- If the two numbers are negative and the MSB is 0, then there is an overflow with a carry-out bit of 1. The carry-out bit must become the sign bit. (if adding a bit is allowed)
- If the two numbers are positive and the MSB is 1, then there is an overflow with a carry-out bit of 0.
   The carry-out bit must become the sign bit. (if adding a bit is allowed)

  Start at LSB



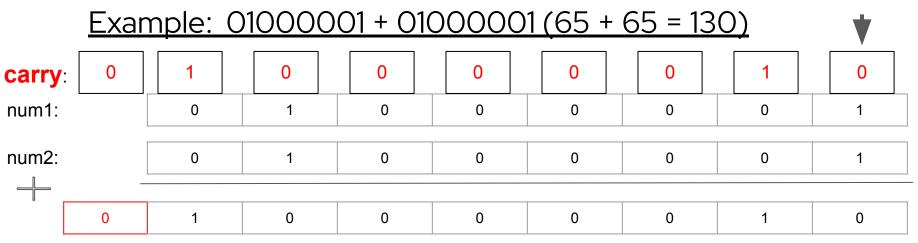
- When adding two signed numbers of different signs, ignore the carry-out bit.
- When adding two signed numbers of the same sign, the sign bit of the sum should match the numbers.
- If the two numbers are negative and the MSB is 0, then there is an overflow with a carry-out bit of 1. The carry-out bit must become the sign bit. (if adding a bit is allowed)
- If the two numbers are positive and the MSB is 1, then there is an overflow with a carry-out bit of 0.
   The carry-out bit must become the sign bit. (if adding a bit is allowed)

  Start at LSB



- When adding two signed numbers of different signs, ignore the carry-out bit.
- When adding two signed numbers of the same sign, the sign bit of the sum should match the numbers.
- If the two numbers are negative and the MSB is 0, then there is an overflow with a carry-out bit of 1. The carry-out bit must become the sign bit. (if adding a bit is allowed)
- If the two numbers are positive and the MSB is 1, then there is an overflow with a carry-out bit of 0. The carry-out bit must become the sign bit. (if adding a bit is allowed)

  Start at LSB



## Weighted-Positional number systems (SIGNED):

Example: Consider 1011, signed binary number (base-2 number)

- starting with lowest weight of 2<sup>0</sup>, moving from right-to-left
- the weight of each bit, increases by a factor of 2
- The sign of the weight of the most significant bit is always negative

The value of a number is weighted sum of its digits:

$$(1011)_2 = (1 \times (-2^3)) + (0 \times 2^2) + (1 \times 2^1) + (1 \times 2^0)$$
  
 $(1011)_2 = -8 + 0 + 2 + 1 = -5$ 

$$111010_{2} = -(1 \times 2^{5}) + (1 \times 2^{4}) + (1 \times 2^{3}) + (0 \times 2^{2}) + (1 \times 2^{1}) + (0 \times 2^{0})$$

$$= (1 \times 2^{4}) + (1 \times 2^{3}) + (0 \times 2^{2}) + (1 \times 2^{1}) + (0 \times 2^{0}) - (1 \times 2^{5})$$

$$= 16 + 8 + 0 + 2 + 0 - 32$$

$$= -6$$

$$001010_{2} = -(0 \times 2^{5}) + (0 \times 2^{4}) + (1 \times 2^{3}) + (0 \times 2^{2}) + (1 \times 2^{1}) + (0 \times 2^{0})$$

= 0 + 8 + 0 + 2 + 0 - 0

= 10

 $= (0 \times 2^4) + (1 \times 2^3) + (0 \times 2^2) + (1 \times 2^1) + (0 \times 2^0) - (0 \times 2^5)$ 

## convert from decimal to signed binary $(-25)_{10} \rightarrow (?)_{2}$

# Repeated Division

Divide the given decimal number by base=2.

Write the remainder after each division until a quotient of zero is obtained.

Division	Quotient	Remainder
25/2	12	1
12/2	6	0
6/2	3	0
3/2	1	1
1/2	0	1
-	-	0

LSB

**MSB** 

We read the answer from the remainder column, bottom-up, adding 0 as the sign bit. If the decimal number is negative. Find the two's complement to get the final answer.

Signed: weighted sum, repeated division, binary addition/subtraction, two's complement

- Use Signed Weighted Sum algorithm to convert from: base<sub>2</sub> → base<sub>10</sub>
- Use Signed Repeated Division algorithm to convert from: base<sub>10</sub> to base<sub>2</sub>
- Properly convert to Two's Complement
- Be able to show bit-by-bit signed addition/subtraction with carry values:
   00001111 + 00000010
   11010101 + 01101011
   10001111 + 10001111