







+3V3 _{R502} D52 ANT SoC Module 0R J501 UART baudrate configuration, default 19200 the table can be found in: ANT_JTAG R504 10K(1%) <u>SWDIO</u>B1 E1 BR3 SWDCLK C1 SWOCLK PØ08 D1 GRXDO Interfacing with ANT General Purpose Chipsets and Modules GND RST E6 P02 RESET GND C507 JP502 BR2 2 JP503 BR3 100nF/50V(10%) NFC1/P009 C2 NFC2/P010 C3 JP501 2 BR1 P011 P012 P013 $\stackrel{\downarrow}{\checkmark}$ RC = 1ms GND P013 P014 B3 P015 C4 P016 D5 P018 B4 P020 TXD0< AIN0/P002 AIN1/P003 AN2/P004 GND GND GND AIN3/P005 SUSPEND pin is ignored, if SLEEP is pulled down O SLEEP AIN4 P028 AIN5 2029 AIN6/P030 R503 If the SLEEP pin is not required, op it need to be tight low TP503 AIN7/P03 -DRTS GPIO U503 ↓ D52MxxM8 GND GND GND IMU MPU-6050 +3V3 FB501 → YI160808Z101-3R0T C501 _ C502 10nF/50V(10%) 100nF/50V(10%) GND GND MPU I2C addr.: —DIMU_INT 0b1101000 23 SCL SCL Register INT_PIN_CFG bit INT_OPEN must be 1, than pin is open-drain 9 AD0 AUX DA AUX_CE **FSYNC** CPOUT 1 ELKIN REGOUT C505 2.2nF/50V(10%) 100nF/50V(10%) GND U501 MPU-6050 Designed by Martin Tavoda ALPS Electric Czech, s.r.o. GND GND GND Sheet: /IMU + ANT/ File: IMU&ANT.kicad_sch Title: Safety Ride Radar Sensor Size: A4 Date: 2023-11-18 Rev: Rev.: WIP KiCad E.D.A. kicad 7.0.9 ld: 5/8





