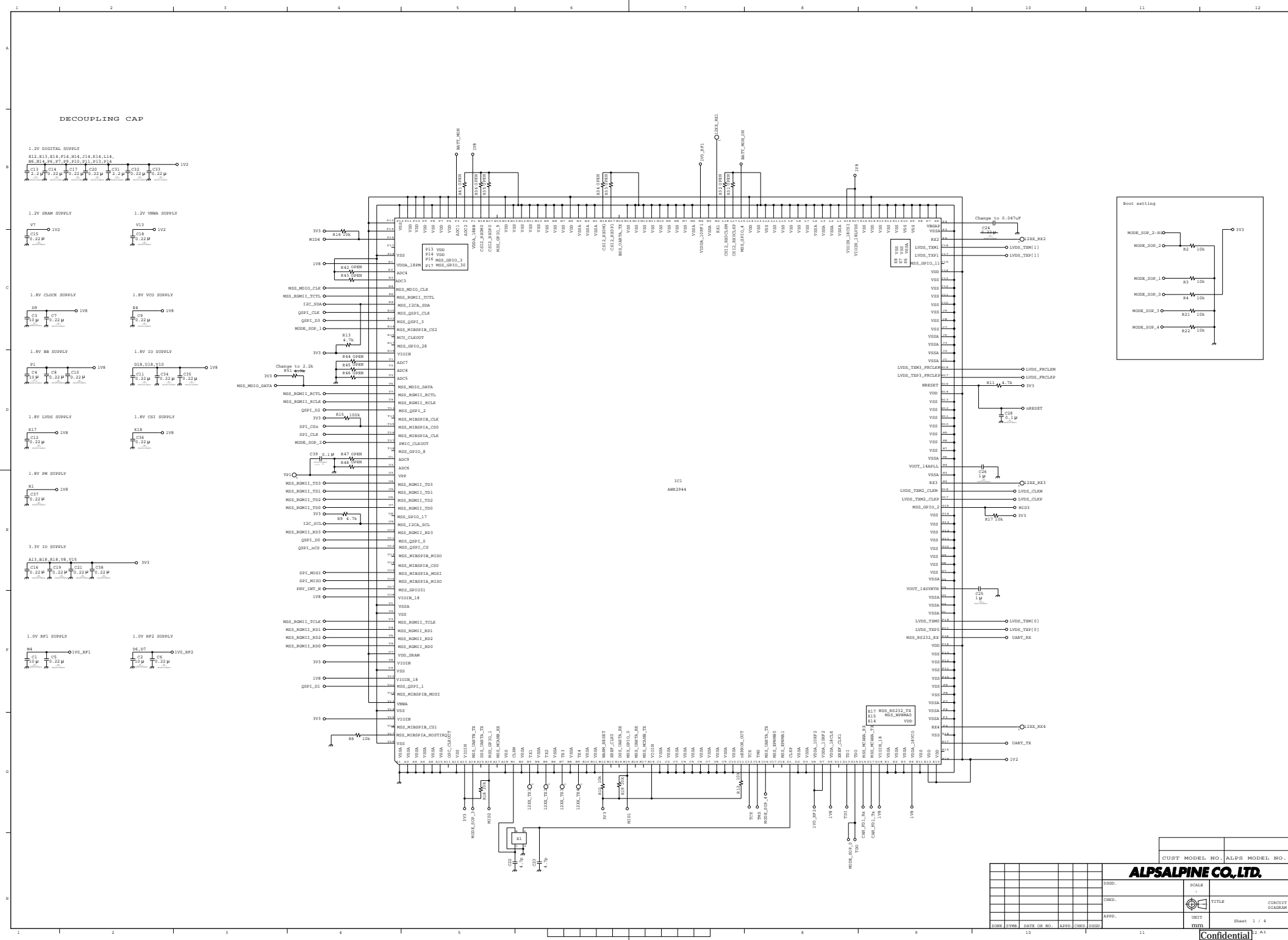


The schematic illustrates the electrical connections for a custom device. It features several power supply sections on the left, each with its own set of decoupling capacitors and voltage regulators. These supplies are connected to a central MCU component, which is surrounded by numerous peripheral components including resistors, capacitors, and connectors. A specific section labeled "Boot setting" provides details on how to configure the device's boot mode using jumpers or switches.

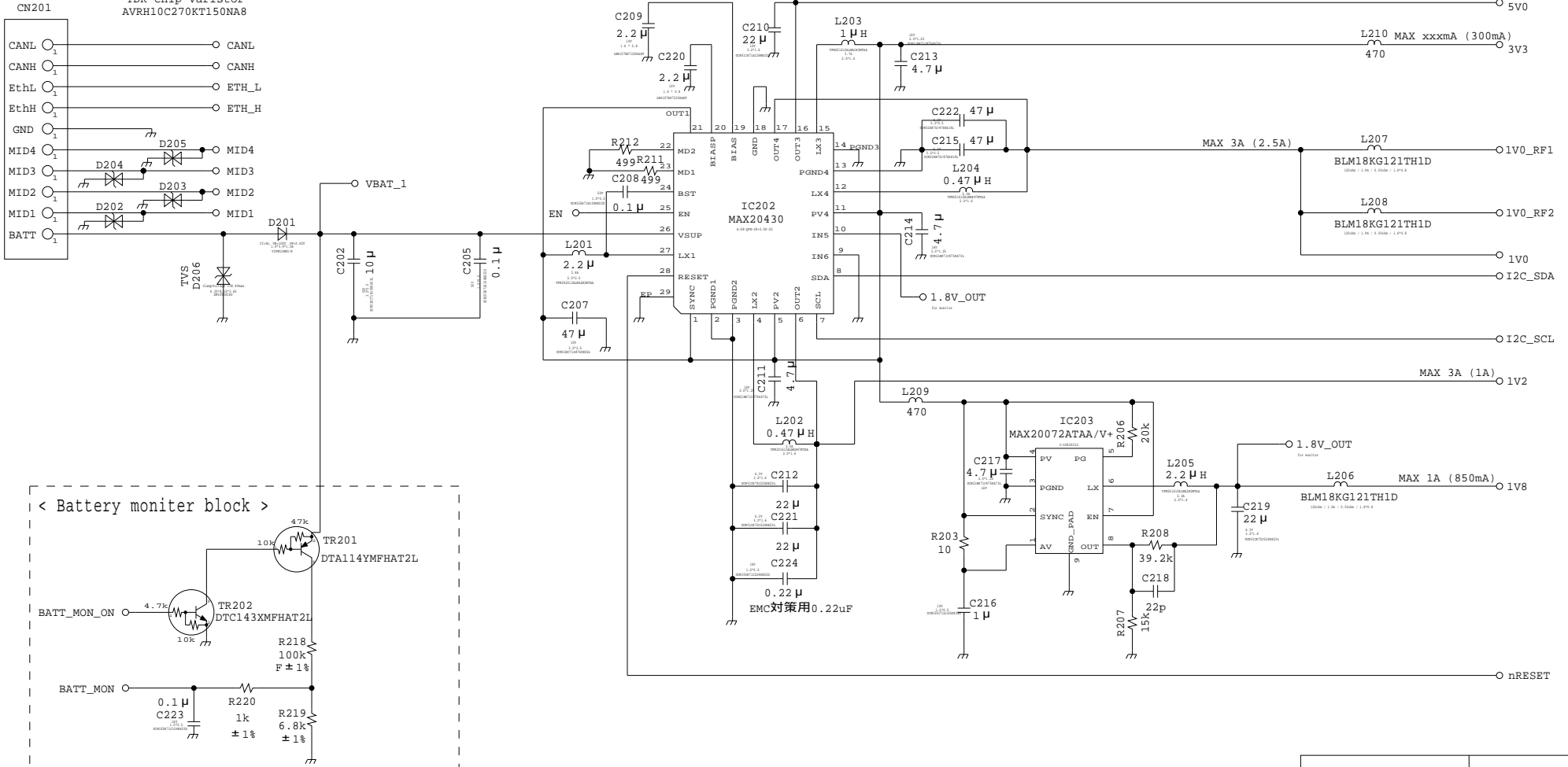
DECOUPLING CAP

1.2V DIGITAL SUPPLY
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< POWER >

D202-205
TDK chip Varistor
AVRH10C270KT150NA8

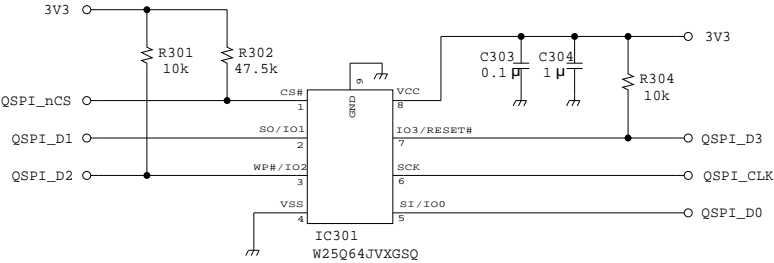


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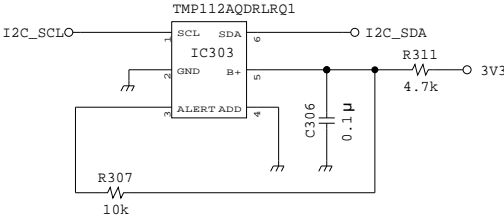
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ZONE, SYMB.	DATE OR NO.	APPD, CHKD, DSGD.	UNIT		

Confidential

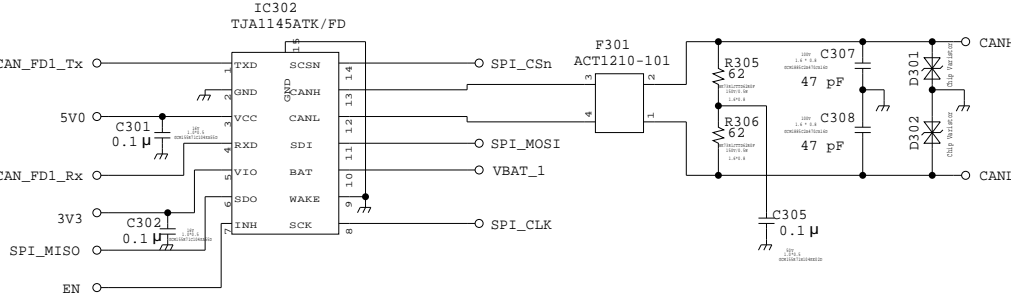
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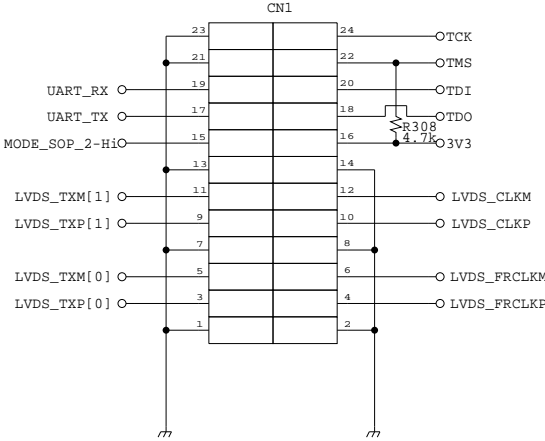
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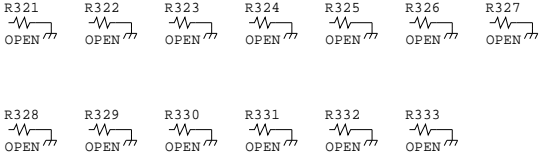
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< JTAG LVDS UART Debug connector >



< Terminals >



CUST MODEL NO.	ALPS MODEL NO.

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						CHKD.		TITLE USRR2 for Debug CIRCUIT Other Block DIAGRAM
						APPD.	UNIT	Sheet 3 / 4
ZONE.	SYMB.	DATE OR NO.	APPD.	CHKD.	DSGD.		mm	

