

*In The Name of God*

COMPUTER ENGINEERING DEPARTMENT OF AMIRKABIR UNIVERSITY OF TECHNOLOGY

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All of the codes are in the proteus and simulated in the protues. Note that all of the sections of the first question were merged into a single file. And all of the requirments are implemented inside a single file.

## 1 QUESTION 1

The error according to the datasheet is equal to 0.2 percent. In order to decrease the error to zero percent we should set the clock frequency to 1.8432 MHz.