

XInC2 Dev 2

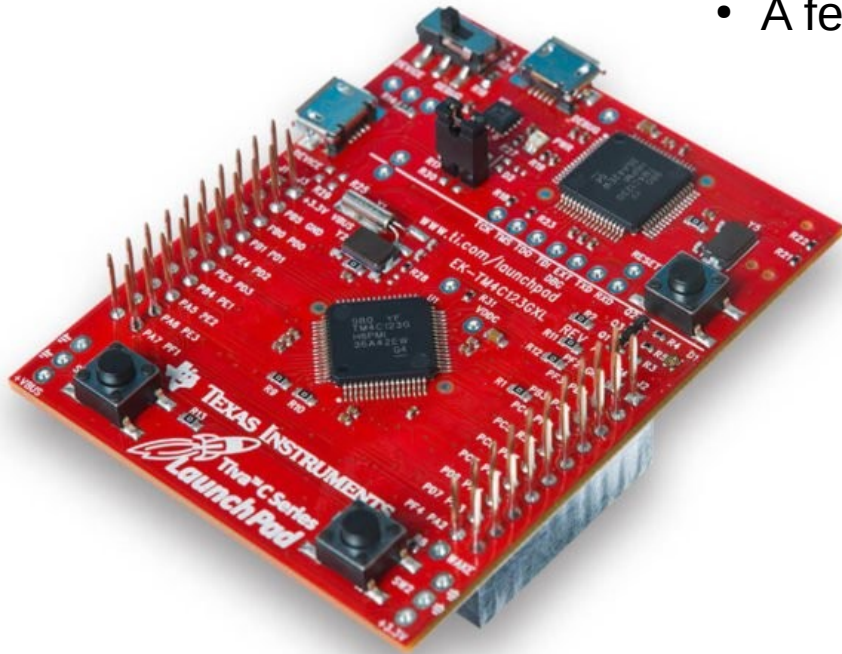
Perf Spec

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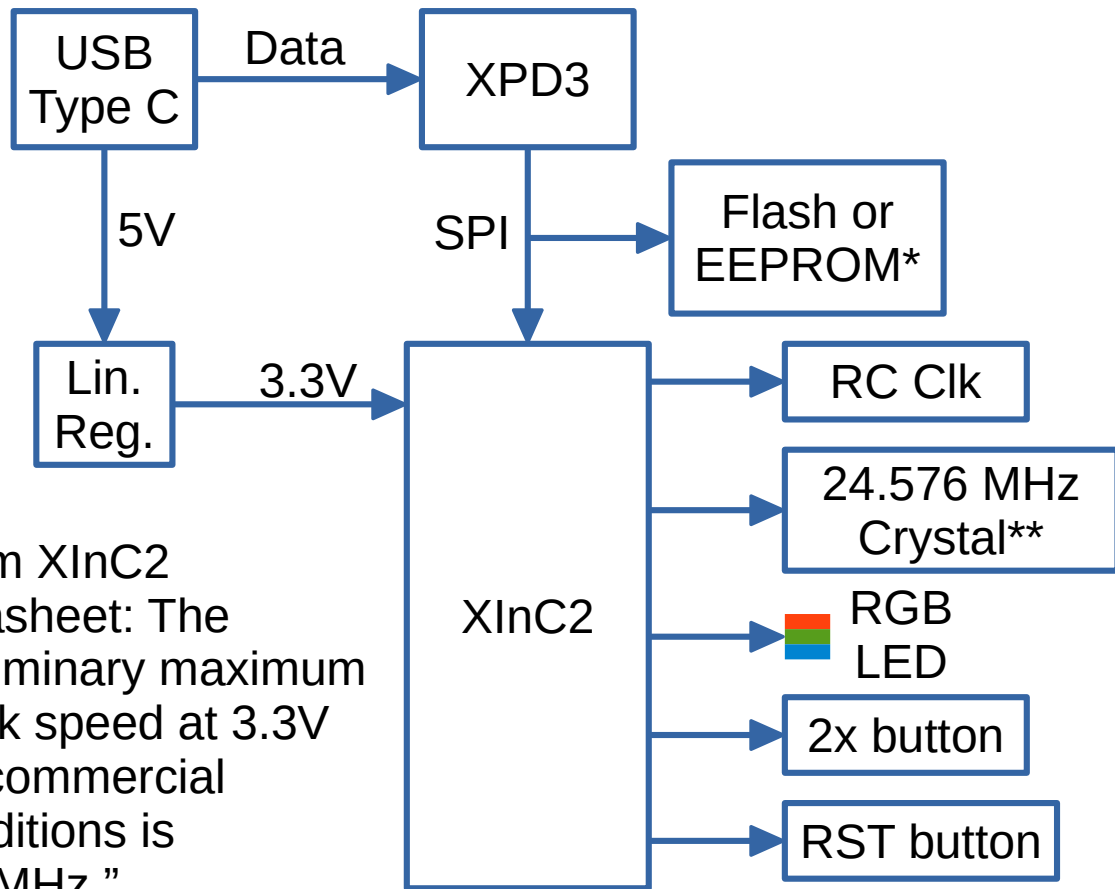
- The target audiences are university capstone groups and hobbyists.
- Design choices will heavily weight compatibility with Arduino and Raspberry Pi ecosystems.
- If possible, PCB design will be two layer, single sided.
- PCB will be able to populate either EEPROM or Flash memory ICs through the use of common footprints and pin outs. Compatible part numbers for each type will be identified.

General board format will follow the below examples:

- Programmer at the top
- uC bottom center
- GPIO around the edge
- A few buttons and LEDs

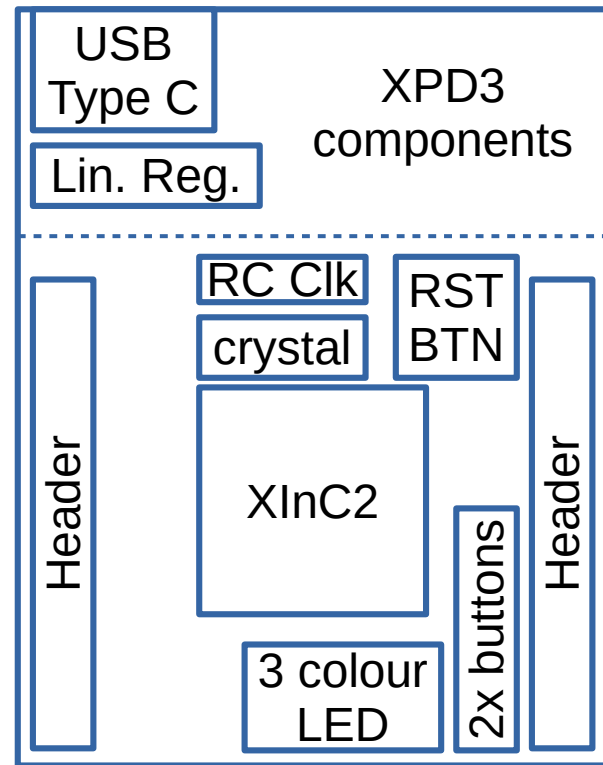


No crystal pulling. Let
the user use TMRB for
something else.



From XInC2
datasheet: The
preliminary maximum
clock speed at 3.3V
for commercial
conditions is
100MHz.”

Rough PCB Layout



- * Either population option possible
- ** Use a common crystal footprint to enable using other frequencies