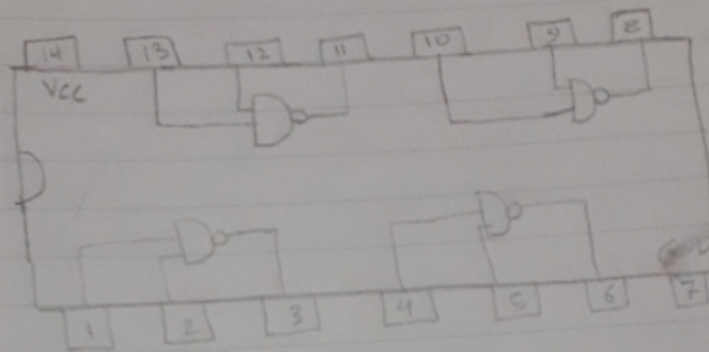


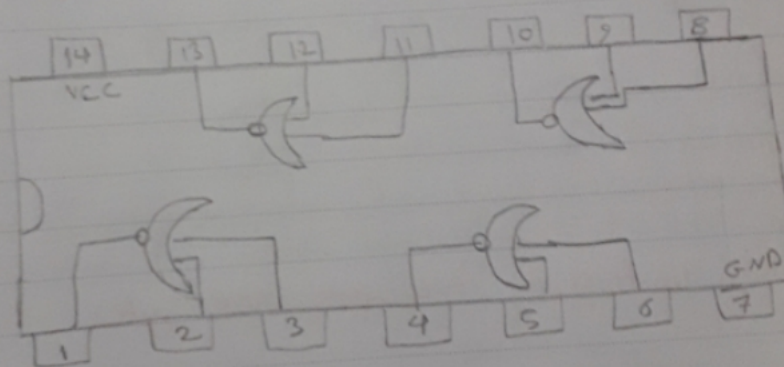
SUBJECT: .....

DATE: / /

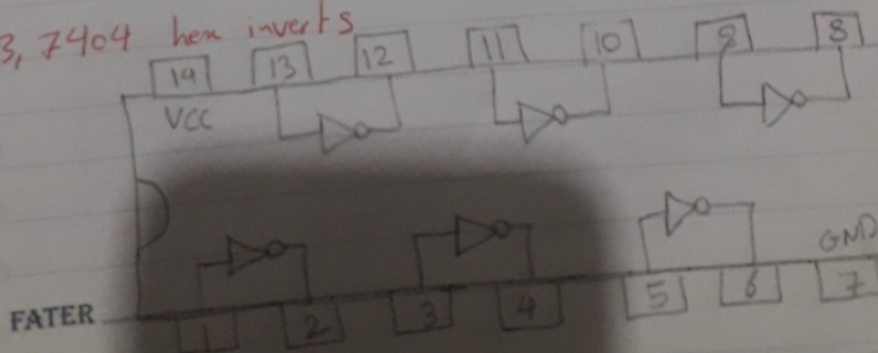
7400 quadrate two-input NAND gates



2, 7402 quadrate two-input NOR gates



3, 7404 hex inverters



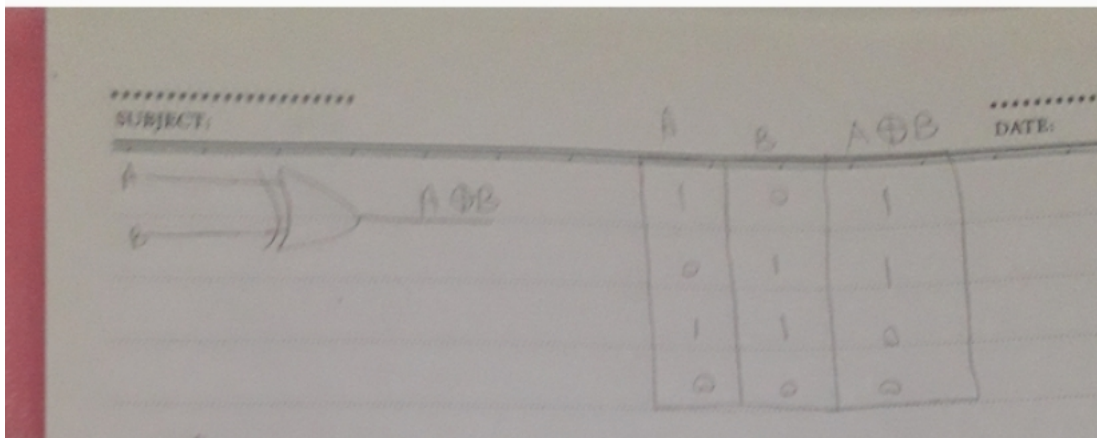


Image saved in /DCIM/Camera.

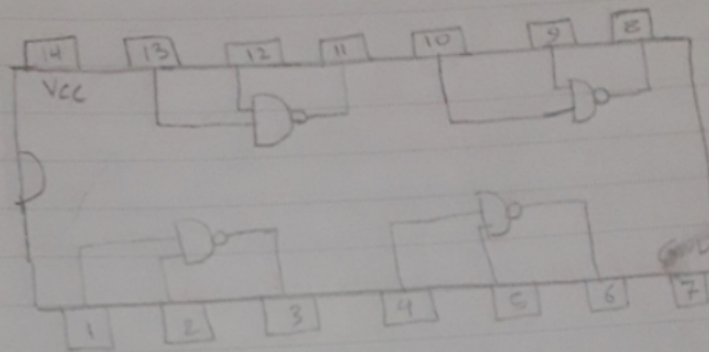
4/3/21



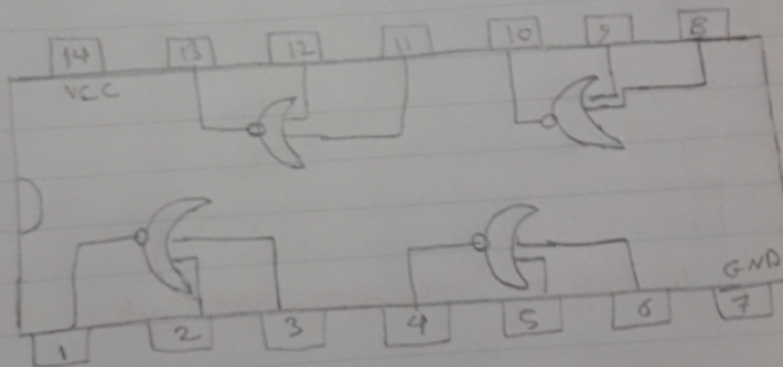
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DATE: / /

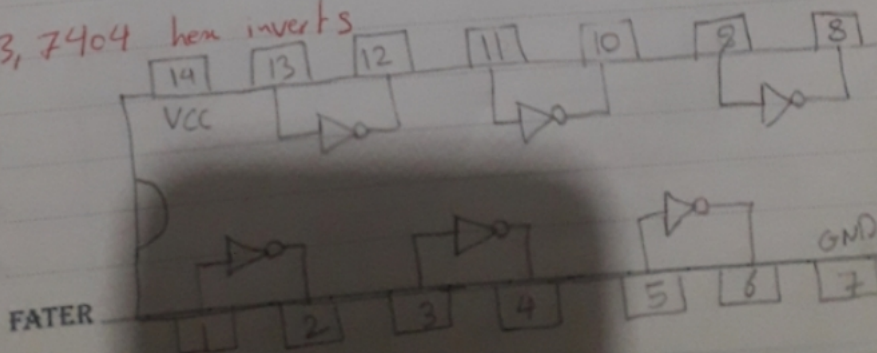
7400 quadrate two-input NAND gates



2, 7402 quadrate two-input NOR gates



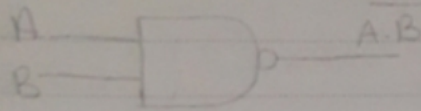
3, 7404 hex inverters





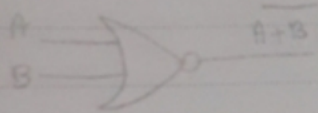
SUBJECT: \_\_\_\_\_

DATE: / /

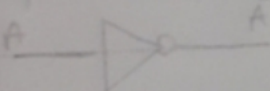


A	B	$\overline{A \cdot B}$
1	0	1
0	1	1
0	0	1
1	1	0

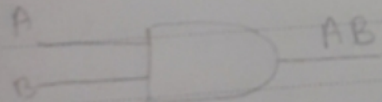
This is NOT-AND gate  
is inverted AND gate.  
 $y = (A \cdot B)'$



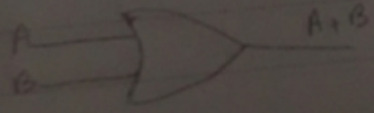
A	B	$\overline{A + B}$
1	0	0
0	1	0
1	1	0
0	0	1



A	$\bar{A}$
1	0
0	1



A	B	$AB$
1	0	0
0	1	0
1	1	1
0	0	0



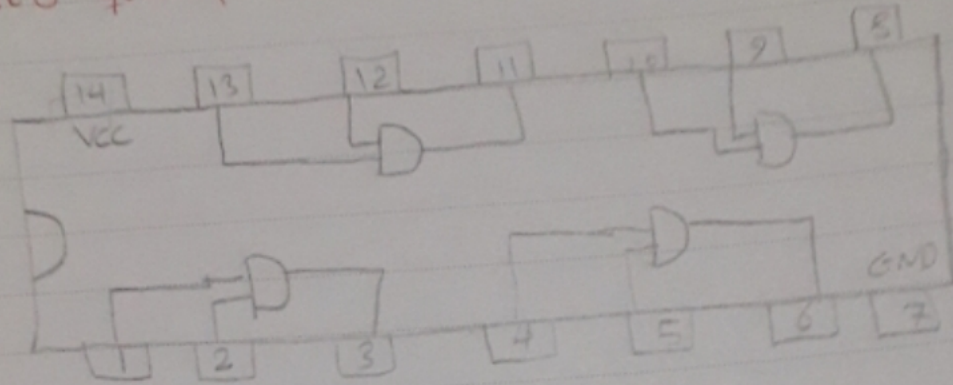
A	B	$A + B$
1	0	1
0	1	1
1	1	1
0	0	0

FATER

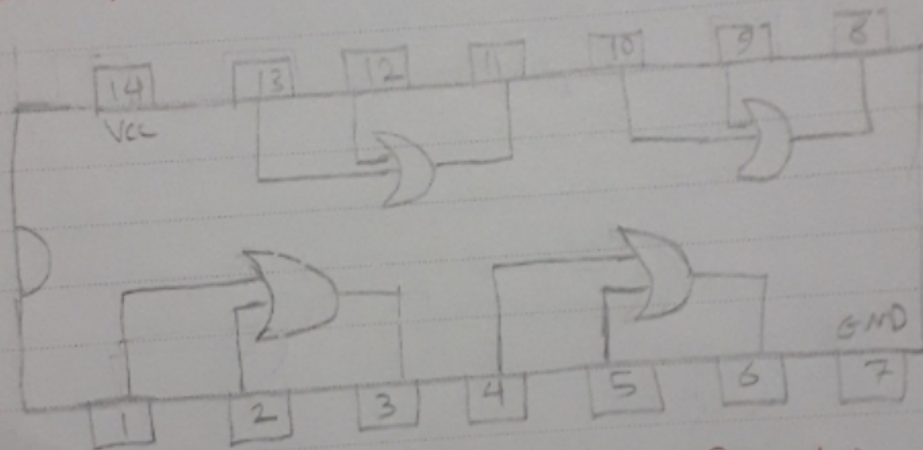
SUBJECT: .....

DATE: / /

7408 quadruple two-input AND gates



7432 quadruple two-input OR gates



7486 quadruple two-input XOR gates

