

CSE260

1. [Lecture 3 \[Boolean Algebra and Logic gates\] Spring 2023](#)

Lecture Video: [Click](#)

Assignment: [Click](#)

Solve:  Assignment 2_21201087_Section11.pdf


KMap: https://youtube.com/playlist?list=PLTIXQu_162Qjgqwuxm7Kd3_w-tmP2_JRY

2. [Lecture 7 \[Adder-Subtractor\] Spring 2023](#)


Lecture Video: [Combinational Circuit \(Half adder and Full adder, Subtractor\)](#)

3. [Lecture 8 \[Encoder-Decoder-Mux-Demux\] Spring 2023](#)

Lecture Video: [Mux Demux Video](#)

Assignment Ques:  CSE260 Assignment 03 Spring 2023_DZK

Assignment Solution:  CSE260 Assignment 3_ 21201087_Yeasin Fiyaz.pdf

Quiz:  CSE260 Quiz 3 Spring 2023 Questions & Answers.pdf

➤ **Lecture 9 and 10 combined: Lecture 9 basic FF, Lecture 10: Circuit to State Diagram**

4. **Lecture Slide:** [Lecture 9 \[Sequential Circuit-FF\] Spring 2023](#)

Lecture Video : [Video 1 \(SR & D FLIP-FLOP\)](#)
[Video 2 \(JK Flip-Flop & T Flip-Flop\)](#)

5 **Lecture 10{Circuit to State Diagram and State to Circuit Diagram}**

Slide: [Lecture 10 \[Sequential Circuit Analysis\] Spring 2023](#)

Lecture video: [Video 1](#)

Quiz Question: [Click](#)

Assignment Ques: [CSE260 Assignment 04 Spring 2023_DZK](#)

Assignment Solution: [CSE260_Assignment 4_21201087.pdf](#)

Counter:

- ▶ Design of synchronous counter using SR flipflop;mod 8 sequence, down counter
- ▶ 3-Bit & 4-bit Up/Down Synchronous Counter
- ▶ 3-Bit Synchronous Up Counter

6 **Lecture 11 [Memory] Spring 2023**

Video: [Click](#)
[Supplementary](#)

Lecture Note: [Click](#)

Tahmid's Drive:

https://drive.google.com/drive/folders/1GC2Di_HUnIQzmTuWVWjAJutHEnPc0vd6

