

1 1 1 1 1 1 0

A	B	C	$Y = ABC$
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	1

I_3	3	1	0	
I_2	2	1	1	$x = 10$
I_1	1	1	0	$y = 11$
I_0	0	1	0	

\bar{A} \bar{B} \bar{C}

0 0 0 1

$I_3 \geq I_2 \geq I_1 \geq I_0$

$I_2 \geq I_1 \geq I_3 \geq I_0$

priority encoder

15/2

I_0	I_1	I_2	I_3	x	y
X	X	X	1	1	1 - 3 I_3 high priority
X	X	1	0	1	0 - 2
X	1	0	0	0	1 - 1
1	0	0	0	0	0 - 0 I_0 low

Note :

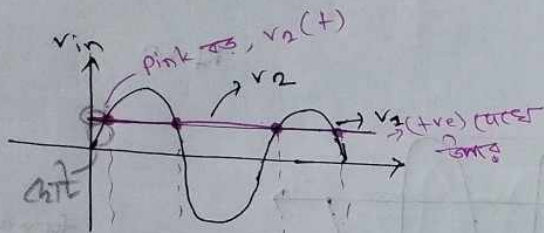
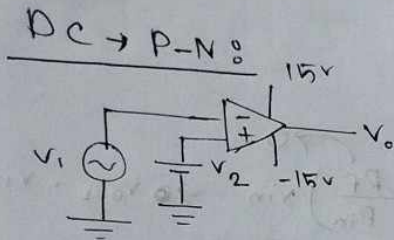
X = don't care

output will always depend on the priority list.

$$u = I_3 + I_2 I_3$$

$$y = I_3 + I_1 I_2 I_3$$

Comparator circuit



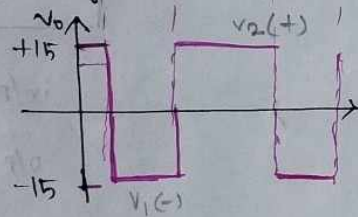
① wave shape draw

② intersecting point (जहाँ मिलेगा)

③ Compare

④ बड़ी अच्छी square wave

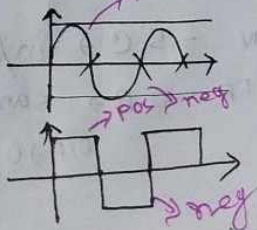
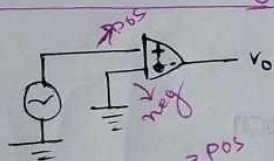
* (जहाँ मिलेगा) ratio to output square wave.



• highest o/p \rightarrow 15V

Zero-crossing Detector

*

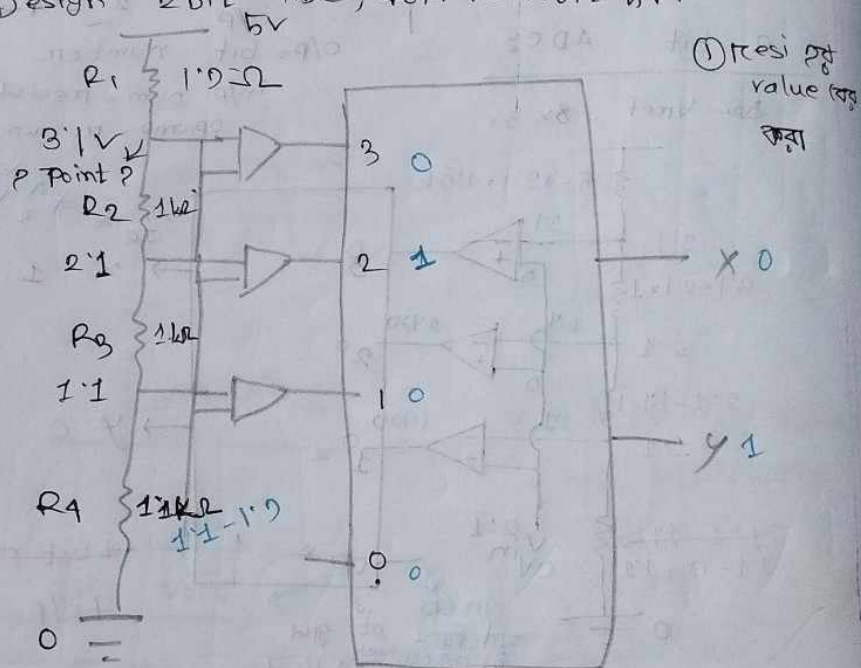


o/p \rightarrow square wave

$V_{ref} = 5V$ 2nd (Voltage)

(not fixed)	0	1	0	0
1:1	-	1:0	0	1
2:1	-	2:0	1	0
3:1	✓	-	1	1

Design 2bit ADC, ref. vol = 5V.



$$R_1 = 5 - 3.1 = 1.9k\Omega$$

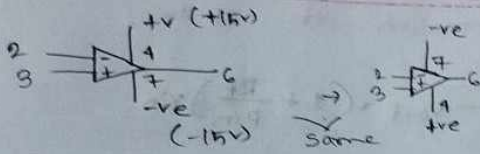
4 to 2 line Priority encoder

Operational Amplifiers

book - ① Coughlin (OP-Amp)

② Ramakanta A graykoed

9/2/29



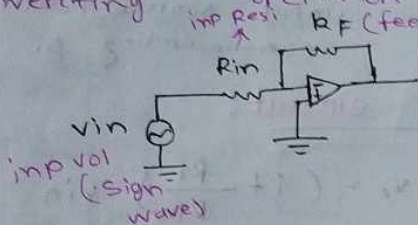
biasing - 15v

offset NULL	1	8	NC → No change
Inverting I/p	2	7	-V
Non Inverting I/p	3	6	output
input vol	4	5	offset NULL

offset NULL: additional voltmeter
NULL / त्रुटि को शून्य करने के लिए
offset exactly 0 करने के लिए 1, 5 pin configuration

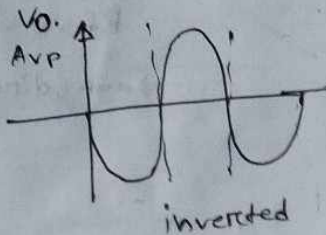
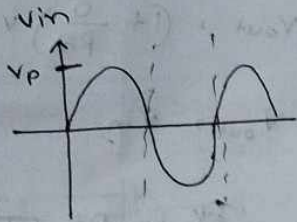
or

Inverting operation of OP-Amp:



$$V_{out} = -\frac{R_f}{R_{in}} V_{in}$$

$$-A V_{in}$$



or
*Design an inverting OP-Amp whose output will be 3V.

$$V_o = -\frac{R_f}{R_{in}} \times 3V \quad \left| \begin{array}{l} R_f = 1k\Omega \\ R_{in} = 1k\Omega \end{array} \right.$$

$$= -3V$$

next thursday quiz-2
(22.2)

ADC → Analog to digital converter

13/2/24

* design

↓
resistor
value
calculate
(for opamp)

a 2bit ADC / 3bit ADC.

N-bit number = o/p

2^n = output input, $n = o/p$

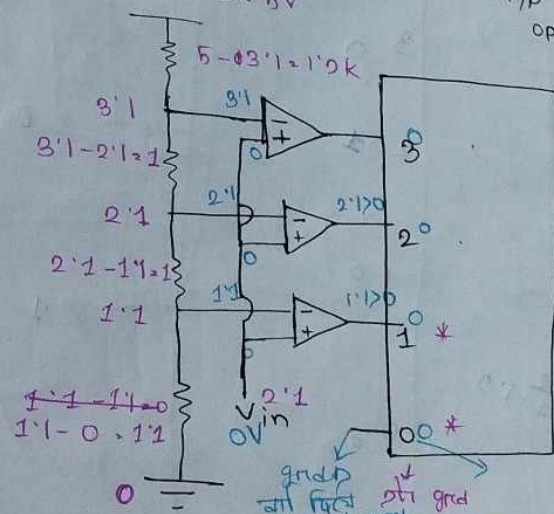
2 bit in/p → $2^2 = 4$ o/p i/p

o/p = bit number

in/p num = resistance num
opamp = n , num - 1

2 bit ADC:

$V_{ref} = 5V$



→ maybe 10
→ x 1

→ y 0

* last 2 bit o/p is correct
* o/p always highest correct.

num of OPAMP = Resistor num. - 1

	o/p			correct o/p
$V_{in} = 0$	0	0	00	00
	0	1	00	01

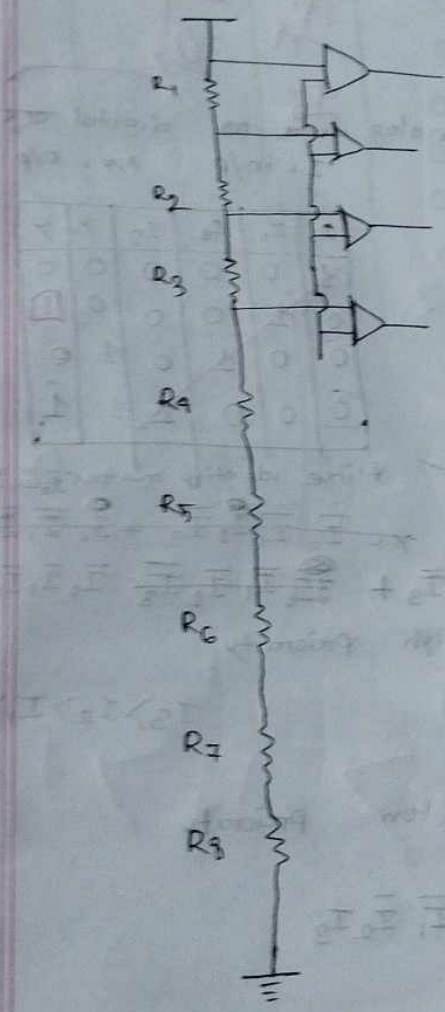


• (-) > (+) → 0

• (+) > (-) → 1

1110

3-bit ADCs



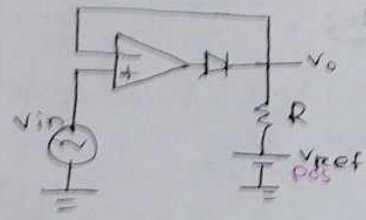
b_2 3 1 0 b_1 b_0 $\times 10$



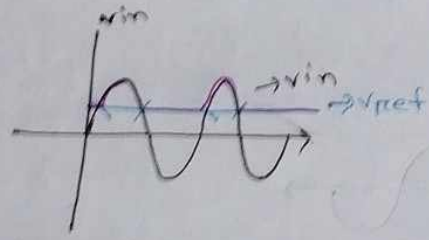
V	X	a_2	a_1	a_0	b_2	b_1	b_0
1	1	1	X	X	X	X	X
0	1	0	1	X	X	X	X
1	0	0	0	X	X	X	X
0	0	0	0	0	0	0	0

$\frac{1}{-}$ neg
 $\frac{1}{+}$ pos

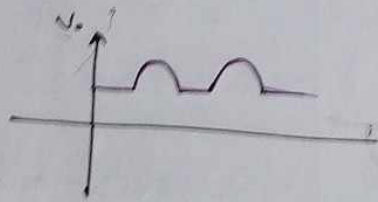
Forward bias Diodes
 Positive DC voltages



i/p

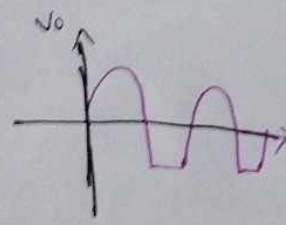
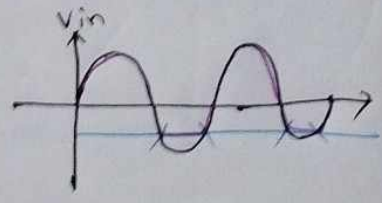
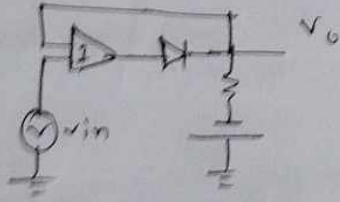


O/P



* 2nd vol हर चारों
 तर, तीरे प्रकाश

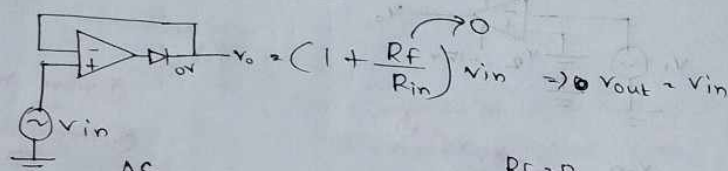
Neg DC vol:



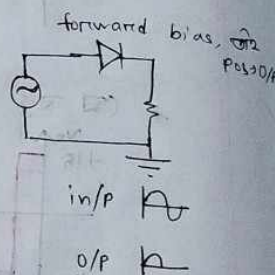
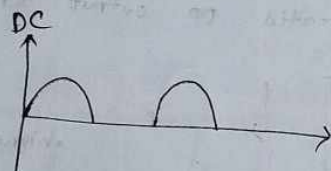
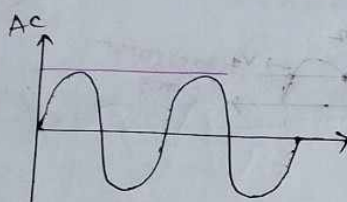
8/2/24

Half wave rectifiers

Forwarded bias Diode



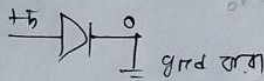
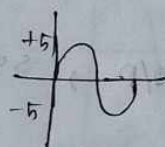
*similarity with vol. fol. circ.
diode! main diff



EEE is the best!

*diode ka shiksha

i/p, o/p same hai



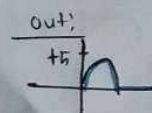
$A > K$

diode - ON - S.C. \rightarrow in/p o/p same

$K > A$

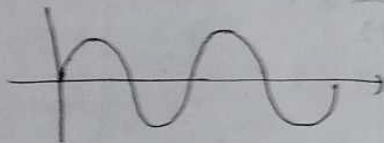
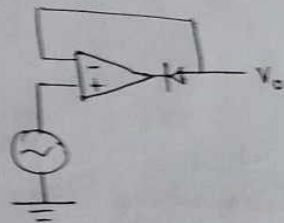
u OFF - O.E. \rightarrow same hai

o/p \rightarrow 0

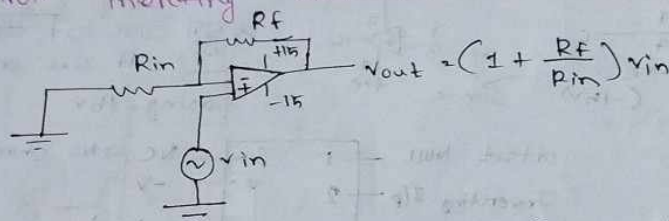


† NMOS 2 0 အား သာသာအား
† PMOS 2 5 အား သာသာအား

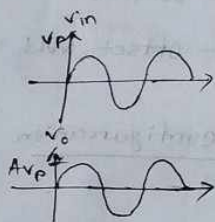
Reverse bias Diode



Non inverting operation of OP-Amp:



$$v_{out} = \left(1 + \frac{R_f}{R_{in}}\right) v_{in}$$



• amplify both times.

* direction same as input.

• value change as

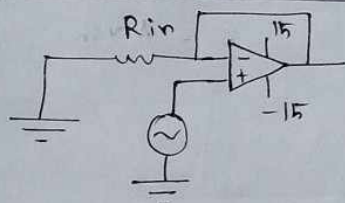
$$V_o = \left(1 + \frac{R_f}{R_{in}}\right) v_{in}$$

$$= (1+2) \cdot 2 \times 3$$

$$= 6V$$

input source positive p.s.o.
non-inverting p.

Voltage follower circuit:

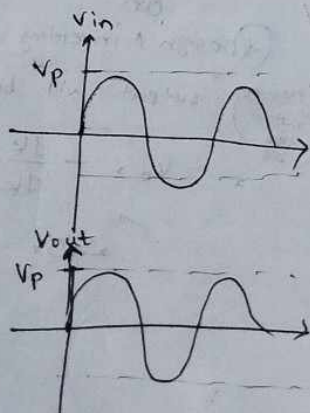


$$V_o = \left(1 + \frac{R_f}{R_{in}}\right) v_{in}$$

$$V_{out} = \left(1 + \frac{0}{R_{in}}\right) v_{in}$$

$$V_{out} = v_{in}$$

Peak voltage



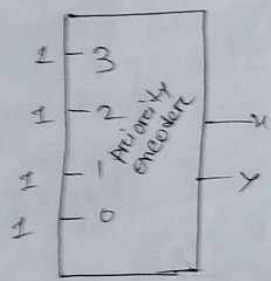
* R_f is 0.

$$R_f = 0$$

* amp, direction same.

$V_{ref} = 5V$ 2^{10}
 (not fixed) 0 - 1 = 0
 $1 \cdot 1 - 1 \cdot 0 = 0$
 $2 \cdot 1 - 2 \cdot 0$

encoder analog data to digital
 $I = in/p$ $x, y = o/p$



I_0	I_1	I_2	I_3	x	y
1	0	0	0	0	0
0	1	0	0	0	1
0	0	1	0	1	0
0	0	0	1	1	1

*like identity matrix $I_0 I_1 I_2 I_3$
 $x = \overline{I_0} \overline{I_1} \overline{I_2} I_3 + \overline{I_0} \overline{I_1} I_2 \overline{I_3} + \overline{I_0} I_1 \overline{I_2} I_3 + \overline{I_0} I_1 I_2 \overline{I_3}$

priority encoder I_3 High Priority

I_0	I_1	I_2	I_3	x	y
x	x	x	1	1	1
x	x	1	0	1	0
x	1	0	0	0	1
1	0	0	0	0	0

I_2
 I_1
 I_0 low Priority

$(1) y = \overline{I_0} \overline{I_1} \overline{I_2} I_3 + \overline{I_0} \overline{I_1} I_2 \overline{I_3}$

$x = don't$ care
 output will always depend on the priority list.