



## Features

- Operation voltage range: 1.65~5.5V
- Inputs Accept Voltages To 5.5V
- High noise immunity
- Low Power Dissipation
- Max  $t_{PD}$  Of 3.2 ns At 5V
- ESD Protection Exceeds JESD 22
- 2000-V Human-Body Model (A114-A)
- 1000-V Charged-Device Model (C101)
- SOT23-6 Package Available
- SOT363 Package Available

## General Description

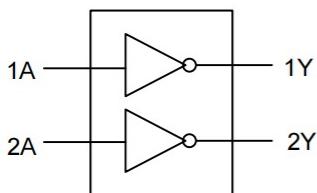
The **74LVC2G04** is a dual inverter gate and it provides the Boolean function  $Y = \overline{A}$  in positive logic.

This device has power-down protective circuit to prevent the device from destruction when it is powered down.

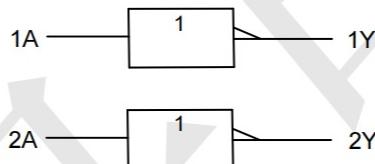
## Ordering Information

ORDER NUMBER	PACKAGE DESCRIPTION	PACKAGE OPTION
74LVC2G04GV	SOT23-6	Tape and Reel,3000
74LVC2G04GW	SOT363	Tape and Reel,3000

## Logic Diagram

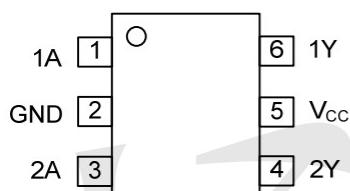


Logic symbol



IEC logic symbol

## Pin Configuration



## Function Table

INPUT(nA)	OUTPUT(nY)
H	L
L	H

Note: H: HIGH voltage level; L: LOW voltage level.

## Applications

- Voltage Level Shifting
- General Purpose Logic
- Power Down Signal Isolation
- Wide array of products such as:
  - PCs, Networking, Notebooks, Netbooks, PDAs
  - Tablet Computers, E-readers
  - Computer Peripherals, Hard Drives, CD/DVD ROM
  - TV, DVD, DVR, Set-Top Box
  - Cell Phones, Personal Navigation / GPS
  - MP3 Players, Cameras, Video Recorders

## Absolute Maximum Ratings

PARAMETER		SYMBOL	RATINGS	UNIT
Supply Voltage		V <sub>CC</sub>	-0.5 ~ +6.5	V
Input Voltage		V <sub>IN</sub>	-0.5 ~ +6.5	V
Output Voltage	Active Mode	V <sub>OUT</sub>	-0.5 ~ V <sub>CC</sub> +0.5	V
	Power-Down Mode		-0.5 ~ +6.5	V
V <sub>CC</sub> or GND Current	I <sub>CC</sub>		±100	mA
Continuous Output Current (V <sub>OUT</sub> =0 to V <sub>CC</sub> )	I <sub>OUT</sub>		±50	mA
Input Clamp Current (V <sub>IN</sub> <0)	I <sub>IK</sub>		-50	mA
Output Clamp Current (V <sub>OUT</sub> >V <sub>CC</sub> or V <sub>OUT</sub> <0 )	I <sub>OK</sub>		-50	mA
Power Dissipation (T <sub>A</sub> =-40°C ~ +125°C)	P <sub>D</sub>		300	mW
Operating Junction Temperature	T <sub>J</sub>		-40 ~ +125	°C
Storage Temperature	T <sub>STG</sub>		-65 ~ +150	°C

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.  
 Absolute maximum ratings are stress ratings only and functional device operation is not implied.  
 2. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

## Recommended Operating Conditions

PARAMETER		SYMBOL	MIN	TYP	MAX	UNIT
Supply Voltage	V <sub>CC</sub>		1.65		5.5	V
Input Voltage	V <sub>IN</sub>		0		5.5	V
Output Voltage	Active Mode	V <sub>OUT</sub>	0		V <sub>CC</sub>	V
	Power-Down Mode		0		5.5	V
Input Transition Rise or Fall Rate	V <sub>CC</sub> =1.65V to 2.7V	t <sub>R</sub> / t <sub>F</sub>	0		20	ns/V
	V <sub>CC</sub> =2.7V to 5.5V		0		10	ns/V

**Electrical Characteristics** ( $T_A = 25^\circ C$ , unless otherwise specified)

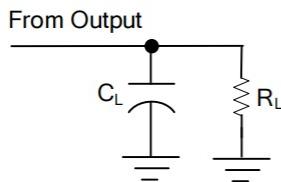
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
High-level Input Voltage	$V_{IH}$	$V_{CC} = 1.65V \sim 1.95V$	$0.65 \times V_{CC}$			V
		$V_{CC} = 2.3V \sim 2.7V$	1.7			V
		$V_{CC} = 2.7V \sim 3.6V$	2			V
		$V_{CC} = 4.5V \sim 5.5V$	$0.7 \times V_{CC}$			V
Low-level Input Voltage	$V_{IL}$	$V_{CC} = 1.65V \sim 1.95V$		$0.35 \times V_{CC}$		V
		$V_{CC} = 2.3V \sim 2.7V$		0.7		V
		$V_{CC} = 2.7V \sim 3.6V$		0.8		V
		$V_{CC} = 4.5V \sim 5.5V$		$0.3 \times V_{CC}$		V
High-Level Output Voltage	$V_{OH}$	$V_{CC} = 1.65 \sim 5.5V, I_{OH} = -100\mu A$	$V_{CC} - 0.1$			V
		$V_{CC} = 1.65V, I_{OH} = -4mA$	1.2			V
		$V_{CC} = 2.3V, I_{OH} = -8mA$	1.9			V
		$V_{CC} = 2.7V, I_{OH} = -12mA$	2.2			V
		$V_{CC} = 3.0V, I_{OH} = -24mA$	2.3			V
Low-Level Output Voltage	$V_{OL}$	$V_{CC} = 1.65 \sim 5.5V, I_{OL} = 100\mu A$		0.1		V
		$V_{CC} = 1.65V, I_{OL} = 4mA$		0.45		V
		$V_{CC} = 2.3V, I_{OL} = 8mA$		0.3		V
		$V_{CC} = 2.7V, I_{OL} = 12mA$		0.4		V
		$V_{CC} = 3.0V, I_{OL} = 24mA$		0.55		V
		$V_{CC} = 4.5V, I_{OL} = 32mA$		0.55		V
Input Leakage Current	$I_{I(LEAK)}$	$V_{CC} = 5.5V, V_{IN} = 5.5V$ or GND		$\pm 0.1$	$\pm 5$	$\mu A$
Power OFF Leakage Current	$I_{OFF}$	$V_{CC} = 0V, V_{IN}$ or $V_{OUT} = 5.5V$		$\pm 0.1$	$\pm 10$	$\mu A$
Quiescent Supply Current	$I_Q$	$V_{CC} = 5.5V, V_{IN} = V_{CC}$ or GND, $I_{OUT} = 0$		0.1	10	$\mu A$
Additional Quiescent Supply Current Per Input Pin	$\Delta I_{CC}$	$V_{CC} = 2.3 \sim 5.5V$ , One input at $V_{CC} - 0.6V$ , Other inputs at $V_{CC}$ or GND		5	500	$\mu A$

**Switching Characteristics** ( $T_A = 25^\circ C$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Propagation delay from input (A) to output(Y)	$t_{PLH}$	$C_L = 30pF, V_{CC} = 1.8 \pm 0.15V, R_L = 1K\Omega$	1.0	3.5	8.0	ns
		$V_{CC} = 2.5 \pm 0.2V, R_L = 500\Omega$	1.0	2.2	4.4	ns
	$t_{PHL}$	$V_{CC} = 2.7V, R_L = 500\Omega$	1.0	2.7	5.2	ns
		$V_{CC} = 3.3 \pm 0.3V, R_L = 500\Omega$	0.5	2.7	4.1	ns
		$V_{CC} = 5 \pm 0.5V, R_L = 500\Omega$	1.0	1.9	3.2	ns

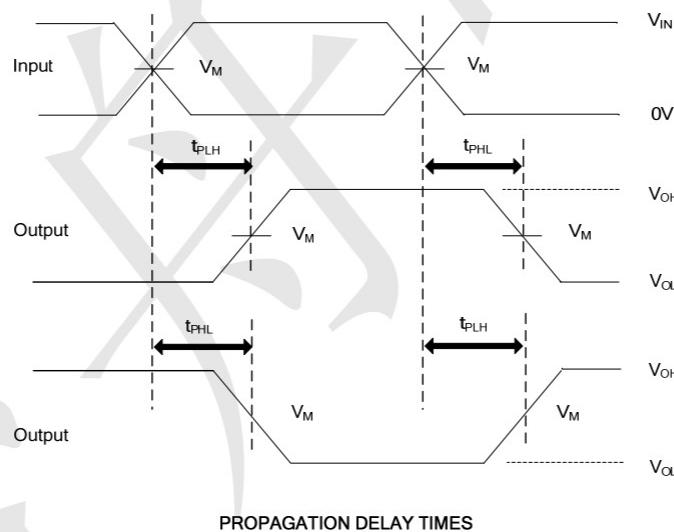


## TEST CIRCUIT AND WAVEFORMS



TEST CIRCUIT

$V_{CC}$	Inputs		$V_M$	$C_L$	$R_L$
	$V_{IN}$	$t_R, t_F$			
$1.8V \pm 0.15V$	$V_{CC}$	$\leq 2ns$	$V_{CC}/2$	30pF	$1K\Omega$
$2.5V \pm 0.2V$	$V_{CC}$	$\leq 2ns$	$V_{CC}/2$	30pF	$500\Omega$
$2.7V$	$2.7V$	$\leq 2.5ns$	1.5V	50pF	$500\Omega$
$3.3V \pm 0.3V$	$2.7V$	$\leq 2.5ns$	1.5V	50pF	$500\Omega$
$5V \pm 0.5V$	$V_{CC}$	$\leq 2.5ns$	$V_{CC}/2$	50pF	$500\Omega$



Notes: 1.  $C_L$  includes probe and jig capacitance.

2. All input pulses are supplied by generators having the following characteristics: PRR  $\leq 10MHz$ ,  $Z_0=50\Omega$ .



**TECH PUBLIC**  
台舟电子

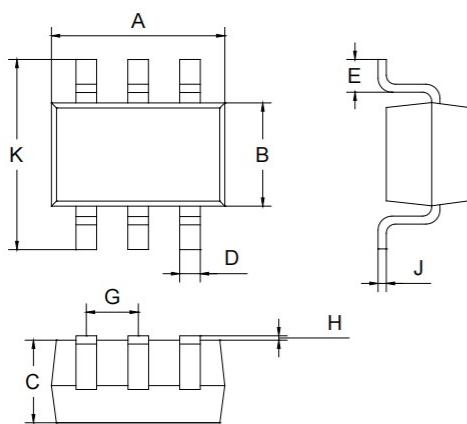
**74LVC2G04**

**Dual Inverter**

[www.sot23.com.tw](http://www.sot23.com.tw)

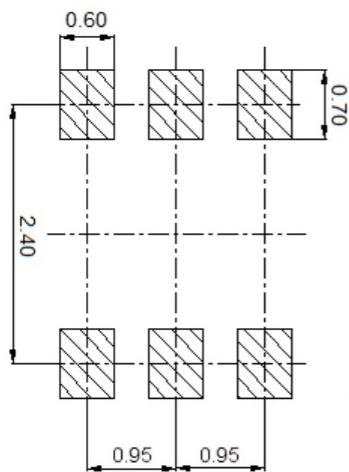
### Package Outline Dimensions (Unit: mm)

SOT23-6



Dimension	Min.	Max.
A	2.80	3.00
B	1.50	1.70
C	1.00	1.20
D	0.35	0.45
E	0.35	0.55
G	0.90	1.00
H	0.02	0.10
J	0.10	0.20
K	2.60	3.00

### Mounting Pad Layout (Unit: mm)





**TECH PUBLIC**  
台舟电子

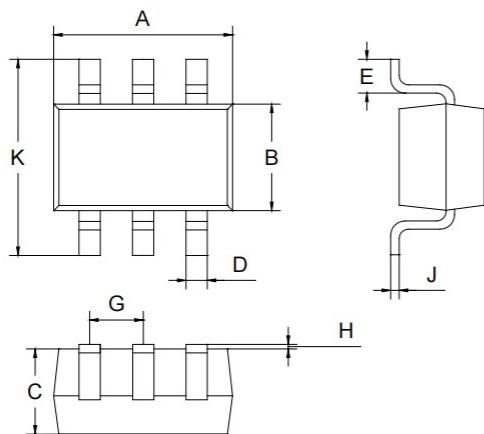
**74LVC2G04**

**Dual Inverter**

[www.sot23.com.tw](http://www.sot23.com.tw)

### Package Outline Dimensions (Unit: mm)

SOT363



Dimension	Min.	Max.
A	2.00	2.20
B	1.15	1.35
C	0.85	1.05
D	0.15	0.35
E	0.25	0.40
G	0.60	0.70
H	0.02	0.10
J	0.05	0.15
K	2.20	2.40

### Mounting Pad Layout (Unit: mm)

