## **METHODOLOGY**

The following points represents the path taken to reach the project's goal.

## **Block Diagram of the components involved:**

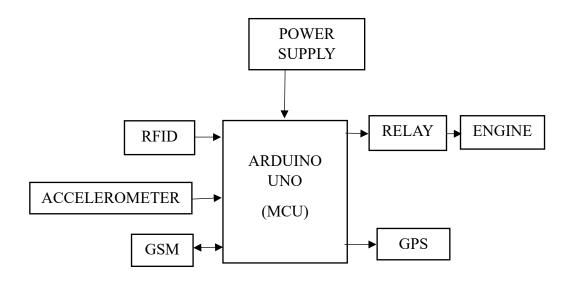


Fig1: Functional block diagram of the device.