OC	Op 1	Op 2	Instruction Description
00	Pn	XX	**Load Pointer Immediate** Pn ← XX, X: {0 .. 9}. Integer value XX is loaded into Pointer Register n
01	Pn	XX	**Add to Pointer Immediate** Pn ← Pn + XX, X: {0 .. 9}. Pointer incremented by integer XX
02	Pn	XX	**Subtract From Pointer Immediate** Pn ← Pn – XX, X{0 .. 9} Pointer decremented by XX
03	XX	XX	**Load Accumulator Immediate** AC ← XXXX, X:{0 .. 9} Load integer value XXXX into accumulator
04	Pn	ZZ	**Load Accumulator Register Addressing** AC ← M(Pn) Load accumulator with contents of memory address contained in Pn, n{0..3}
05	XX	ZZ	**Load Accumulator Direct Addressing** AC ← M(XX) Load Accumulator with contents of address XX, X{0..9}
06	Pn	ZZ	**Store Accumulator Register Addressing** M(Pn) ← AC Store contents of Accumulator to the memory location held in Pn.
07	XX	ZZ	**Store Accumulator Direct Addressing** M(XX) ← AC Store contents of AC into memory location XX, X{0..9}.

08	Rn	Pn	**Store Register to memory: Register Addressing** M(Pn) ← Rn Store contents of register Rn into memory address pointed to by Pn, n{0..3}
09	Rn	XX	**Store Register to Memory: Direct Addressing** M(XX) ← Rn Store contents of Register n, n{0..3} to Memory location XX, X{0..9}.
10	Rn	Pn	**Load Register from memory: Register Addressing** Rn ← M(Pn) Load Register Rn with the contents of memory location pointed to by Pn, n{0..3}.
11	Rn	XX	**Load register from memory: Direct Addressing** Rn ← M(XX) Load Register Rn, n{0..3}, with the contents of memory location XX, X{0..9}.
12	XX	XX	**Load Register R0 Immediate** R0 ← XXXX Load Register R0 with integer value XXXX, X{0..9}
13	Rn	Rp	**Register to Register Transfer** Rn ← [Rp] Rn is assigned the contents of Rp, n, p {0..3}
14	Rn	ZZ	**Load Accumulator from Register** ACC ← [Rn] Accumulator assigned the contents of Rn, n{0..3}
15	Rn	ZZ	**Load Register from Accumulator** Rn ←[Acc] Register Rn, n{0..3} assigned the contents of the accumulator

16	XX	XX	**Add Accumulator Immediate** AC ← AC + XXXX, X:{0 .. 9}
17	XX	XX	**Subtract Accumulator Immediate** AC ← AC – XXXX, X{0..9}
18	Rn	ZZ	**Add contents of Register to Accumulator** AC ← AC + Rn, n{0..3}
19	Rn	ZZ	**Subtract contents of Register from Accumulator** AC ← AC – Rn, n{0..3}
20	Pn	ZZ	**Add Accumulator Register Addressing.** AC ← AC + M(Pn) Add contents of memory pointed to by Pn, n{0..3} to AC.
21	XX	ZZ	**Add Accumulator Direct Addressing** AC ← AC + M(XX) Add contents of memory location XX to Accumulator X {0..9}
22	Pn	ZZ	**Subtract from Accumulator Register Addressing** AC ← AC – M(Pn) Subtract Contents of memory location pointed to by Pn, n{0..3} from Accumulator
23	XX	ZZ	**Subtract from Accumulator Direct Addressing** AC <- AC – M(XX), X{0..9} Subtract from accumulator contents of memory location XX.
24	Pn	ZZ	**Compare Equal Register Addressing** If AC == M(Pn) then PSW[0] = T else PSW[0] = F If contents of accumulator are equal to contents of memory location pointed to by Pn, n{0..3}, then byte 0 of the PSW set to T, else set to F.

25	Pn	ZZ	**Compare Less Register Addressing** If AC < M(Pn) then PSW[0] = T else PSW[0] = F If contents of Accumulator are less than the contents of memory location pointed to by Pn, n{0..3}, then byte 0 of the PSW set to T, else set to F.
26	Pn	ZZ	**Compare Greater Register Addressing** If AC > M(Pn) then PSW[0] = T else PSW[0] = F If contents of accumulator are greater than contents of memory location pointed to by Pn, n{0..3}, then byte 0 of the PSW set to T, else set to F.
27	XX	XX	**Compare Greater Immediate** If AC > XXXX, X{0..9}, then PSW[0] = T else PSW[0] = F If contents of Accumulator are greater than the value XXXX, then byte 0 of the PSW set to T, else set to F.
28	XX	XX	**Compare Equal immediate** If AC == XXXX, X{0..9}, then PSW[0] = T else PSW[0] = F Compare contents of accumulator to integer.
29	XX	XX	**Compare Less Immediate** If AC < XXXX, X{0..9}, then PSW[0] = T else PSW[0] = F Compare contents of accumulator to integer.
30	Rn	ZZ	**Compare Register Equal** if ACC == [Rn] then PSW[0] = T else PSW[0] = F Compare contents of accumulator to contents in register Rn, n{0..3}
31	Rn	ZZ	**Compare Register Less** If [Acc] < [Rn], then PSW[0] = T else PSW[0]=F Compare contents of accumulator to contents in Register Rn, n{0..3}

32	Rn	ZZ	**Compare Register Greater** if [ACC] > [Rn] then PSW[0] = T else PSW[0] = F Compare contents of accumulator to contents in register Rn, n{0..3}
33	XX	ZZ	**Branch Conditional True** If PSW[0] = T then PC = XX, X{0..9}
34	XX	ZZ	**Branch Conditional False** If PSW[0] = F then PC = XX, X{0..9}
35	XX	ZZ	**Branch Unconditional** PC = XX, X{0..9}
99	ZZ	ZZ	**Halt**