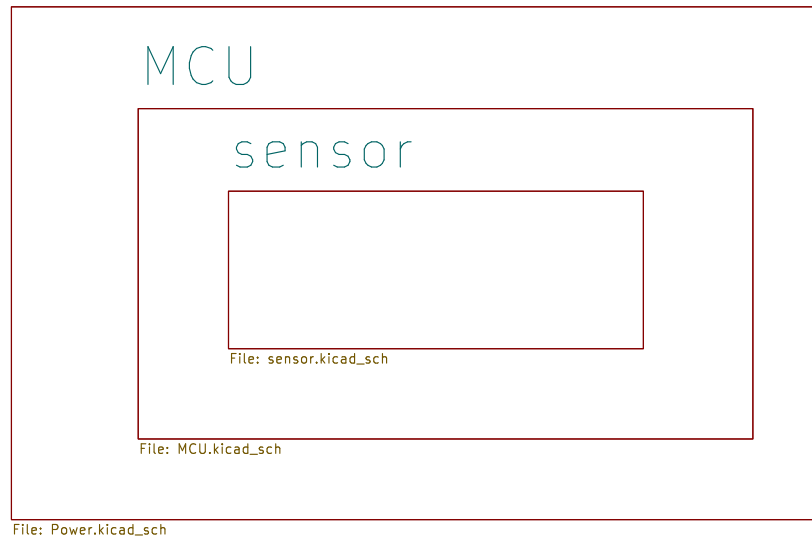
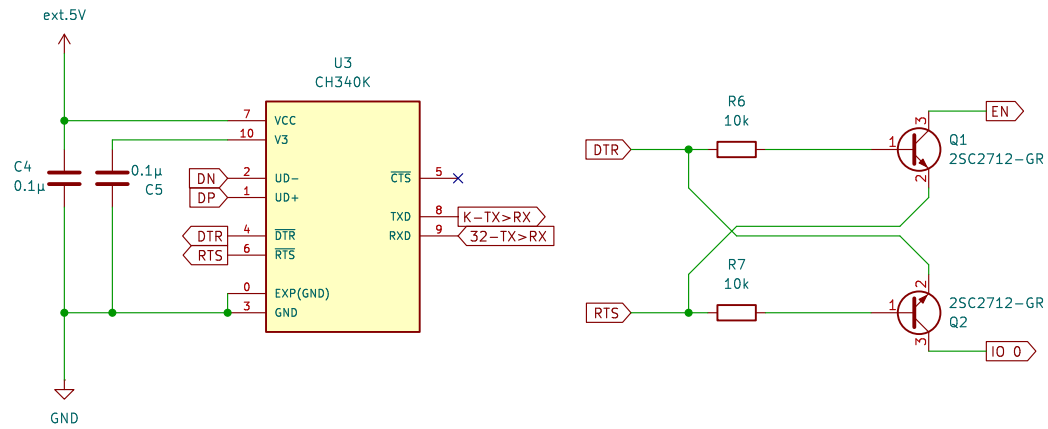
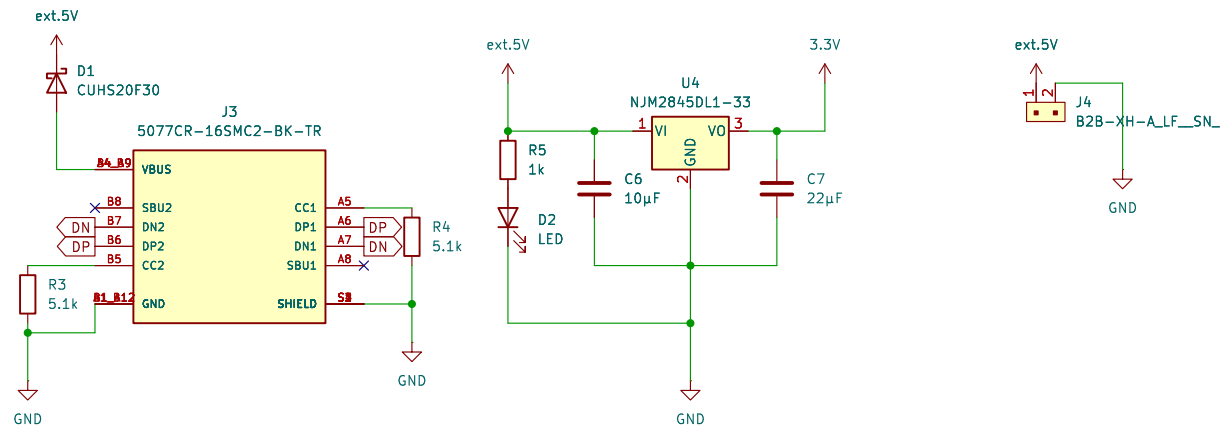


Power





The diagram shows an ESP32-WROOM-32E module (U1) with the following connections:

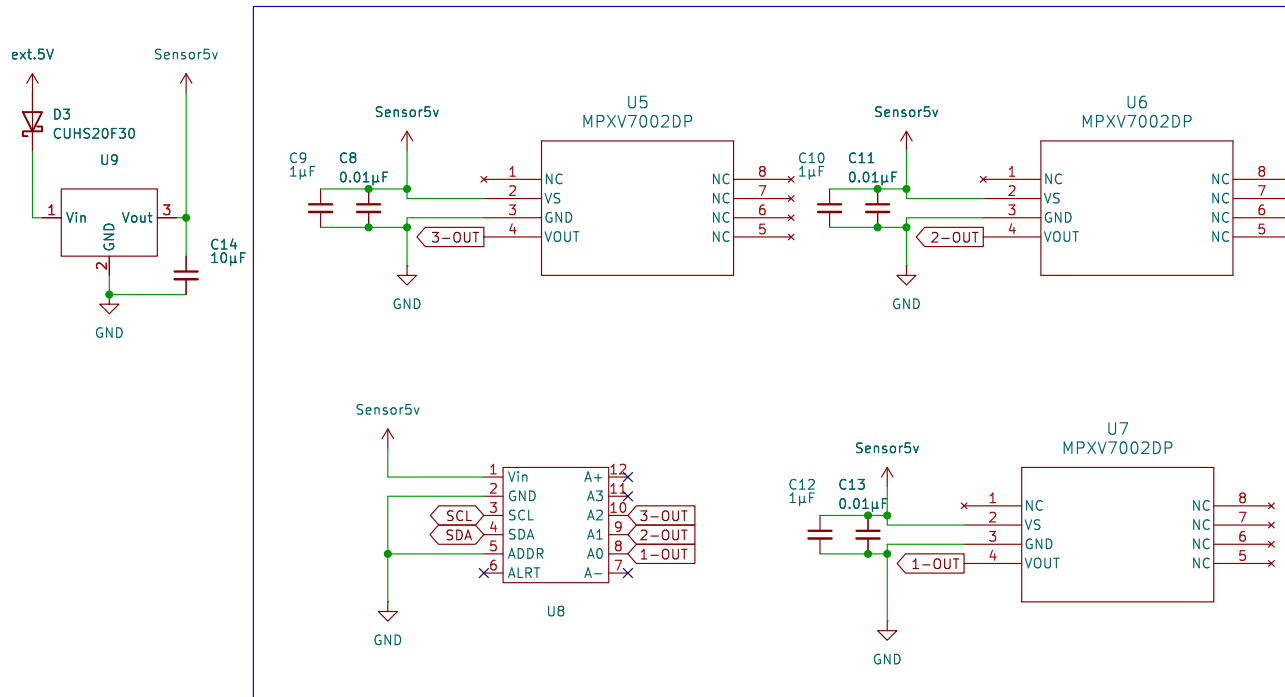
- Power:**
 - VDD (pin 2) is connected to 3.3V.
 - GND (pin 1) is connected to GND.
- Control:**
 - EN (pin 3) is connected to a 3.3V supply through a 10k resistor (R1) and to the EN pin of a switch (SW1).
 - SW1 is connected to GND through a 0.1μF capacitor (C1).
- GPIOs:**
 - IO0 (pin 25) is connected to a 3.3V supply through a 10k resistor (R2) and to the IO0 pin of a switch (SW2).
 - SW2 is connected to GND through a 0.1μF capacitor (C2).
- Other Connections:**
 - SENSOR_VP (pin 4) is connected to 3.3V.
 - SENSOR_VN (pin 5) is connected to GND.
 - TXD0/I01 (pin 100) is connected to TXD0 of a UART module.
 - RXD0/I03 (pin 104) is connected to RXD0 of a UART module.
 - UART1-RX (pin 16) is connected to RXD0 of a UART module.
 - UART1-TX (pin 17) is connected to TXD0 of a UART module.
 - UART2-RX (pin 27) is connected to RXD0 of a UART module.
 - UART2-TX (pin 28) is connected to TXD0 of a UART module.
 - CLK (pin 30) is connected to CLK of a UART module.
 - DAT0 (pin 31) is connected to DAT0 of a UART module.
 - SDA (pin 33) is connected to SDA of a UART module.
 - SCL (pin 36) is connected to SCL of a UART module.
 - CMD (pin 37) is connected to CMD of a UART module.
 - CANTX (pin 8) is connected to CANTX of a UART module.
 - CANRX (pin 9) is connected to CANRX of a UART module.

The schematic diagram illustrates the UART module's connections. It features two headers, J5 and J6, each with four pins. Header J5 is connected to an external 5V supply (ext.5V) at pin 1, a signal line (B4B-XH-A_LF_SN_) at pin 2, the UART1-RX signal at pin 3, and the UART1-TX signal at pin 4. Header J6 is connected to ground (GND) at pin 1, an external 5V supply (ext.5V) at pin 2, a signal line (B4B-XH-A_LF_SN_) at pin 3, the UART2-RX signal at pin 4, and the UART2-TX signal at pin 5. The UART1 and UART2 signals are shown as bidirectional arrows, indicating they can be used for both sending and receiving data.

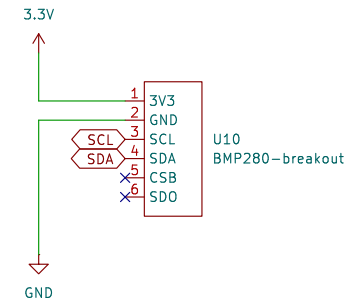
Pinout diagram for the DM3AT-SF-PEJM5 module (J1):

Pin	Signal
1	DAT2
2	DAT3/CD
3	CMD
4	VDD
5	CLK
6	VSS
7	DAT0
8	DAT1
9	DET_A
10	DET_B
11	SHIELD
12	GND
13	GND
14	GND

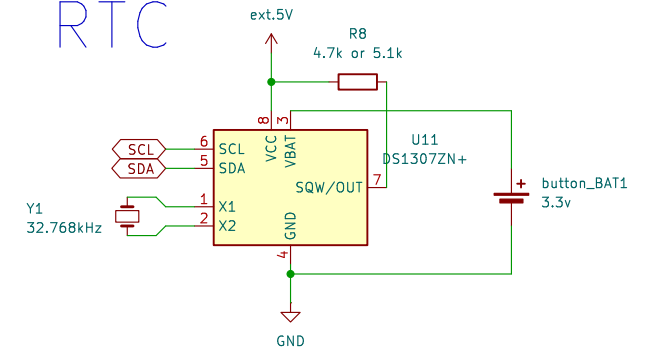
Pitot tube



Temp Hum Prss



RTC



I2C addr
 ADS1115 breakout: default 0x48
 DHT20: 0x38