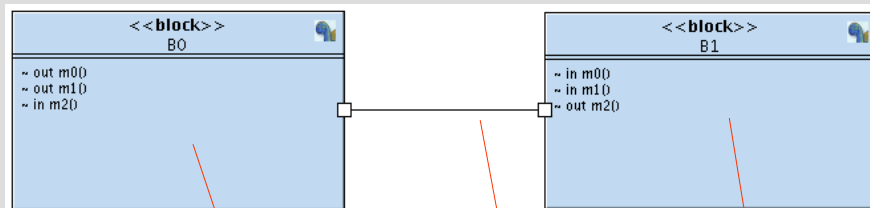
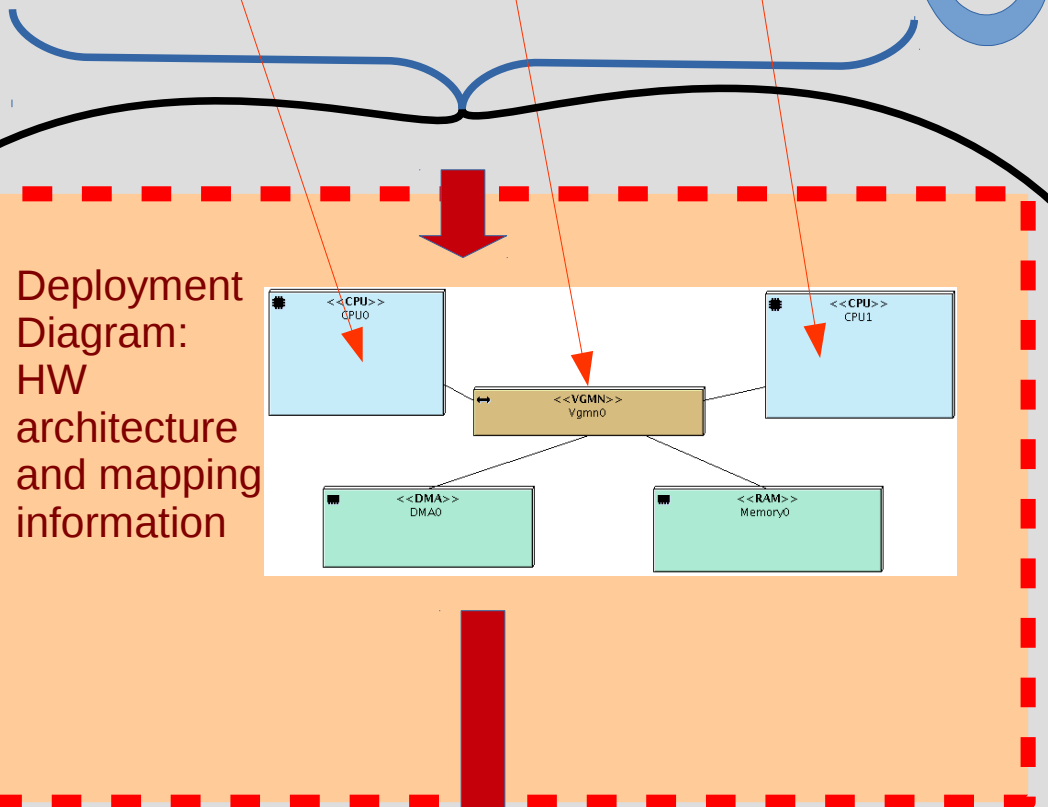


AVATAR

Model of SW components



Functional simulation and formal verification



Deployment Diagram:
HW architecture and mapping information

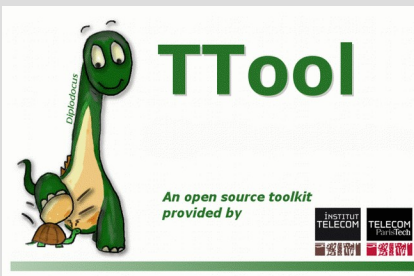
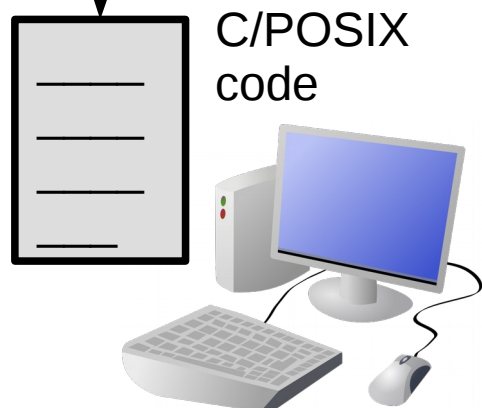
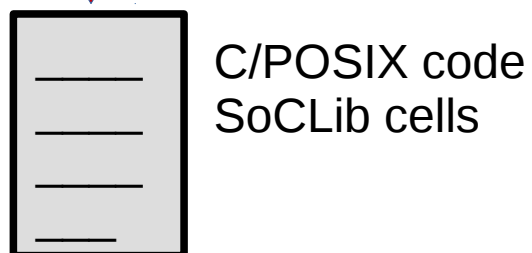


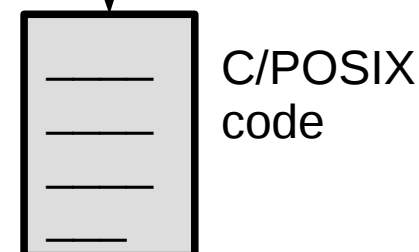
Diagram edition,
model transformation,
model backtracing



Execution on local host



CABA simulation of HW and SW



Execution on target

