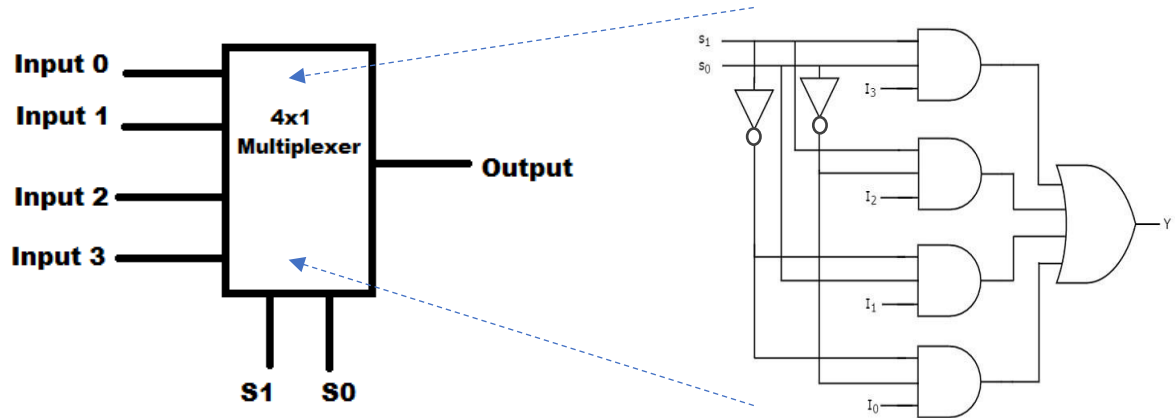


EEE3096S Lecture 24: Class/Takehome Exercise

Pseudo code thinking exercise...

Remember how a multiplexer is made using AND, NOT and OR gates?



I know you don't know the full Verilog code yet, but you know there are registers and wires that connect things (i.e. other modules) up. Experiment with your own pseudocode on how you might express this circuit as code. You could just use binary expressions and that would likely be close enough. *We will revisit this next lecture, using actual Verilog.*

Do your coding / rough notes / drawing below:
