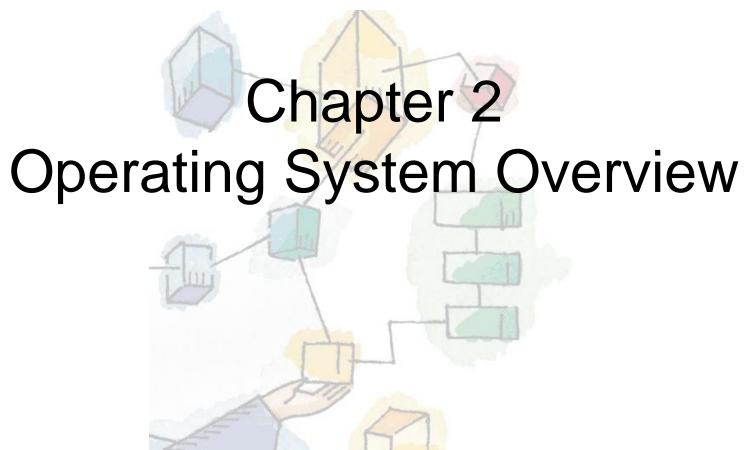
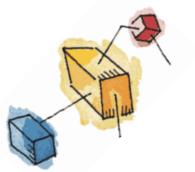
Operating Systems: Internals and Design Principles, 6/E William Stallings



Patricia Roy
Manatee Community College, Venice,
FL
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Operating System

- A program that controls the execution of application programs
- An interface between applications and hardware



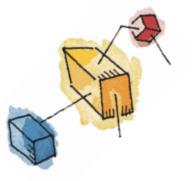


perating System Objectives

- Convenience
- Efficiency
- Ability to evolve







Layers and Views

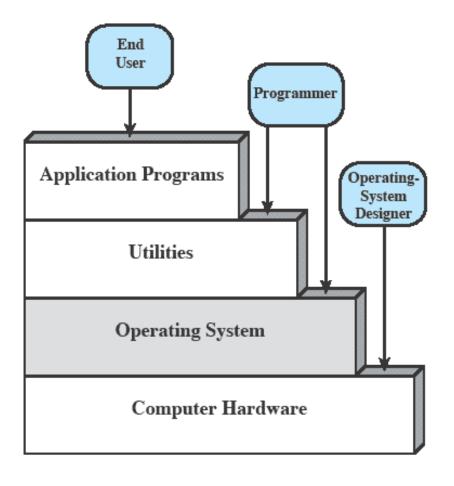




Figure 2.1 Layers and Views of a Computer System

- Program development
 - Editors and debuggers
- Program execution
- Access I/O devices





- Controlled access to files
- System access





- Error detection and response
 - Internal and external hardware errors
 - Software errors
 - Operating system cannot grant request of application

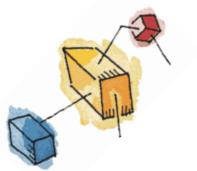




- Accounting
 - Collect usage statistics
 - Monitor performance
 - Used to anticipate future enhancements
 - Used for billing purposes







Operating System

- Responsible for managing resources
- Functions same way as ordinary computer software
 - It is a program that is executed
- Operating system relinquishes control of the processor





OS as Resource Manager

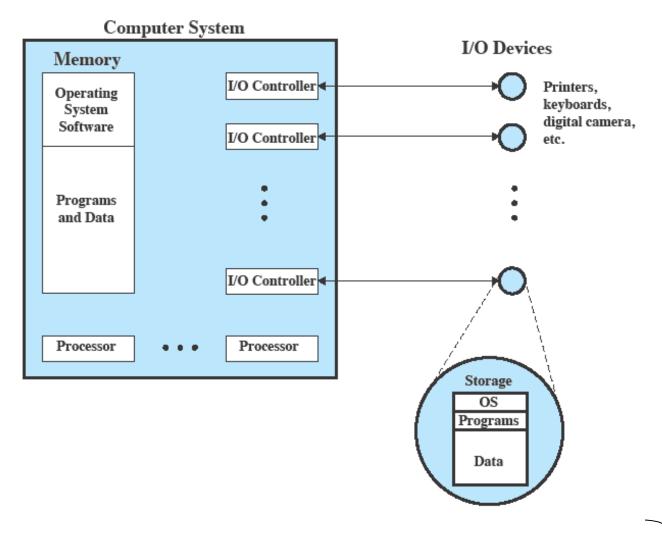
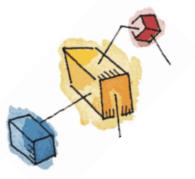




Figure 2.2 The Operating System as Resource Manager



Kernel

- Portion of operating system that is in main memory
- Contains most frequently used functions
- Also called the nucleus







- Hardware upgrades plus new types of hardware
- New services
- Fixes



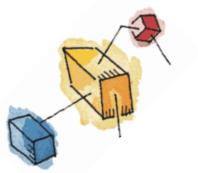




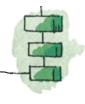
- Serial processing
 - No operating system
 - Machines run from a console with display lights, toggle switches, input device, and printer



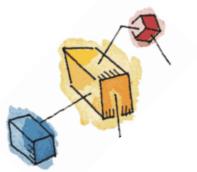




- Serial processing
 - Schedule time
 - Setup included loading the compiler, source program, saving compiled program, and loading and linking



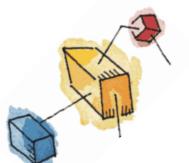




- Simple batch system
 - Monitor
 - Software that controls the sequence of events
 - Batch jobs together
 - Program returns control to monitor when finished







Job Control Language

- Special type of programming language
- Provides instruction to the monitor
 - What compiler to use
 - What data to use

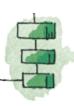






- \$JOB
- \$FTN
 - ... FORTRAN instructions
 - ... FORTRAN instructions
 - ... FORTRAN instructions
- \$LOAD
- \$RUN
 - DATA
 - DATA
- \$END





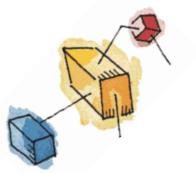


Hardware Features

- Memory protection
 - Does not allow the memory area containing the monitor to be altered
- Timer
 - Prevents a job from monopolizing the system







Hardware Features

- Privileged instructions
 - Certain machine level instructions can only be executed by the monitor
- Interrupts
 - Early computer models did not have this capability





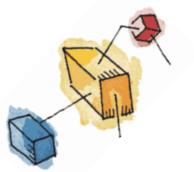


Memory Protection

- User program executes in user mode
 - Certain instructions may not be executed







Memory Protection

- Monitor executes in system mode
 - Kernel mode
 - Privileged instructions are executed
 - Protected areas of memory may be accessed





System Utilization Example

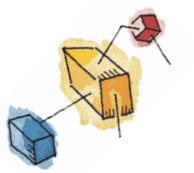
Read one record from file $15 \mu s$ Execute 100 instructions $1 \mu s$ Write one record to file $15 \mu s$ TOTAL $31 \mu s$

Percent CPU Utilization $=\frac{1}{31} = 0.032 = 3.2\%$

Figure 2.4 System Utilization Example

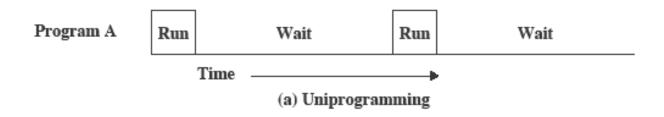






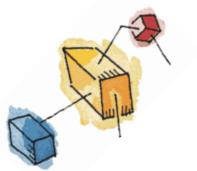
Uniprogramming

 Processor must wait for I/O instruction to complete before proceeding



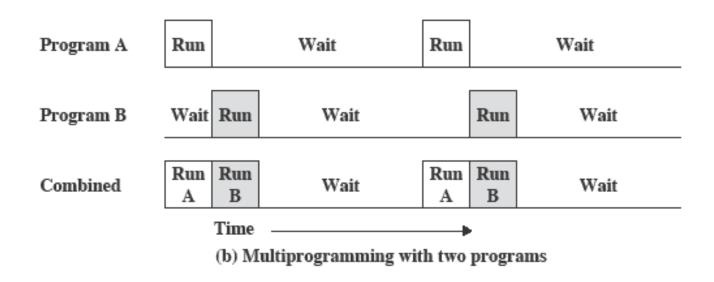






Multiprogramming

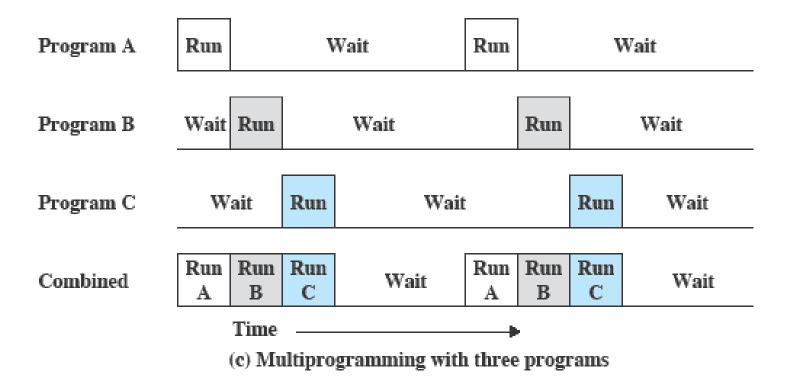
 When one job needs to wait for I/O, the processor can switch to the other job





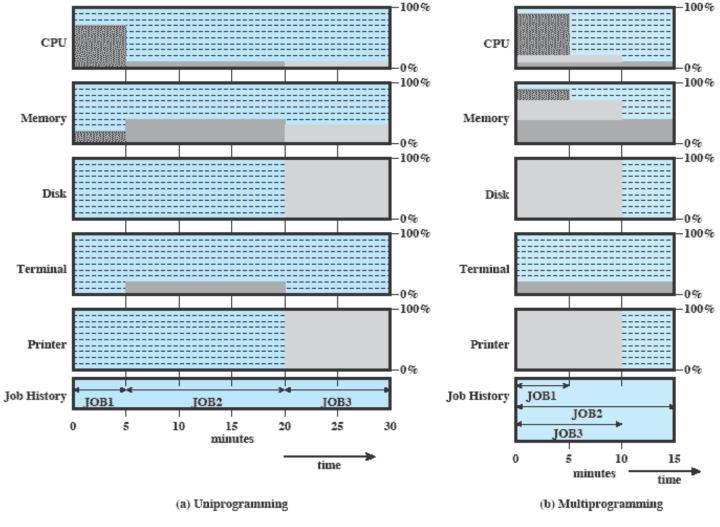


Multiprogramming





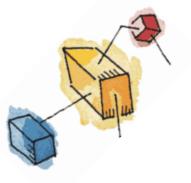
Utilization Histograms











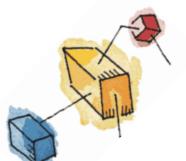
Example

Table 2.1 Sample Program Execution Attributes

	JOB1	JOB2	JOB3
Type of job	Heavy compute	Heavy I/O	Heavy I/O
Duration	5 min	15 min	10 min
Memory required	50 M	100 M	75 M
Need disk?	No	No	Yes
Need terminal?	No	Yes	No
Need printer?	No	No	Yes







Time Sharing Systems

- Using multiprogramming to handle multiple interactive jobs
- Processor's time is shared among multiple users
- Multiple users simultaneously access the system through terminals





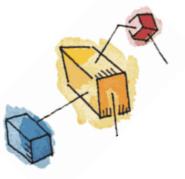
Batch Multiprogramming versus Time Sharing

Table 2.3 Batch Multiprogramming versus Time Sharing

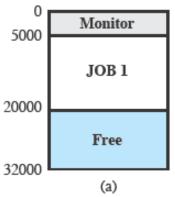
	Batch Multiprogramming	Time Sharing
Principal objective	Maximize processor use	Minimize response time
Source of directives to operating system	Job control language commands provided with the job	Commands entered at the terminal

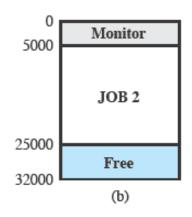


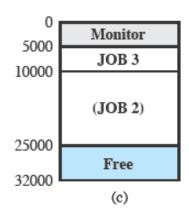


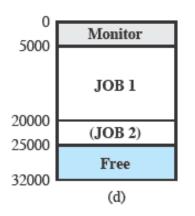


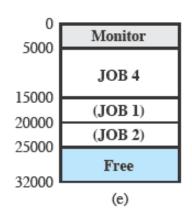
CTSS Operation











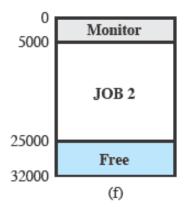




Figure 2.7 CTSS Operation



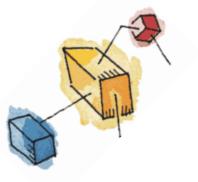


Major Achievements

- Processes
- Memory management
- Information protection and security
- Scheduling and resource management
- System structure





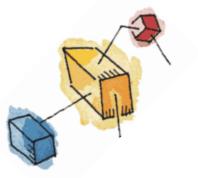


Process

- A program in execution
- An instance of a program running on a computer
- The entity that can be assigned to and executed on a processor







Process

- A unit of activity characterized by
 - A single sequential thread of execution
 - A current state
 - An associated set of system resources





Difficulties with Designing System Software

- Improper synchronization
- Failed mutual exclusion
- Nondeterminate program operation
- Deadlocks





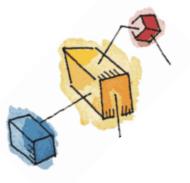


Process

- Consists of three components
 - An executable program
 - Associated data needed by the program
 - Execution context of the program
 - All information the operating system needs to manage the process







Process

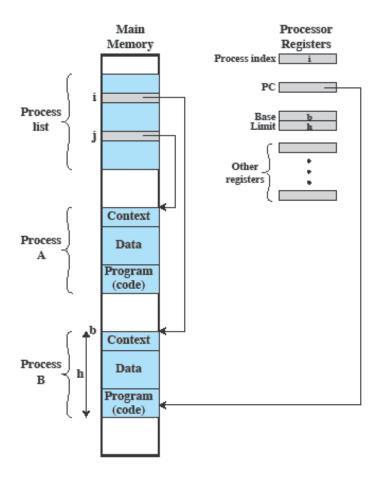
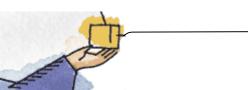
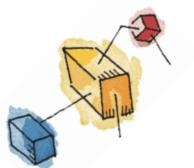


Figure 2.8 Typical Process Implementation







Memory Management

- Process isolation
- Automatic allocation and management
- Support of modular programming
- Protection and access control
- Long-term storage





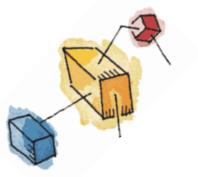


Virtual Memory

- Implements long-term store
- Information stored in named objects called files
- Allows programmers to address memory from a logical point of view



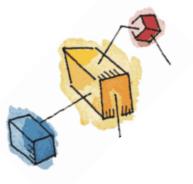




Paging

- Allows process to be comprised of a number of fixed-size blocks, called pages
- Virtual address is a page number and an offset within the page
- Each page may be located anywhere in main memory
- Real address or physical address is the main memory address



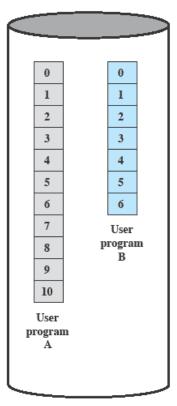


Virtual Memory

A.1			
	A.0	A.2	
	A.5		
B.0	B.1	B.2	B.3
		A.7	
	A.9		
		A.8	
	B.5	B.6	

Main Memory

Main memory consists of a number of fixed-length frames, each equal to the size of a page. For a program to execute, some or all of its pages must be in main memory.



Disk

Secondary memory (disk) can hold many fixed-length pages. A user program consists of some number of pages. Pages for all programs plus the operating system are on disk, as are files.



Figure 2.9 Virtual Memory Concepts

Virtual Memory Addressing

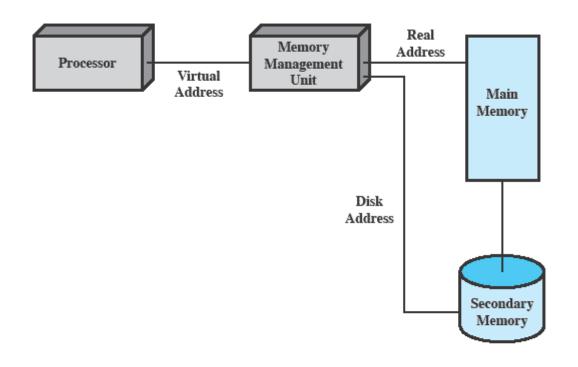


Figure 2.10 Virtual Memory Addressing





Information Protection and Security

- Availability
 - Concerned with protecting the system against interruption
- Confidentiality
 - Assuring that users cannot read data for which access is unauthorized





Information Protection and Security

- Data integrity
 - Protection of data from unauthorized modification
- Authenticity
 - Concerned with the proper verification of the identity of users and the validity of messages or data





Scheduling and Resource Management

- Fairness
 - Give equal and fair access to resources
- Differential responsiveness
 - Discriminate among different classes of jobs





Scheduling and Resource Management

- Efficiency
 - Maximize throughput, minimize response time, and accommodate as many uses as possible





ey Elements of an Operating System

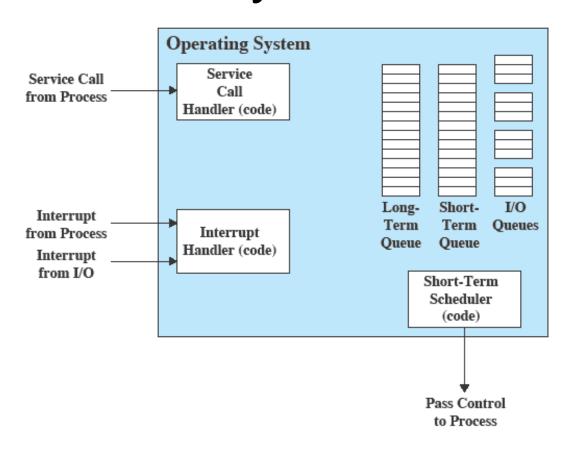
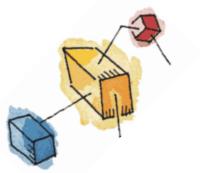


Figure 2.11 Key Elements of an Operating System for Multiprogramming





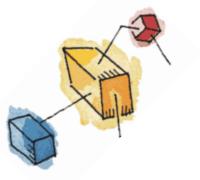


System Structure

- View the system as a series of levels
- Each level performs a related subset of functions
- Each level relies on the next lower level to perform more primitive functions
- This decomposes a problem into a number of more manageable subproblems







Levels

- Level 1
 - Electronic circuits
 - Objects are registers, memory cells, and logic gates
 - Operations are clearing a register or reading a memory location
- Level 2
 - Processor's instruction set
 - Operations such as add, subtract, load, and store

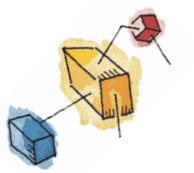


Levels

- Level 3
 - Adds the concept of a procedure or subroutine, plus call/return operations
- Level 4
 - Interrupts





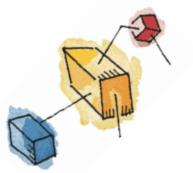


Concepts Related to Multiprogramming

- Level 5
 - Process as a program in execution
 - Suspend and resume processes
- Level 6
 - Secondary storage devices
 - Transfer of blocks of data







Concepts Related to Multiprogramming

- Level 7
 - Creates logical address space for processes
 - Organizes virtual address space into blocks





Deal with External Objects

- Level 8
 - Communication of information and messages between processes
- Level 9
 - Supports long-term storage of named files
- Level 10
 - Provides access to external devices using standardized interfaces

Deal with External Objects

- Level 11
 - Responsible for maintaining the association between the external and internal identifiers
- Level 12
 - Provides full-featured facility for the support of processes
- Level 13
 - Provides an interface to the OS for the user





- Microkernel architecture
 - Assigns only a few essential functions to the kernel
 - Address spaces
 - Interprocess communication (IPC)
 - Basic scheduling





- Multithreading
 - Process is divided into threads that can run concurrently
 - Thread
 - Dispatchable unit of work
 - executes sequentially and is interruptable
 - Process is a collection of one or more threads

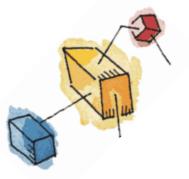




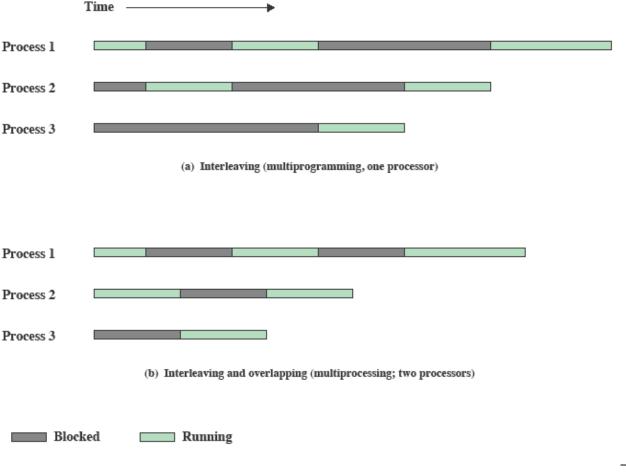
- Symmetric multiprocessing (SMP)
 - There are multiple processors
 - These processors share same main memory and I/O facilities
 - All processors can perform the same functions







Multiprogramming and Multiprocessing







- Distributed operating systems
 - Provides the illusion of a single main memory space and single secondary memory space





- Object-oriented design
 - Used for adding modular extensions to a small kernel
 - Enables programmers to customize an operating system without disrupting system integrity



