### GLASGOW COLLEGE UESTC

### **Electronic Devices (UESTC 3002)**

Date:Dec.24th,2021 Time: 7:00-9:00pm

#### Attempt all PARTS. Total 100 marks

Use one answer sheet for each of the questions in this exam.

Show all work on the answer sheet.

For Multiple Choice Questions, use the dedicated answer sheet provided.

Make sure that your University of Glasgow and UESTC Student Identification Numbers are on all answer sheets.

An electronic calculator may be used provided that it does not allow text storage or display, or graphical display.

All graphs should be clearly labelled and sufficiently large so that all elements are easy to read.

The numbers in square brackets in the right-hand margin indicate the marks allotted to the part of the question against which the mark is shown. These marks are for guidance only.

# FORMULAE SHEET IS PROVIDED AT THE END OF PAPER Fundamental Constants and Useful Material Properties

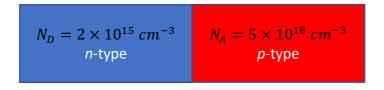
$$q = 1.6 \times 10^{-19} C$$
 $T = 300 K$ 
 $J = 1.38 \times 10^{-23} J K^{-1}$ 
 $\epsilon_0 = 8.85 \times 10^{-12} F m^{-1}$ 
 $h = 6.62 \times 10^{-34} m^2 kg/s$ 
 $Band\ Gap\ Si,\ E(Si) = 1.1\ eV$ 
 $m_0 = 9.1 \times 10^{-31} kg$ 

| Quest                          | ion 1: General Semiconductor Concepts [2  | 25 marks]           |
|--------------------------------|---|---------------------|
|                                | er the following questions:  Complete this sentence using the choices below: The diode  Is the basic building block of all electronic devices.  Has similar characteristics to a simple switch  Is a 2-terminal device  | [1]                 |
| iv.                            | All the above   |                     |
| b)<br>i.<br>ii.<br>iii.<br>iv. | Choose the correct answer: A current ratio of $I_{\mathcal{C}}/I_{\mathcal{E}}$ is usually: Less than unity greater than unity equal to unity equal to zero   | [1]                 |
| i.<br>ii.<br>iii.              | Complete this sentence: Diffused impurities with valence electron called donor atoms.  5 3 4  | ns are<br>[1]       |
| iv.                            | 0   |                     |
| i.                             | Complete this sentence: In the atomic lattice, the and formucleus.  Electrons, neutrons Protons, neutrons Electrons, protons All the above  | n the<br>[2]        |
| e)                             | What does a high resistance reading in the reversed bias region of a Bipola Transistor (BJT) indicate?  | r Junction<br>[1]   |
| f)                             | What happens to a semiconductor's carrier mobility when the doping conce increased?   | entration is<br>[1] |
| g)                             | At what angles to normal incidence is the output power of a solar cell greate lowest?   | est and<br>[2]      |
| h)                             | A particular 1 $cm^2$ solar cell produces 40 W when placed under a sun conce X1000. What is the efficiency of this solar cell, assuming its Fill Factor is un   |                     |
| i)                             | Calculate the gain of a transistor having $I_C = 10 \ mA$ and $I_B = 100 \ \mu A$ .   | [2]                 |
| j)                             | A transistor has a gain of 100 and a base current, $I_B$ , of 50 $\mu A$ . Calculate the current, $I_E$ .   | e emitter<br>[2]    |
| k)                             | An n-type silicon of 1-micron length and cross-sectional area of $100~\mu m^2$ h of 2 V applied across it. If $n_i=1\times 10^{10}~cm^{-3}$ , $N_d=1\times 10^{16}~cm^{-3}$ $1500~cm^2V^{-1}s^{-1}$ , find the following: i) Electron drift velocity. ii) Time it takes for an electron to cross the 1 $\mu m$ length. iii) Drift current density due to electrons. iv) Drift current due to electrons. |                     |

I) Find  $D_n$  assuming the n-type silicon is at room temperature and  $\mu_n=1500~cm^2V^{-1}s^{-1}$  [2]

#### Question 2: PN-Junction Diode

[25 marks]



- a) Compare and contrast the energy band diagrams of this pn-junction diode in the forward and reverse bias regions, making sure to label all aspects of your schematic diagrams.
- b) Calculate the barrier potential of this diode, assuming  $n_i=10^{10}cm^{-3}$  . [2]
- c) Calculate the carrier concentration, p, on the n-side. [2]
- d) Determine the depletion layer width when  $\epsilon_r = 11.5$ :
- i) Reverse bias voltage of 4 V is applied. [2]
- ii) Forward bias voltage of 0.5 V is applied. [2]
- e) Explain the similarities and differences between a *pn*-junction diode and a single junction crystalline silicon solar cell. [4]
- f) Draw a schematic diagram of a basic solar cell device, making sure to indicate the direction of incident light. [2]
- g) A PV cell engineer fabricates a  $100 \times 100 \ mm^2$  solar cell that produces a 1.5 W output when illuminated with a 10 W light source. The designer measures a short circuit current of 3.5 A and an open circuit voltage of 0.61V. What is the efficiency and fill factor of this cell?
- h) How would you design a more efficient solar cell device? [3]

Question 3 – BJTs [25 Marks]

- a) Discuss the typical characteristics of a Bipolar Junction Transistor (BJT). In your answer, make sure to explain the BJT's terminals with BJT symbols. Please also discuss which type of BJT is widely used and why? You may draw sketches to illustrate your answer. [9]
- b) In a common base BJT connection,  $I_E = 1$  mA,  $I_C = 0.95$  mA. Calculate the value of  $I_B$ .
- c) Define cut-off, active and saturation regions in a BJT? [6]
- d) If the collector current changes from 2 mA to 3 mA in a transistor when collectoremitter voltage is increased from 2 V to 10 V, what is the output resistance? [4]

e) What is  $\tau_{FB}$  if W<sub>B</sub> = 70 nm and D<sub>B</sub> = 10 cm<sup>2</sup>/s? Discuss the significance of your answer relative to the propagation of other electromagnetic waves. [4]

## **Question 4 – Field Effect Transistors**

# [25 Marks]

| a) | Draw the transfer characteristic for n-channel depletion type MOSFET?                                 | [3]           |
|----|---|---------------|
| b) | Which JFET parameter(s) influence the size of the depletion region in the device. explain your answer | Please<br>[2] |
| c) | Compare and contrast "Normally ON" and "Normally OFF" MOSFETs. Why are t termed this way?             | hey<br>[5]    |
| d) | Define the amplification factor in a JFET?  | [5]           |
| e) | Compare the characteristics of JFET and MOSFET.   | [6]           |
| f) | Why is the FET called a "voltage operated device"?  | [2]           |
| g) | What do you understand by the term "pinch-off voltage"?   | [2]           |

#### **EQUATION SHEET**

$$E_s - E_b = hv_{sb} = \frac{mq^4}{8h^2e^2} \left(\frac{1}{b^2} - \frac{1}{a^2}\right)$$

$$np = n_l^2$$

$$I_0 = AA^*T^2 \exp(-(q\phi_B)/kT)$$

$$I_0 = AQ^*T_0$$

$$I_0 = AA^*T^2 \exp(-(q\phi_B)/kT)$$

$$I_0 = AQ^*T_0$$

$$I_0 = AQ^*$$

$$I_{DS} = K(V_{GS} - V_{th})^2 (1 + \lambda V_{DS})$$
  
for  $V_{GS} > V_T$ ,  $V_{DS} > V_{GS} - V_T$ 

$$A_V = \frac{v_{ds}}{v_{gs}} = -\frac{R_D(g_m v_{gs})}{v_{gs}} = -g_m R_D$$

$$K = \frac{Z\mu_e \epsilon}{2Lt_{ox}}$$

$$x_p = \sqrt{\frac{2\epsilon_o \epsilon_r V_{bi} N_d}{q N_a (N_d + N_a)}}$$

$$N_v = 2\left(\frac{2\pi m_h^* kT}{h^2}\right)^{3/2}$$

$$x_n = \sqrt{\frac{2\epsilon_o \epsilon_r V_{bi} N_a}{q N_d (N_d + N_a)}}$$

$$N_c = 2\left(\frac{2\pi m_e^* kT}{h^2}\right)^{3/2}$$

$$W = \sqrt{\frac{2\epsilon_r \epsilon_o V_{bi} (N_a + N_d)}{q N_d N_a}}$$

$$I_D = I_{DSS} \left( 1 - \frac{V_{GS}}{V_P} \right)^2$$

$$I_{DS} = \frac{\mu C_{ox} Z}{L} \left[ (V_{GS} - V_T) V_{DS} - \frac{V_{DS}^2}{2} \right] (1 + \lambda V_{DS})$$
for  $V_{GS} > V_T$ ,  $V_{DS} \le V_{GS} - V_T$