



Design example: Figures of merit

Often many numbers contribute:

Define a Figure of merit for the component

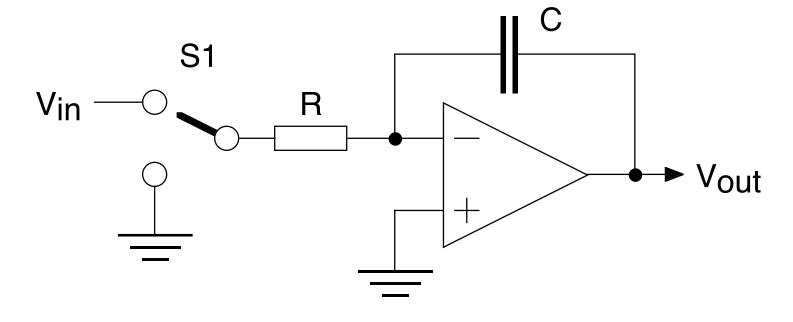
Figure of merit is a function of all significant contributions of the component to the total system error.

Figure of merit changes **monotonically** with the overall error due to the component

The total error contributed by the component is clearly a good choice



Example: Integrate-and-Hold



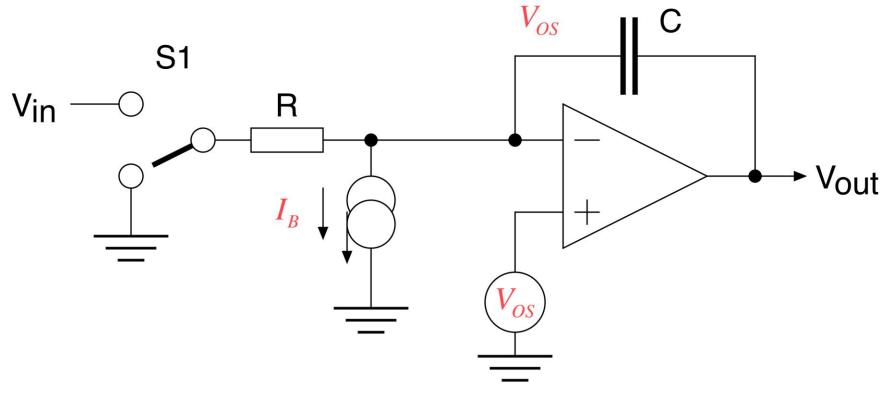
If S1 connects to V_{in} circuit is an integrator $V_{out} = -\hat{0} \frac{V_{in}}{RC} dt$

If S1 connects to 0V circuit is a memory $V_{out} = constant$





Important parameter is drift during "Memory"



$$\left| \frac{dV_{\text{out}}}{dt} \right| = \frac{1}{C} \frac{dQ}{dt} = \frac{1}{C} \overset{\text{@}}{\in} I_B + \frac{V_{OS} \overset{\text{"O}}{\circ}}{R} \overset{\text{"O}}{\circ}$$



Error Analysis (2)

Time constant is fixed: $\tau = RC$: one free parameter (R)

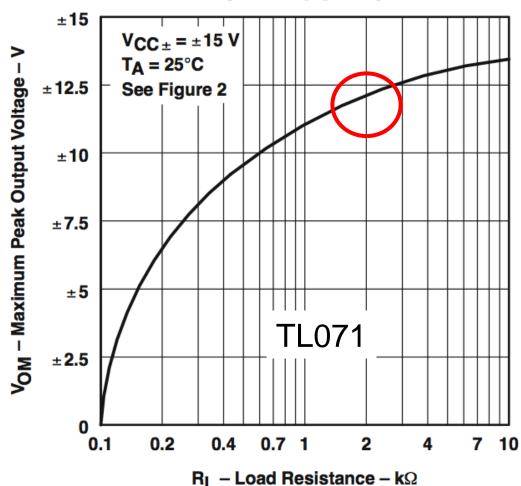
$$\left| \frac{dV_{\text{out}}}{dt} \right| = \frac{1}{C} \left(I_B + \frac{V_{OS}}{R} \right) = \frac{1}{\tau} \left(I_B R + V_{OS} \right)$$

Optimisation: make R as small as possible



Practical limits on R:

MAXIMUM PEAK OUTPUT VOLTAGE vs LOAD RESISTANCE



If R too small, Input Voltage range is too small

Also:

- Supply current
- Power dissipation
- Thermal drift
- V across PCB
- Available C values

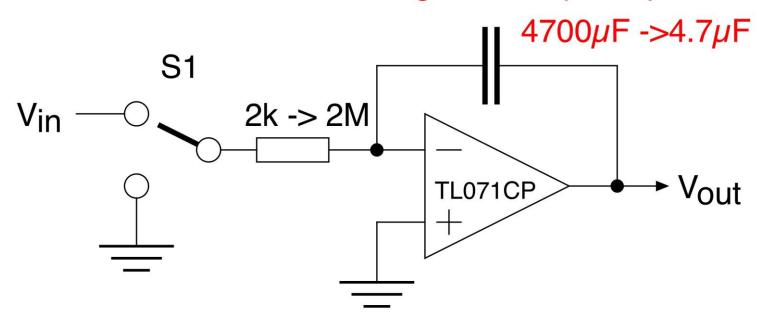
R≥2kΩ



Error Analysis (4)

Naïve circuit (τ=10s)

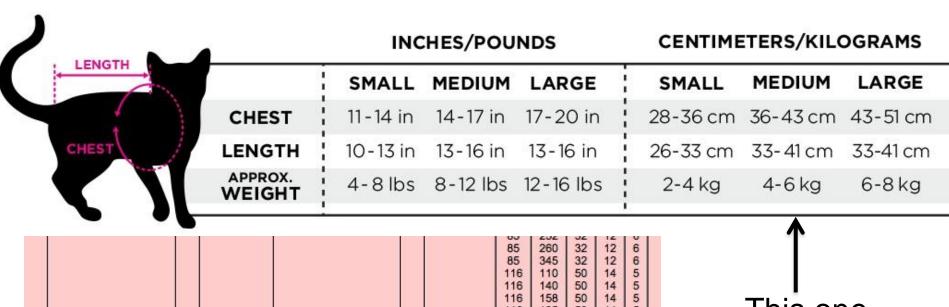
No good 4700μ F capacitors



$$\left| \frac{dV_{\text{out}}}{dt} \right| = \frac{1}{\tau} (I_B R + V_{OS}) = \frac{1}{10s} \cdot (200 \, pA \cdot 2M\Omega + 10mV) = 1.04 \, mV \cdot s^{-1}$$



How large is a 4700µF Polypropylene Capacitor?



32 50 50 50 50 50 50 50 116 185 116 215 116 230 255 116 50 116 116 Dims, in mm. ◍

(Wima DC-LINK MKP 6 HP 4920µF 600V)

This one

This one

Electrolytic Capacitors No!!! Relatively small Low quality Poor tolerance Temperature sensitive Not ideal for precision circuits!

