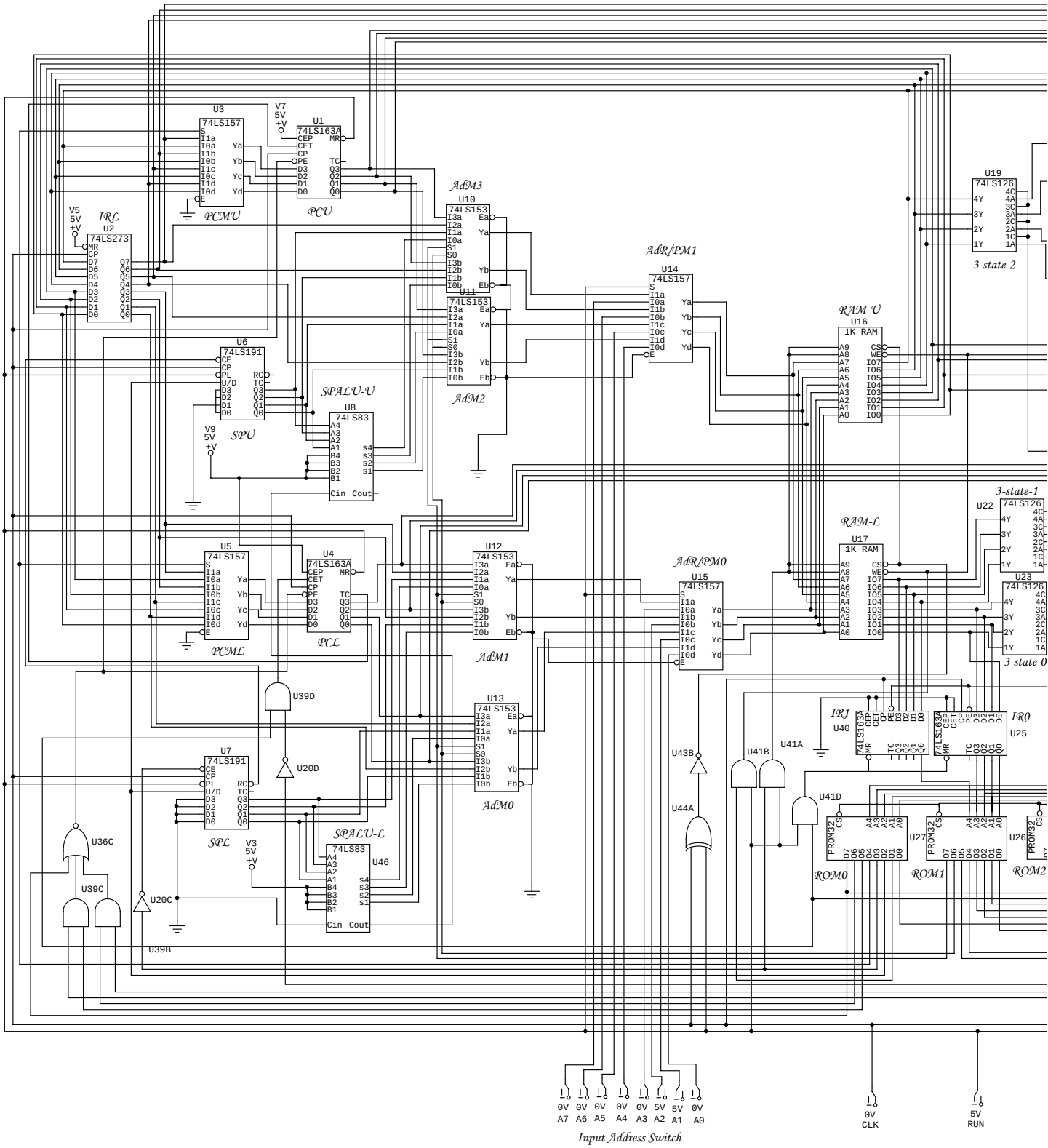
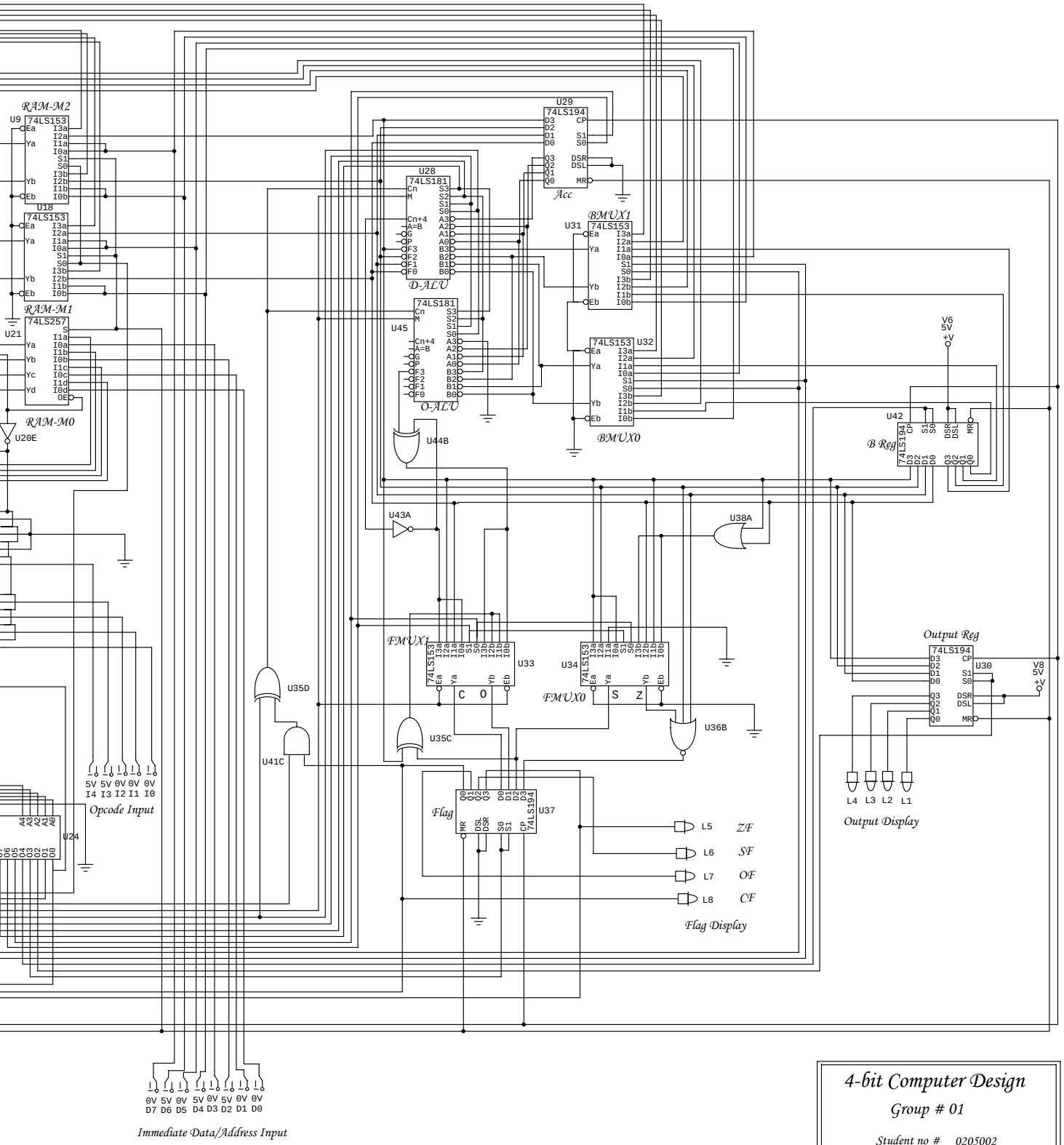

U3
74LS157
PCMU
U1
74LS163A
PCU
V7
5V
+V
V5
5V
+V
IRL
U2
74LS273
AdM3
U10
74LS153
U11
74LS153
AdM2
AdR/PM1
U14
74LS157
U19
74LS126
3-state-2
RAM-U
U16
1K RAM
U6
74LS191
SPU
SPALU-U
U8
74LS83
V9
5V
+V
U5
74LS157
PCML
U4
74LS163A
PCL
U12
74LS153
AdM1
AdR/PM0
U15
74LS157
RAM-L
U17
1K RAM
U22
74LS126
3-state-1
U23
74LS126
3-state-0
U13
74LS153
AdM0
IR1
U40
74LS163A
IR0
U25
74LS163A
U39D
U20D
U7
74LS191
SPL
U36C
U39C
U20C
U39B
V3
5V
+V
SPALU-L
74LS83
U46
U43B
U44A
U41B
U41A
U41D
PROM32
U27
ROM0
PROM32
U21
ROM1
PROM32
U26
ROM2
0V
A7
0V
A6
0V
A5
0V
A4
0V
A3
5V
A2
5V
A1
0V
A0
Input Address Switch
0V
CLK
5V
RUN



4-bit Computer Design

Group # 01

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