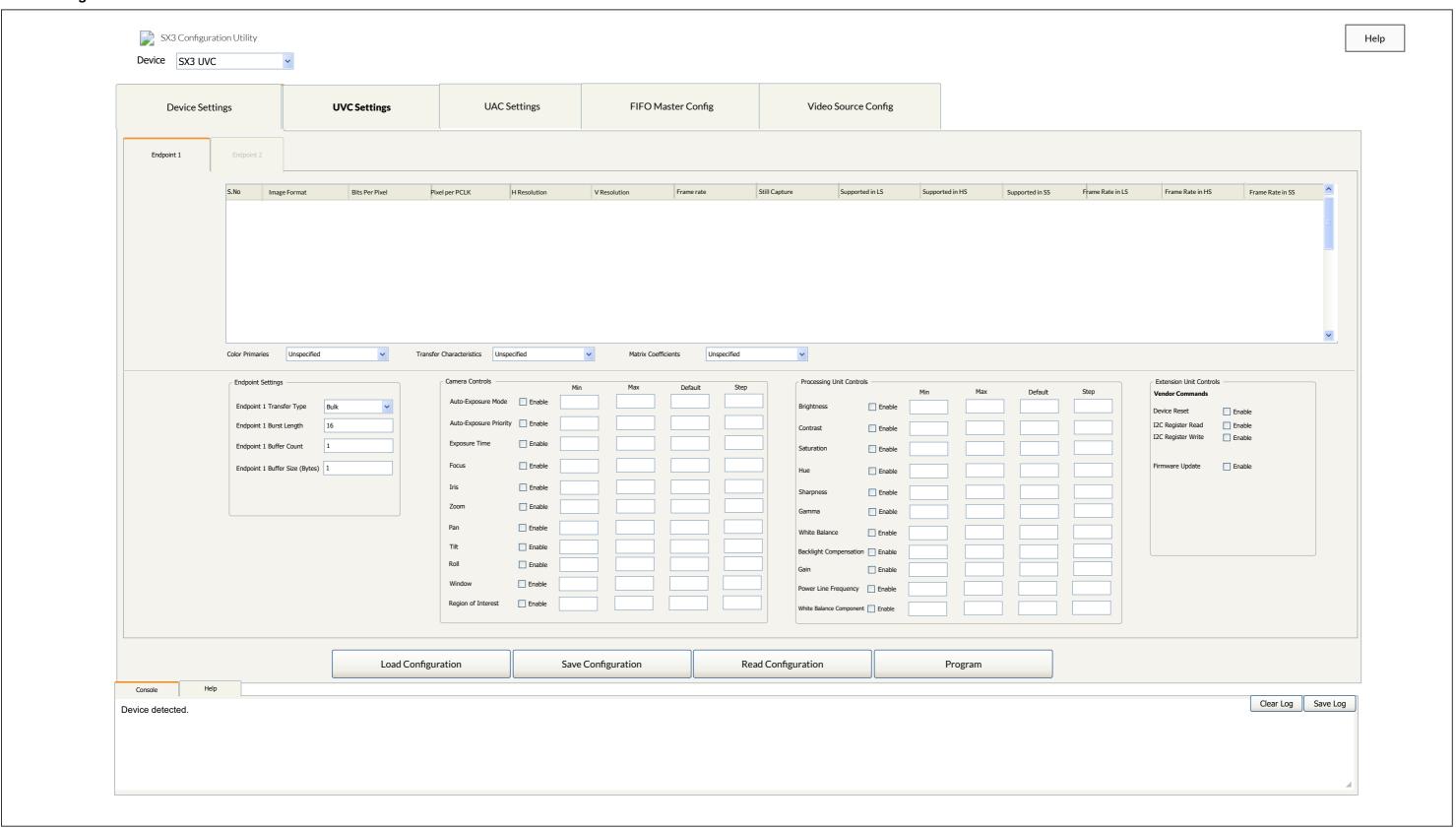
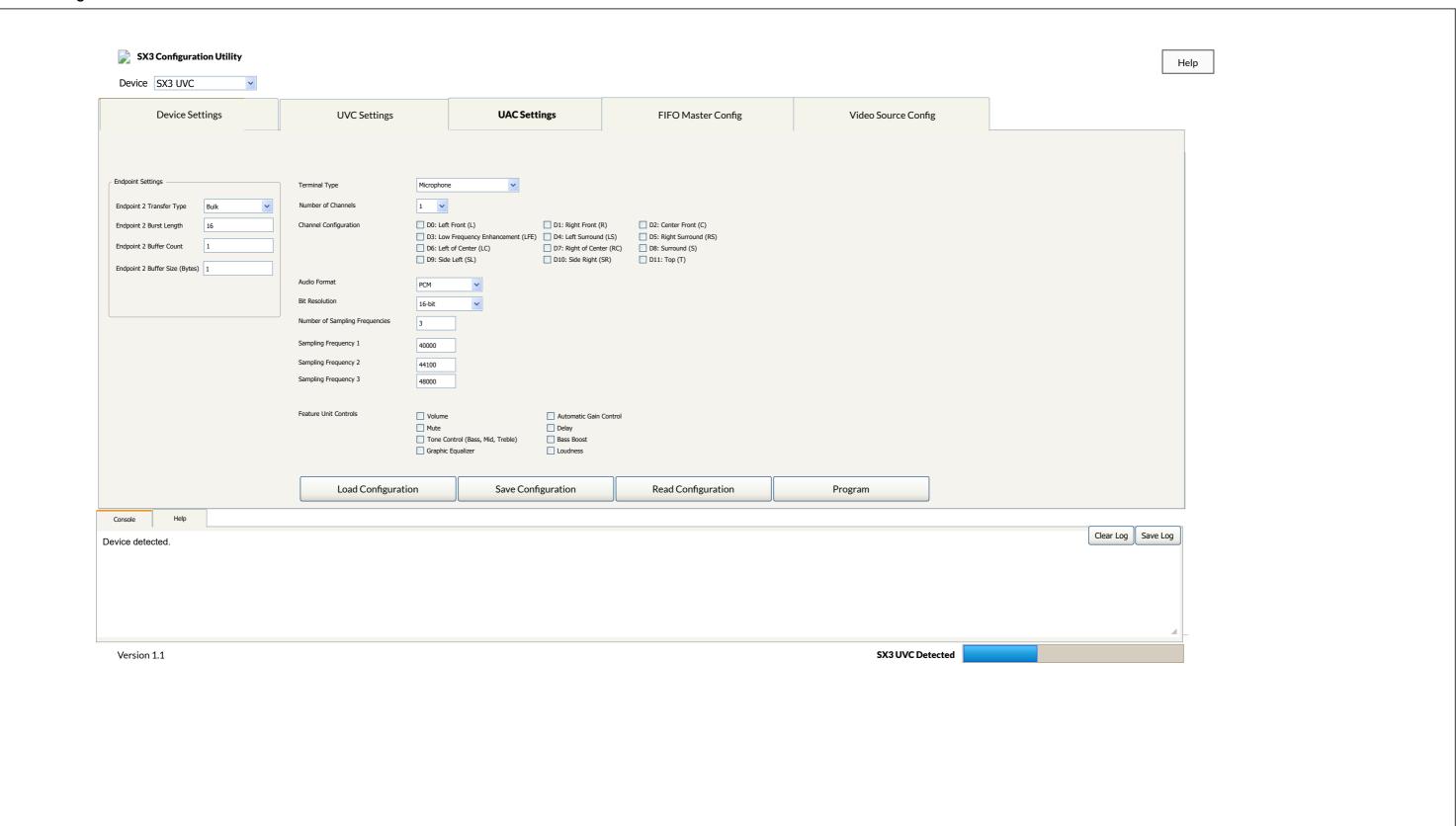


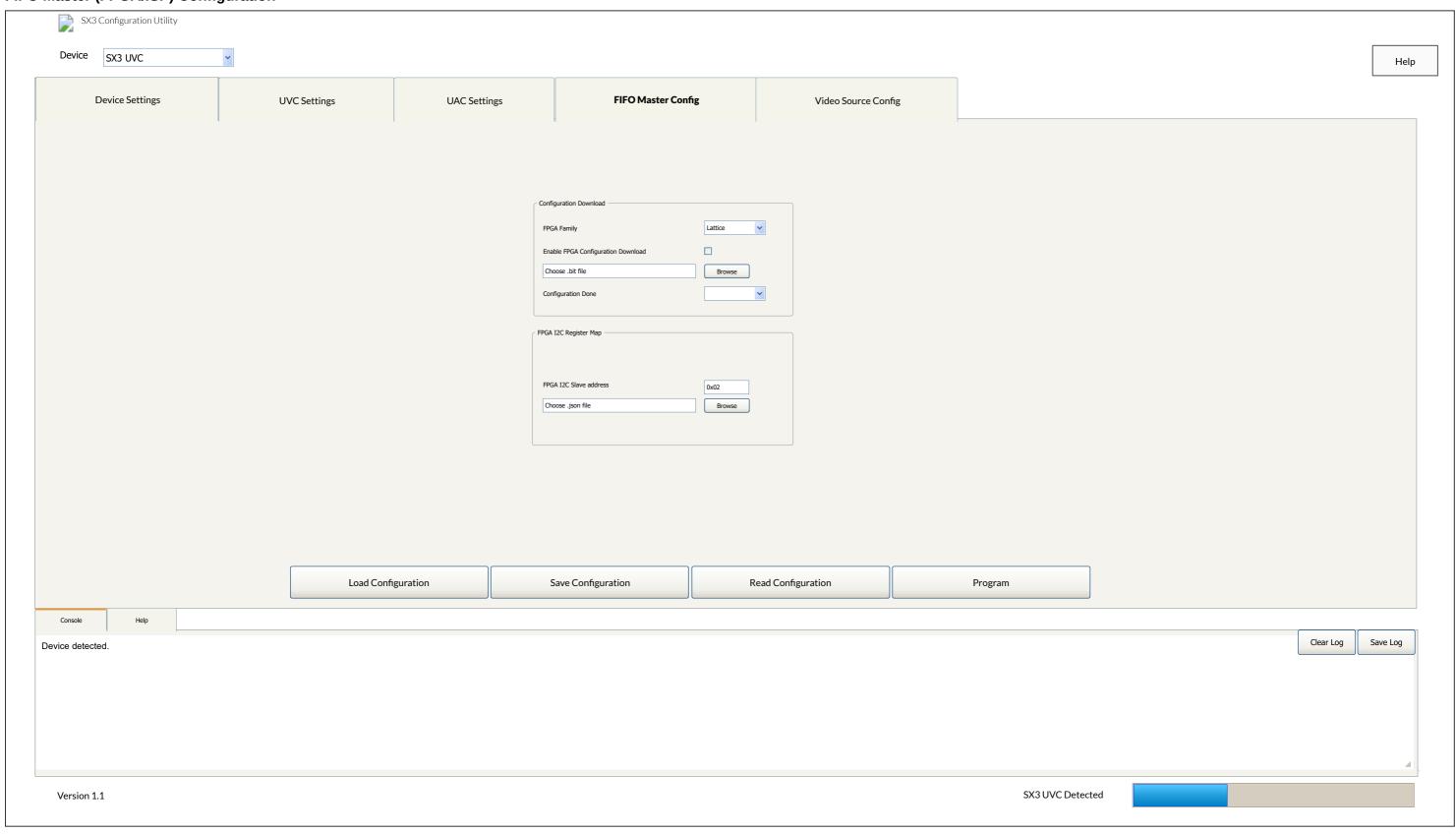
## **UVC Settings**



## **UAC Settings**



## FIFO Master (FPGA/ISP) Configuration



## **Video Source Configuration**

