Lab 3: Interrupt And Exception

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- 1. AArch64 Exception Handling
 - 1.1
- 2. Homework
 - 2.1 What happens when an interrupt occur
 - 2.2 Trap Frame
 - 2.3 Store and Reload Registers

1. AArch64 Exception Handling

I have briefly introduced the exception handling of AArch64 in my report of lab1. This section will further discuss upon this topic, based on ARMv8-A Programmer Guide.

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