

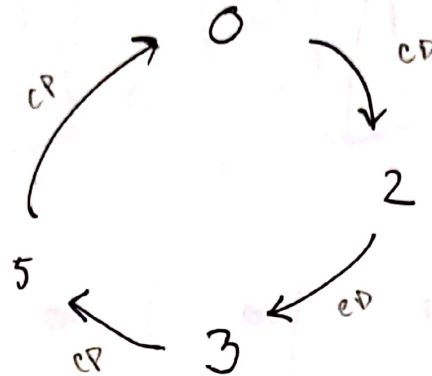
1805006

CSE-201

Online

27.06.2021

State Diagram:



①

Transition Table + ~~State~~ Table

y_2	y_1	y_0	Y_2	Y_1	Y_0
0	0	0	0	1	0
0	1	0	0	1	1
0	1	1	1	0	1
1	0	1	0	0	0

Same as

D_2, D_1, D_0

3x

(Using D FFs)

~~Logic~~

K Maps :- D_2 :

y_2	y_1, y_0			
	00	01	11	10
0	0	-	1	0
1	-	0	-	-

$D_2 = y_1 y_0$

(2)

$D_1:$

	y_1, y_0	00	01	11	10
y_2	0	1	-	0	1
	1	-	0	-	-

$D_1 = \overline{y_0}$

$D_0:$

	y_1, y_0	00	01	11	10
y_2	0	0	-	1	1
	1	-	0	-	-

$D_0 = y_1$

Circuit Diagram:

