## CSE 341 Microprocessor

## Fall 2022

| Quiz 4   | Full Marks - 15                        |
|--|--|
| ANSWER ALL THE QUESTIONS   | Time: 30 mins                          |
| Name   | ID                                     |
| Section  |  |
|  |  |
| [CO4] 1. <b>Describe</b> the role of the Interrupt Service Ro<br>[IMR], and Interrupt Request Register. [6 marks]  | egister [ISR], Interrupt Mask Register |
| <ul> <li>[C04] 2. Suppose, you need to service 24 had microprocessor.</li> <li>a. State the name of the process to connect muter [1 mark]</li> <li>b. Explain how PICs have connected to the material control of PICs required.</li> </ul> | ultiple PICs with the microprocessor.  |
| [C05] 3. <b>Illustrate</b> the way DMA operates. You may o   | draw a diagram if you need. [4 marks]  |