

CSE 341 Microprocessor

Fall 2022

Make Up Quiz

Full Marks - 15

ANSWER ALL THE QUESTIONS

Time: 30 mins

Name_____ ID_____

Section _____

Question 1

[C04]

Suppose, the Interrupt Vector (IV) of an Interrupt Service Routine (ISR) is 12E5Ch whose segment address is stored at memory locations 0008Eh and 0008Fh. If the segment address of this IV is 1234h, then, answer the following questions:

- A. Calculate the value at memory location 0008Dh [6]
- B. Calculate the Interrupt type responsible for the above ISR [2]

Question 2

[C04] Suppose an 8086 microprocessor is serving interrupt requested via IR3 of PIC. Now there is a request via IR2 while IR 3 is being serviced. Explain how PIC deals with this. You should include in your answer the value of Interrupt Service Register [ISR] and Interrupt Request Register bit value [IRR]. [5 marks]