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Section: 12

Course: CSE340

Assignment: 02

Ans: to the que no: 01

Given

add : 20%

addi : 20%

not : 0%

beq : 25%

lw : 25%

sw : 10%

Memory Access: lw and sw

So, sw and lw will use Data Memory.

∴ Fraction of all cycles is the data

$$\begin{aligned}\text{memory used} &= \text{lw} + \text{sw} \\ &= 25\% + 10\% \\ &= 35\%\end{aligned}$$

A

Sub: _____

Day

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Time: _____

Date: / /

Ans: to the que no: 02

In the input of sign-extender circuit we will need addi, beq, lw, sw.

\therefore fraction of all cycles is the input of the sign-extender circuit needed

$$= \text{addi} + \text{beq} + \text{lw} + \text{sw}$$

$$= 20\% + 25\% + \text{lw} + \text{sw}$$

$$= 20\% + 25\% + 25\% + 10\%$$

$$= 80\%$$

Ans: to the que no: 03

Using Stall

1. $\text{lw } \$t0, 36(\$t1) \dots$



2. $\text{lw } \$t2, 40(\$t0) \dots$



3. $\text{lw } \$t3, 44(\$t2) \dots$

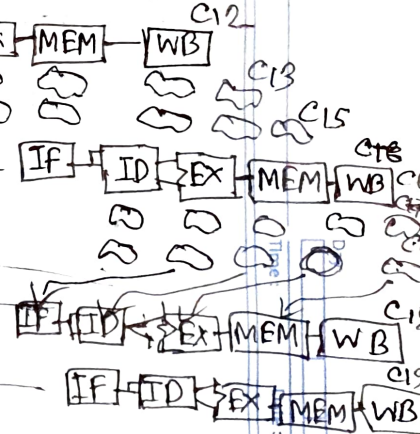
4. $\text{sl } \$t3, \$t2, 2 \dots$



5. $\text{sub } \$t0, \$t3, \$t2 \dots$

6. $\text{addi } \$t0, \$t0, 2 \dots$

7. $\text{srli } \$t0, \$t3, 2 \dots$

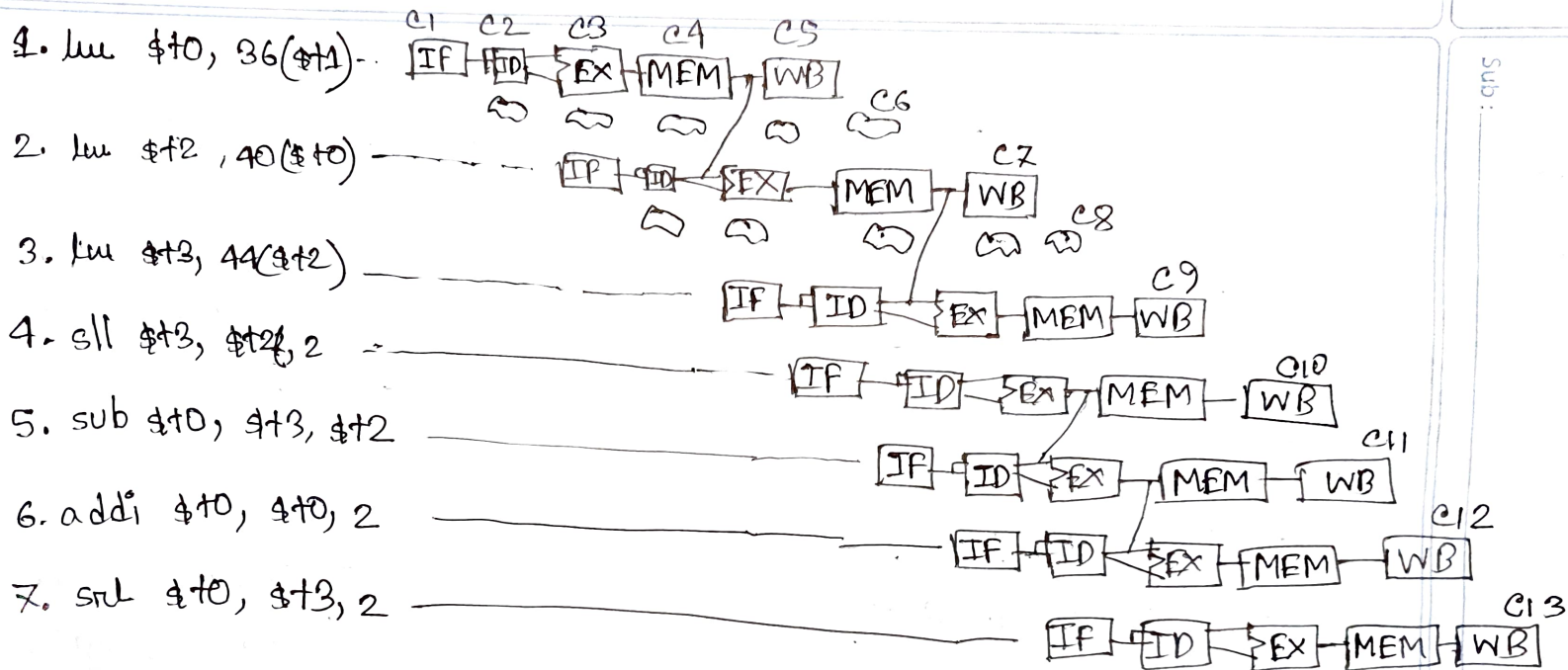


\therefore Here, we see we need 19 clock cycles.

$$\text{CPI} = \frac{19}{7} = 2.714$$

Ans

Ans: to the que no: 04



13 Clock cycles

$$CPI = \frac{13}{7} = 1.857$$



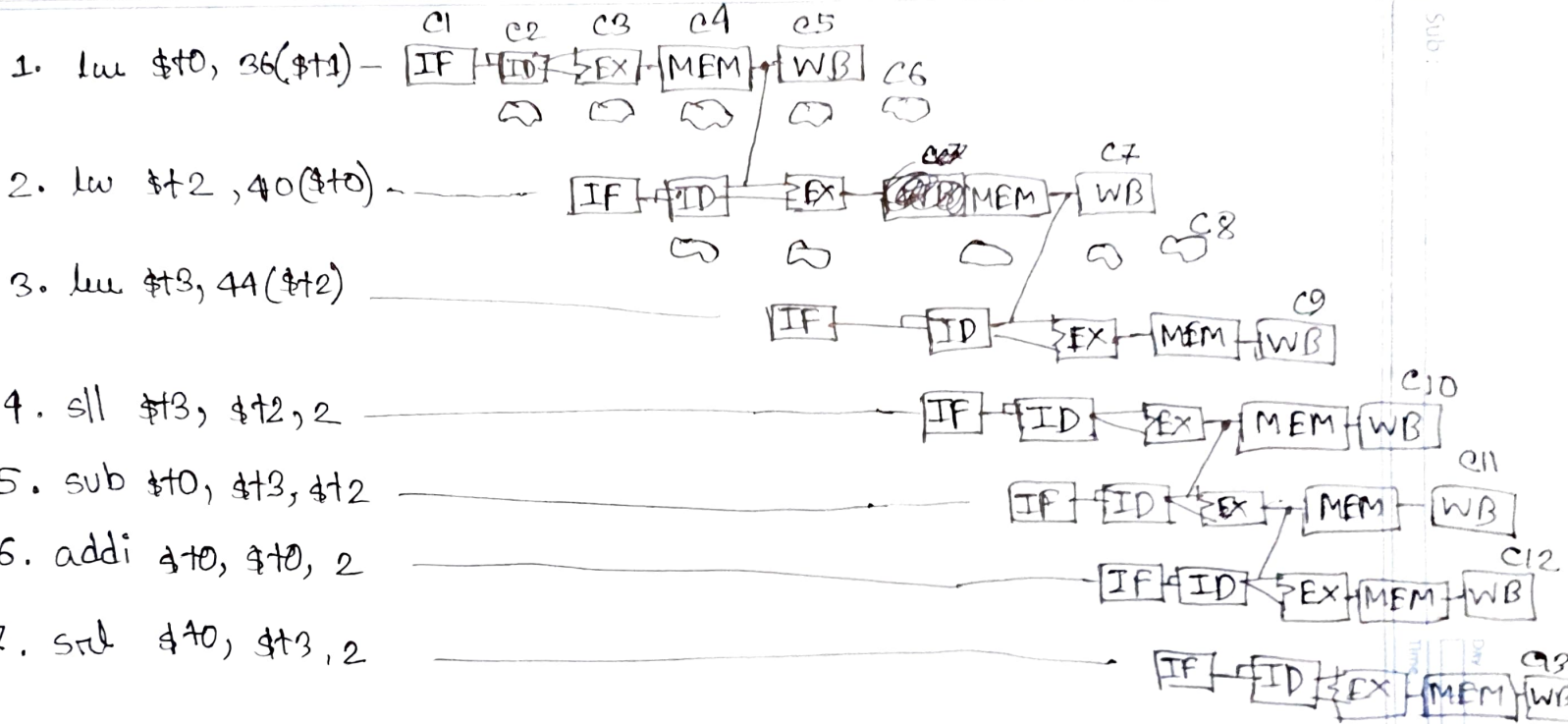
Sub:

Day

Time:

Date: / /

Ans: to the que no: 5



Here, we can see that scheduling is impossible to apply as it will change the sequence of the code. Therefore, only stall and forwarding is done. (Clock cycle 13)

$$\therefore CPI = \frac{13}{7} = 1.857$$