

5 12 8 9
opcode reg-A 0 1 2 3 4 5 6 7 8 9

SM reg-A 0 1 2 3 4 5 6 7 8 9

stores registers in addresses (consecutive)
starting from "reg-A", if their corresponding
bit is set high.

S0
R7 → IMEMA
IMEMB → IR

S24
IR11-9 → RF-A1
RF-D1 → T1
IR7 → SE116 → T2

S25
T1 → ALUA
T2 → ALUB, DMEM-A
ALUC → T3
IR2-0 → BA7 → Dec → RFA1
RF-D1 → Dmem DID

S26
IR6 → SE116 → T1
T3 → T2

S27
T1 → ALUA
T2 → ALUB, DMEMA
ALUC → T3
IR7-0 → BA6 → Dec → RFA1
RF-D1 → Dmem DID

S28
IR5 → SE116 → T1
T3 → T2

S29
T1 → ALUA
T2 → ALUB, DMEMA
ALUC → T3
IR7-0 → BA5 → Dec → RFA1
RF-D1 → Dmem DID

S30
IR4 → SE116 → T1
T3 → T2

S31

$T_1 \rightarrow ALWA$ $T_2 \rightarrow ALUB, D\Delta EMA$ $ALUC \rightarrow T_3$ $IR_{7-0} \rightarrow BA_4 \rightarrow Dec \rightarrow RFAI$ $RFDI \rightarrow 2 \Delta mem DI$	ADD S31 S32
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S32

$IR_3 \rightarrow SE116 \rightarrow T_1$ $T_3 \rightarrow T_2$	- S32 S33
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S33

$T_1 \rightarrow ALWA$ $T_2 \rightarrow ALUB, D\Delta EMA$ $ALUC \rightarrow T_3$ $IR_{7-0} \rightarrow BA_3 \rightarrow Dec \rightarrow RFAI$ $RFDI \rightarrow 2 \Delta mem DI$	ADD S33 S34
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S34

$IR_2 \rightarrow SE116 \rightarrow T_1$ $T_3 \rightarrow T_2$	- S34 S35
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S35

$T_1 \rightarrow ALWA$ $T_2 \rightarrow ALUB, D\Delta EMA$ $ALUC \rightarrow T_3$ $IR_{7-0} \rightarrow BA_2 \rightarrow Dec \rightarrow RFAI$ $RFDI \rightarrow 2 \Delta mem DI$	ADD S35 S36
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S36

$IR_1 \rightarrow SE116 \rightarrow T_1$ $T_3 \rightarrow T_2$	- S36 S37
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S37

$T_1 \rightarrow ALWA$ $T_2 \rightarrow ALUB, D\Delta EMA$ $ALUC \rightarrow T_3$ $IR_{7-0} \rightarrow BA_1 \rightarrow Dec \rightarrow RFAI$ $RFDI \rightarrow 2 \Delta mem DI$	ADD S37 S38
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S38

$IR_0 \rightarrow SE116 \rightarrow T_1$ $T_3 \rightarrow T_2$	- S38 S39
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S39

$T_1 \rightarrow ALWA$ $T_2 \rightarrow ALUB, D\Delta EMA$ $ALUC \rightarrow T_3$ $IR_{7-0} \rightarrow BA_0 \rightarrow Dec \rightarrow RFAI$ $RFDI \leftarrow \Delta mem DI$	ADD S39 S40
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