INTRODUCTION TO MICROCONTROLLERS





BINARY CELL

- A binary cell is a memory element that can store one bit of information.
- Binary cell storing logic 1:

1

Binary cell storing logic 0:



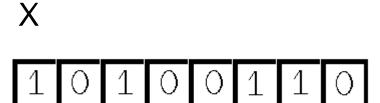


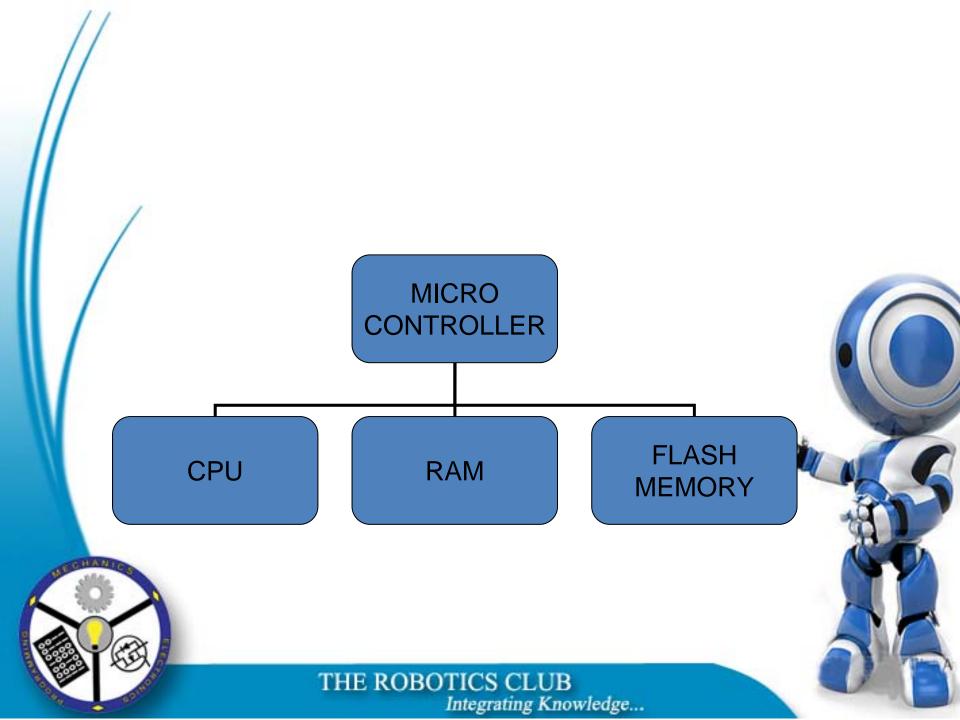


REGISTER

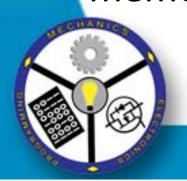
- A group of binary cells is called a Register.
- Every register is characterized by:
 - 1. Name of the register.
 - 2. The number of binary cells in the register.







- The CPU is interfaced to the RAM and Flash memory.
- We feed our program into the flash memory.
- When we turn ON the microcontroller, CPU accesses the instructions from RAM which in turn accesses our code from the Flash memory.



RAM and FLASH MEMORY

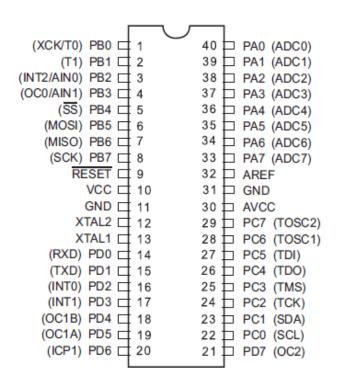
- RAM consists of several registers.
- Configuring the registers of RAM is the core job of the programmer.

 The code that we write is stored in the Flash Memory.



ATmega 16 pin configuration









Integrating Knowledge...

PORT

- A 'port' is a set of 8 I/O pins.
- There are four ports in ATmega 16.
- •/ Hence there are 32 bidirectional pins.
- The four ports are named as: A , B , C and D
- Our first job while writing a program is to configure which ports are input ports and which ports are output ports.

PROGRAMMING

- The program is written in C in AVR Studio 4.
- /The first statement SHOULD be :

#include<avr/io.h>

- Open the main() function.
- Complete the code.
- Close the main() function.
- Compile the program.



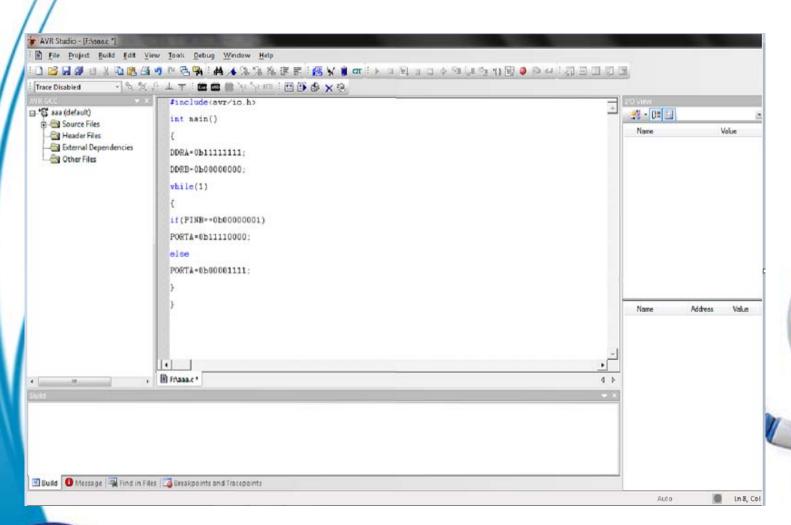


SAMPLE CODE

```
#include<avr/io.h>
int main()
DDRA=0b11111111;
DDRB=0b00000000;
while(1)
if(PINB==0b00000001)
PORTA=0b11110000;
else
PORTA=0b00001111;
```











THE ROBOTICS CLUB

Integrating Knowledge...

SAMPLE CODE WITH COMMENTS



DDR – DATA DIRECTION REGISTER

- There is a data direction register for each port.
- Setting a bit in the DDR register to logic 1 will make the corresponding pin an output pin.
- Setting a bit in the DDR register to logic 0 will make the corresponding pin an input pin.
- To make the first four pins(PA0...3) as input pins and the other four pins(PA4...7) as output pins, the statement should be

DDRA=0b11110000;

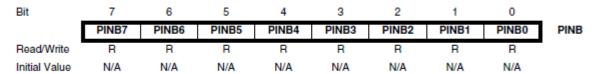
Bit	7	6	5	4	3	2	1	0
	DDA7	DDA6	DDA5	DDA4	DDA3	DDA2	DDA1	DDA0
Read/Write	R/W							
Initial Value	0	0	0	0	0	0	0	0



PIN REGISTER

There is a PIN register for every port.

 If the port is configured as an input port, the inputs at each pin of the port are stored in the corresponding bits of the PIN register.





PORT REGISTER

• There is a PORT register for every port.

 If the port is configured as an output port, setting the bits of the PORT register to logic 1 or logic 0 would set the corresponding pins to logic 1 or logic 0.

To put logic 1 in the first six pins and logic 0 in the next two pins of port A, the statement should be

PORTA=0b00111111;



Bit	7	6	5	4	3	2	1	0	_
	PORTA7	PORTA6	PORTA5	PORTA4	PORTA3	PORTA2	PORTA1	PORTA0	PORTA
Read/Write	R/W								
Initial Value	0	0	0	0	0	0	0	0	

- By using the three registers
 - DDR
 - PIN
 - PORT

we can write the code for basic bots like line follower, obstacle avoider etc using simple if else statements.

The more number of registers you know, the more complex applications you can make.



GOING FURTHER

 For more information on various other registers of ATmega 16, download its datasheet (pdf) from

www.atmel.com

Visit

www.extremeelectronics.co.in

www.engineersgarage.com



