

[illegible]

## LDO VOLTAGE REGULATOR

## BATTERY CONNECTOR

J2  
B2B-PH-5M4-TB

2 VBAT  
1 GND

[illegible]

A diagram showing a purple rectangular block representing a device. On its right edge, three pins are labeled: USB\_DN, USB\_DP, and VBUS. Each label is connected to a green line that extends to the right, where the same label is repeated: USB\_DN, USB\_DP, and VBUS.

File: ESP32-C3-02.kicad\_sch

[illegible]

The diagram shows the connection of a W25Q32JVSSIQ SPI flash memory (U5) to a +3.3V supply and GND. The chip is connected via CS, SCLK, and MOSI/DI pins. A pull-up resistor R20 (50 ohms) is connected to the MOSI/DI pin. The chip's VCC and GND pins are connected to the +3.3V supply and GND respectively. A capacitor C14 (0.1uF) is connected to the +3.3V supply.

SENSORS

PHOTO\_C

SDA

SCL

MIC\_OUT

PHOTO\_C

SDA

SCL

MIC\_OUT

File: SENSORS.kicad\_sch

USER INTERFACE

SDA

SCL

GPIO18

GPIO19

GPIO08

EN

BOOT

DTR

RTS

EN

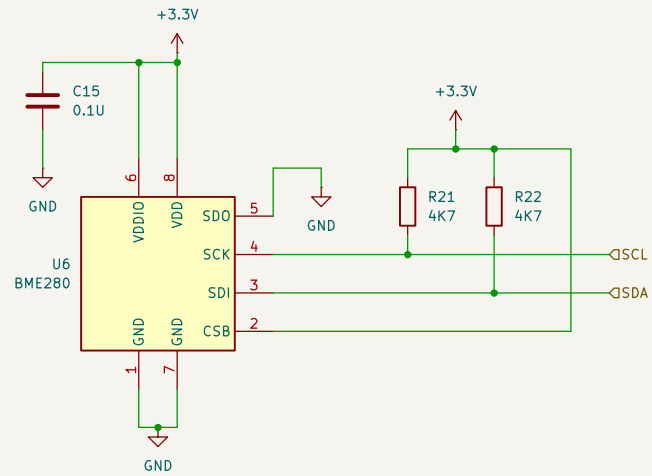
BOOT

DTR

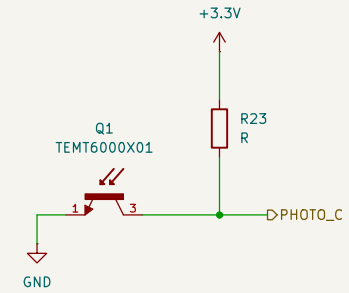
RTS

File: USER\_INTERFACE.kicad\_sch

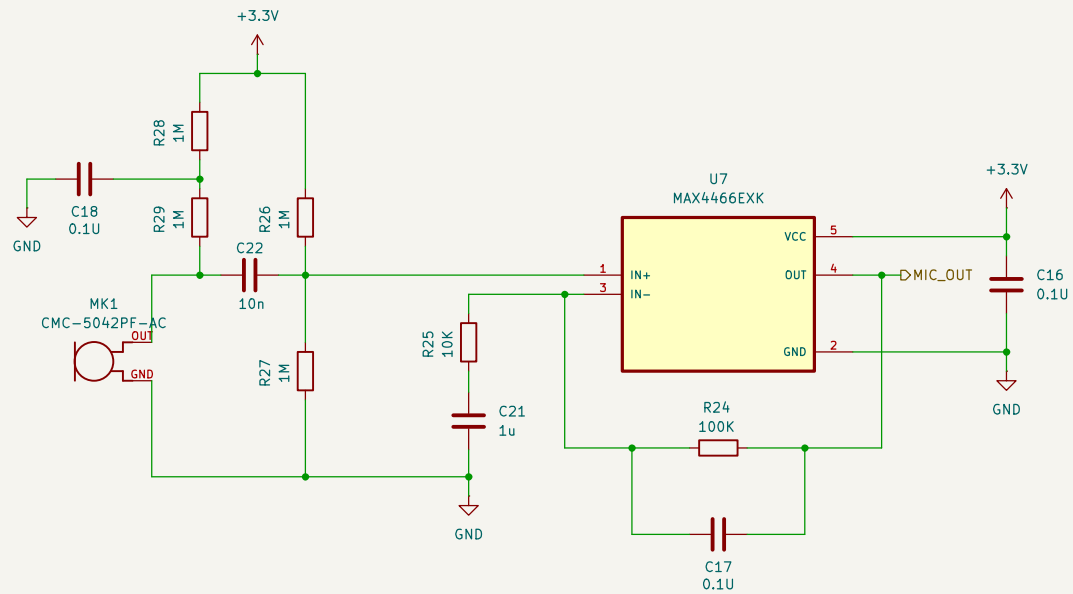
### BME280 TEMPERATURE SENSOR



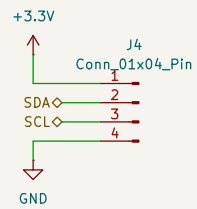
### AMBIENT LIGHT SENSOR



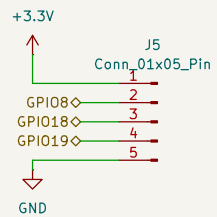
### PREAMPLIFIER AND MICROPHONE



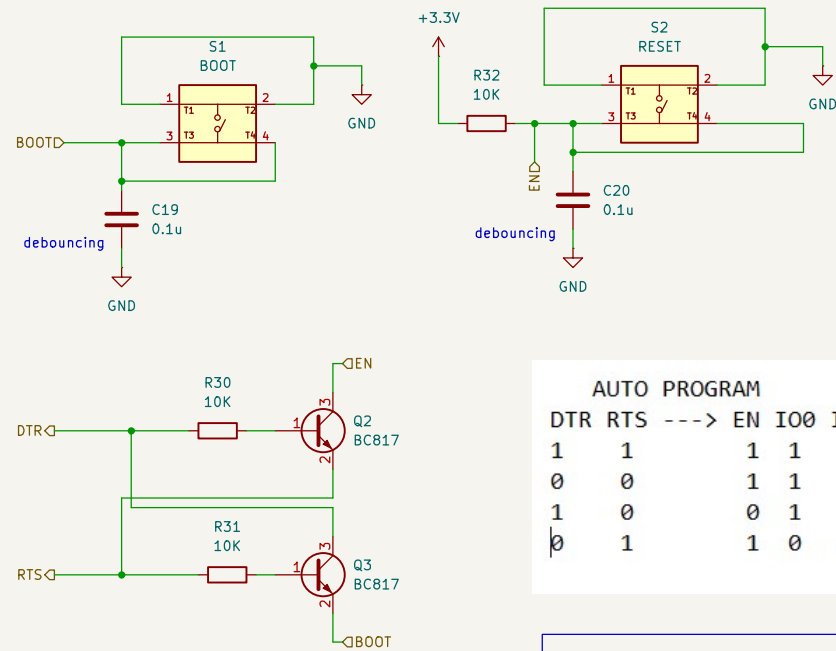
### OLED DISPLAY/I2C BREAKOUT



### GPIO BREAKOUT



### BOOT AND RESET



### AUTO PROGRAM

DTR	RTS	---	EN	IO0	IO2
1	1		1	1	1
0	0		1	1	1
1	0		0	1	1
0	1		1	0	0

more about the boot mode of ESP32  
go to [texplo.re/esp32boot](http://texplo.re/esp32boot)