Question bank for Unit Test2

1. Elaborate six stage CPU instruction pipeline.
2. What is horizontal & vertical micro instruction?
3. Elaborate the Flynn's Classification in detail.
4. Define the following terms microinstruction, micro operation, and micro program.
5. Discuss the floating point representation IEEE standard 754.
6. Problems on Booth’s algorithm, Restoring Division Algorithm
7. Problems on IEEE 754 single and double precision standard of floating point representation.
8. Discuss cache organization in details.
9. What is locality of reference? List and define different types of locality.
10. Categorize the types of memories based on the hierarchy of speed and size.
11. Problems on page replacement algorithm.
12. Discuss the working of:-

(i)DMA

(ii) Programmed I/O

(iii) Interrupt Driven I/O.

1. Discuss I/O processor and I/O channels with diagram.