Rajib Das, M.E.Tel.E

Contact Information	Cooch Behar Govt. Engineering College, West Bengal – 736170 Email: rajibdasece@gmail.com, Phone: 9163309694 www.rajibdasece.wordpress.com
Current Position	Assistant Professor Department of Electronics and Communication Engineering Cooch Behar Government Engineering College October 2018 – Present
Education	**M.E.Tel.E,** Jadavpur University (2013) Thesis title: **Some Studies on Design and FPGA Implementation of Digital Core for Multipurpose UHF RFID Tag.** CGPA: 8.75 **B.Tech,** Electronics and Communication Engineering (2011) **Dr. B.C. Roy Engineering College, Durgapur** CGPA: 7.86 **Higher Secondary** (2006) **West Bengal Council of Higher Secondary Education, West Bengal, India** Percentage: 72.90% **Secondary** **West Bengal Board of Secondary Education, West Bengal, India, 2004** Percentage: 80.62%
Research Interests	➢ Control Systems ➢ Signal Processing ➢ Simulation and Modeling ➢ Internet Of Things (IOT) ➢ Execute Online Test for Competitive Exam
Software & Hardware Attainments	➢ MATLAB ➢ LABVIEW ➢ Moodle ➢ Xilinx ISE ➢ Xilinx FPGA kit ➢ Multisim ➢ Arduino Uno ➢ Raspberry pi
Teaching Experience	**Assistant Professor, SUPREME KNOWLEDGE FOUNDATION GROUP OF INSTITUTIONS,** Mankundu, Hooghly, **712139** 4th July 2013 – 5th October 2018 ➢ Signals & Systems. (EC 303) ➢ Control Systems. (EC 503) ➢ Circuit Theory & Network (EC 301) ➢ Communication Systems (CS 401) ➢ Digital signal Processing (EC 602) ➢ Guiding final year UG students for B.Tech projects

Award & Achievement	➢ Certificate from Paschimbanga Bigyan Mancha. ➢ Won prize in school, college level Annual Sports Competition. ➢ Qualified **Gate 2011** in Electronics and Communication Engineering. ➢ Qualified **UGC NET** December 2014 for both **JRF** and **Assistant Professor in Electronic Science.** ➢ Paper Setter Cum Examiner for 52nd Skill Competition under Craftsmen Training Scheme, 2016, Govt. of West Bengal.
Publications: Conferences	[1] **Rajib Das "FPGA Implementation of Digital Modulation Schemes: BPSK and QPSK using VHDL",** IJECT- International Journal of Electronics and Communication Technology, ISSN:2230-7107(online),vol-5,Jan-Mar 2014 [2] **Rajib Das**, Amrita Guha and Ananya Bhattacharya,**''FPGA Based Higher Order FIR filter using XILINX System Generator''** International conference on Signal Processing, Communication, Power and Embedded System , IEEE Conference ,Oct-2016.Pages 111-115, DOI:10.1109/SCOPES.2016.7955632