

# MCU & Pins

The diagram illustrates the pin connections for the MCU & Pins section. It includes two main components: IC4 (CC2650MODAM0HR) and IC1 (TPS73133DBVR).

**IC4 (CC2650MODAM0HR) Pin Connections:**

- 1: GND(1)
- 2: NC(1)
- 3: GND(2)
- 4: DIO\_0
- 5: DIO\_1
- 6: DIO\_2
- 7: DIO\_3
- 8: DIO\_4
- 9: JTAG\_TMS
- 10: JTAG\_TCK
- 11: DIO\_5/JTAG\_TDO
- 12: DIO\_6/JTAG\_TDI
- 13: NRESET
- 14: DIO\_7
- 15: DIO\_8
- 16: DIO\_9
- 17: DIO\_10
- 18: DIO\_11
- 19: DIO\_12
- 20: DIO\_13
- 21: DIO\_14
- 22: VDD(1)
- 23: VDD(2)
- 24: NC(2)
- 25: NC(3)
- 26: EXPOSED\_GND(1)
- 27: EXPOSED\_GND(2)
- 28: EXPOSED\_GND(3)
- 29: EXPOSED\_GND(4)

**IC1 (TPS73133DBVR) Pin Connections:**

- 1: GPIO14
- 2: GPIO13
- 3: GPIO12
- 4: GPIO11
- 5: GPIO10
- 6: GPIO9
- 7: GPIO8
- 8: GPIO7
- 9: GPIO6
- 10: GPIO5
- 11: GPIO4
- 12: GPIO3
- 13: GPIO2
- 14: GPIO1
- 15: GPIO0
- 16: NC
- 17: AREF
- 18: 3V
- 19: RESET
- 20: VCC
- 21: NRESET

**Reset Button:** SW1 (SPST\_TACT-KMR2) connected to VCC, GND, and NRESET.

**JTAG Connection:** FTSH-105-01-F-DV-K connector connected to JTAG\_TMS, JTAG\_TCK, DIO5\_TDO, DIO6\_TDI, and NRESET.

