

# LITERATURE REVIEW: Accurately computing large floating-point numbers using parallel computing

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## 1 Introduction

It is known that for accurately summing up a very large number of small and large floating point numbers one has to use snowball effect to obtain larger numbers from small ones. We like to pursue this idea and extend it in parallel computing. For that we study the existing literature on how to compute the exact sum of  $n$  floating numbers parallelly.

## 2 Literature Review

Give an overview of the relevant literature. Cite all relevant papers, like [2], [12], [3], [10], [5], [1], and [4]. Outline for each paper the relevant results in relation to your project. Make sure that you don't just list all relevant papers in random order. Devise a scheme to group papers by subject. The goal is to present to the reader the state-of-the-art in the field selected for your project.

Demmel and Nguyen [7] used Rumps algorithm for floating point summation that is reproducible independent of the order of summation that may be different with the dynamic scheduling of parallel computing resources and floating point nonassociativity. Their absolute error bound is 2-28 times macheps, and requires constant amount of extra memory usage. Demmel and Hida [6] analysed several algorithms and showed that if a wider accumulator of  $F$  bits is used to sum  $n$  floating point numbers each of at most  $f$  bits, and if sum is carried out in descending order of exponents then an error of at most 1.5 times the least significant bit can occur provided that number of summands does not exceed  $2^{(F-f)}$ . Rump, Ogita and Oishi [13] showed that their algorithm results in a value nearest to the true sum. In 2013 the authors [8] have shown how to use tree reduced parallelism to compute sum by using parallel associative reduction, iterative refinement and conservative early detection to obtain an algorithm of order  $\log n$ . Neal [11] presented two algorithms in one of which a small superaccumulator with 67 64-bit chunks each with 32-bit overlap with the next chunk was used to allow carry propagation to be done infrequently. Kai and wang [9] have shown that computing ensuring minimum error when  $n$  numbers can be both positive and negative is NP-hard. However their algorithm can sum with no more than  $2(\lceil \log(n-1)+1 \rceil) \cdot \epsilon$  where  $\epsilon$  is the worst case minimum error over all possible orders.

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