

Informative attachment to RF guideline revision 1.05

			Szenario 1	Szenario 2	Szenario 3	Szenario 4	Szenario 5	Szenario 6	Szenario 7	Szenario 8	Szenario 9	Szenario 10	Szenario 11	Szenario 11
			qualified RF-module	qualified RF-module	qualified RF-module	qualified RF-module	qualified RF-module	qualified RF-module	qualified RF-module	qualified RF-module	qualified RF-module	qualified RF-module	qualified RF-module	qualified RF-module
			RF-integrated circuit	extreme voltage range expanded	nominal voltage changed	extreme temperature range expanded	using antenna with higher gain	using external oscillator	adding Power Amplifier	using host with internal power regulator/supply	using a regulated power supply	hardware changes	replacing the Antenna Port	adding shielding in the host
			see 5.1.1	see 5.2.1	see 5.2.1	see 5.2.2	see 5.2.3	see 5.3.2	see 5.4.2	see 5.3.2	see 5.4.4	see 5.4.1	see 5.4.2	see 5.3.2
Testcase detail information	extr. temp condition	extr.voltage condition												
	normal	normal	X		X		X		X	see Szenario 2/3	see Szenario 2/3	X	X	X
	low	low	X	either X		X	X		X	see Szenario 2/3	see Szenario 2/3	X	X	X
	low	high	X	either X		X	X		X	see Szenario 2/3	see Szenario 2/3	X	X	X
	high	low	X	or X		X	X		X	see Szenario 2/3	see Szenario 2/3	X	X	X
	high	high	X	or X		X	X		X	see Szenario 2/3	see Szenario 2/3	X	X	X
TRMCA01/C	normal	normal	X		X				X	see Szenario 2/3	see Szenario 2/3	X		
Power Density	low	low	X	either X		X			X	see Szenario 2/3	see Szenario 2/3	X		
	low	high	X	either X		X			X	see Szenario 2/3	see Szenario 2/3	X		
	high	low	X	or X		X			X	see Szenario 2/3	see Szenario 2/3	X		
	high	high	X	or X		X			X	see Szenario 2/3	see Szenario 2/3	X		
TRMCA01/C	normal	normal	X		X				X	see Szenario 2/3	see Szenario 2/3	X		
Power Control														
TRMCA04/C	normal	normal	X		X		X	X	X	see Szenario 2/3	see Szenario 2/3	X		X
TX Output Spectrum – Frequency range	low	low	X	either X		X	X	X	X	see Szenario 2/3	see Szenario 2/3	X		X
	low	high	X	either X		X	X	X	X	see Szenario 2/3	see Szenario 2/3	X		X
	high	low	X	or X		X	X	X	X	see Szenario 2/3	see Szenario 2/3	X		X
	high	high	X	or X		X	X	X	X	see Szenario 2/3	see Szenario 2/3	X		X
TRMCA05/C	normal	normal	X		X			X	X	see Szenario 2/3	see Szenario 2/3	X		
TX Output Spectrum – 20 dB Bandwidth	low	low	X	either X		X		X	X	see Szenario 2/3	see Szenario 2/3	X		
	low	high	X	either X		X		X	X	see Szenario 2/3	see Szenario 2/3	X		
	high	low	X	or X		X		X	X	see Szenario 2/3	see Szenario 2/3	X		
	high	high	X	or X		X		X	X	see Szenario 2/3	see Szenario 2/3	X		
TRMCA06/C	normal	normal	X		X			X	X	see Szenario 2/3	see Szenario 2/3	X		X
TX Output Spectrum – Adjacent channel power	low	low	X	either X		X		X	X	see Szenario 2/3	see Szenario 2/3	X		X
	low	high	X	either X		X		X	X	see Szenario 2/3	see Szenario 2/3	X		X
	high	low	X	or X		X		X	X	see Szenario 2/3	see Szenario 2/3	X		X
	high	high	X	or X		X		X	X	see Szenario 2/3	see Szenario 2/3	X		X
TRMCA07/C	normal	normal	X		X				X	see Szenario 2/3	see Szenario 2/3	X		
Modulation Characteristics	low	low	X	either X		X			X	see Szenario 2/3	see Szenario 2/3	X		
	low	high	X	either X		X			X	see Szenario 2/3	see Szenario 2/3	X		
	high	low	X	or X		X			X	see Szenario 2/3	see Szenario 2/3	X		
	high	high	X	or X		X			X	see Szenario 2/3	see Szenario 2/3	X		
TRMCA08/C	normal	normal	X		X			X	X	see Szenario 2/3	see Szenario 2/3	X		
Initial Carrier Frequency Tolerance	low	low	X	either X		X		X	X	see Szenario 2/3	see Szenario 2/3	X		
	high	low	X	either X		X		X	X	see Szenario 2/3	see Szenario 2/3	X		
	high	low	X	or X		X		X	X	see Szenario 2/3	see Szenario 2/3	X		
	high	high	X	or X		X		X	X	see Szenario 2/3	see Szenario 2/3	X		
TRMCA09/C	normal	normal	X		X				X	see Szenario 2/3	see Szenario 2/3	X		
Carrier Frequency Drift	low	low	X	either X		X			X	see Szenario 2/3	see Szenario 2/3	X		
	low	high	X	either X		X			X	see Szenario 2/3	see Szenario 2/3	X		
	high	low	X	or X		X			X	see Szenario 2/3	see Szenario 2/3	X		
	high	high	X	or X		X			X	see Szenario 2/3	see Szenario 2/3	X		
RCVCA01/C	normal	normal	X		X			either X	X	see Szenario 2/3	see Szenario 2/3	X	either X	
Sensitivity – single slot packets	low	low	X	either X		X		either X	X	see Szenario 2/3	see Szenario 2/3	X	either X	
	low	high	X	either X		X		either X	X	see Szenario 2/3	see Szenario 2/3	X	either X	
	high	low	X	or X		X		either X	X	see Szenario 2/3	see Szenario 2/3	X	either X	
	high	high	X	or X		X		either X	X	see Szenario 2/3	see Szenario 2/3	X	either X	
RCVCA02/C	normal	normal	X		X			or X (long. supp.)	X	see Szenario 2/3	see Szenario 2/3	X	or X (long. supp.)	
Sensitivity - multi-slot packets	low	low	X	either X		X		or X (long. supp.)	X	see Szenario 2/3	see Szenario 2/3	X	or X (long. supp.)	
	low	high	X	either X		X		or X (long. supp.)	X	see Szenario 2/3	see Szenario 2/3	X	or X (long. supp.)	
	high	low	X	or X		X		or X (long. supp.)	X	see Szenario 2/3	see Szenario 2/3	X	or X (long. supp.)	
	high	high	X	or X		X		or X (long. supp.)	X	see Szenario 2/3	see Szenario 2/3	X	or X (long. supp.)	
RCVCA03/C	normal	normal	X		X			X	X	see Szenario 2/3	see Szenario 2/3	X		
CI performance														
RCVCA04/C	normal	normal	X		X			X	X	see Szenario 2/3	see Szenario 2/3	X		
Blocking performance														
RCVCA05/C	normal	normal	X		X				X	see Szenario 2/3	see Szenario 2/3	X		
Intermodulation Performance														
RCVCA05/C	normal	normal	X		X				X	see Szenario 2/3	see Szenario 2/3	X		
Maximum Input Level														
TP/PHYS/TRX/BV-01/C	normal	normal						X						
Baseband Master TX timing	low	low				X		X						
	low	high				X		X						
	high	low				X		X						
	high	high				X		X						