<u>Lab 4 Parts A + B + C Reference Outputs (libg.mtr.gz)</u>

Part A:

A.libq.res

: 10000001 CYCLES

CORE_0_INST : 100000000 CORE_0_CYCLES : 100000000 CORE_0_IPC : 1.000

MEMSYS_IFETCH_ACCESS : 100000000 MEMSYS_LOAD_ACCESS : 36353358

MEMSYS_STORE_ACCESS : 7500768

MEMSYS_IFETCH_AVGDELAY : 0.000

MEMSYS_LOAD_AVGDELAY : 0.000

MEMSYS_STORE_AVGDELAY : 0.000

DCACHE_READ_ACCESS : 36353358 DCACHE_WRITE_ACCESS : 750076
DCACHE_READ_MISS : 1186718
DCACHE_WRITE_MISS : 4
DCACHE_READ_MISS_PERC : 3.26
DCACHE_WRITE_MISS_PERC : 0.00
DCACHE_DIRTY_EVICTS : 265840 : 7500768

3.264 0.000

Part B:

B.S1MB.libq.res

CYCLES : 230540751

CORE_0_INST : 100000000 CORE_0_CYCLES : 230540750

CORE_0_IPC : 0.434

MEMSYS_IFETCH_ACCESS : 100000000 MEMSYS_LOAD_ACCESS : 36353358 MEMSYS_STORE_ACCESS : 7500768 MEMSYS_IFETCH_AVGDELAY : 1.000 MEMSYS_LOAD_AVGDELAY : 4.591 MEMSYS_STORE_AVGDELAY : 1.000

ICACHE_READ_ACCESS : 100000000
ICACHE_WRITE_ACCESS : 0
ICACHE_READ_MISS : 17
ICACHE_WRITE_MISS : 0
ICACHE_READ_MISS_PERC : 0.000
ICACHE_WRITE_MISS_PERC : 0.000

ICACHE_DIRTY_EVICTS : 0

DCACHE_READ_ACCESS : 36353358
DCACHE_WRITE_ACCESS : 7500768
DCACHE_READ_MISS : 1186718
DCACHE_WRITE_MISS : 4
DCACHE_READ_MISS_PERC : 3.264

DCACHE_WRITE_MISS_PERC : 0.000 DCACHE_DIRTY_EVICTS : 265840

L2CACHE_READ_ACCESS : 1186739 L2CACHE_WRITE_ACCESS : 265840 L2CACHE_READ_MISS : 1186738

L2CACHE WRITE MISS : 0

L2CACHE_READ_MISS_PERC : 100.000 L2CACHE_WRITE_MISS_PERC : 0.000

L2CACHE_DIRTY_EVICTS : 263498

DRAM_READ_ACCESS : 1186738 DRAM_WRITE_ACCESS : 263498 DRAM_READ_DELAY_AVG : 100.000 DRAM_WRITE_DELAY_AVG : 100.000

Part C:

C.S1MB.CP.libq.res

CYCLES : 230540751

CORE_0_INST : 100000000 CORE_0_CYCLES : 230540750

CORE_0_IPC : 0.434

MEMSYS_IFETCH_ACCESS : 100000000
MEMSYS_LOAD_ACCESS : 36353358
MEMSYS_STORE_ACCESS : 7500768
MEMSYS_IFETCH_AVGDELAY : 1.000
MEMSYS_LOAD_AVGDELAY : 4.591
MEMSYS_STORE_AVGDELAY : 1.000

ICACHE_READ_ACCESS : 100000000 ICACHE_WRITE_ACCESS : 0 ICACHE_READ_MISS : 17 ICACHE_WRITE_MISS : 0

ICACHE_READ_MISS_PERC : 0.000 ICACHE_WRITE_MISS_PERC : 0.000 ICACHE_DIRTY_EVICTS : 0

DCACHE_READ_ACCESS : 36353358 DCACHE_WRITE_ACCESS : 7500768 DCACHE_READ_MISS : 1186718 DCACHE_WRITE_MISS : 4

DCACHE_WRITE_MISS : 4
DCACHE_READ_MISS_PERC : 3.264
DCACHE_WRITE_MISS_PERC : 0.000
DCACHE_DIRTY_EVICTS : 265840

L2CACHE_READ_ACCESS : 1186739 L2CACHE_WRITE_ACCESS : 265840 L2CACHE_READ_MISS : 1186738 L2CACHE_WRITE_MISS : 0

L2CACHE_READ_MISS_PERC : 100.000 L2CACHE_WRITE_MISS_PERC : 0.000

L2CACHE_DIRTY_EVICTS : 263498

DRAM_READ_ACCESS : 1186738 DRAM_WRITE_ACCESS : 263498 DRAM_READ_DELAY_AVG : 100.000 DRAM_WRITE_DELAY_AVG : 100.000

C.S1MB.OP.libq.res

: 206050671 CYCLES

CORE 0 INST : 100000000 CORE_0_CYCLES : 206050670

CORE 0 IPC 0.485

MEMSYS IFETCH ACCESS : 100000000 MEMSYS LOAD ACCESS : 36353358 MEMSYS_STORE_ACCESS
MEMSYS_IFETCH_AVGDELAY : 7500768 1.000 MEMSYS_LOAD_AVGDELAY 3.917 MEMSYS STORE AVGDELAY : 1.000

ICACHE READ ACCESS : 100000000

ICACHE WRITE ACCESS 0 ICACHE_READ_MISS 17 ICACHE_WRITE_MISS 0

ICACHE_WRITE_MISS_PERC : ICACHE_DIPTY_FORE 0.000 0.000

ICACHE_DIRTY_EVICTS

DCACHE READ ACCESS : 36353358 DCACHE_WRITE_ACCESS : 7500768 DCACHE READ MISS : 1186718

DCACHE WRITE MISS 4

DCACHE_READ_MISS_PERC : DCACHE_WRITE_MISS_PERC : 3.264 0.000 DCACHE_DIRTY_EVICTS : 265840

L2CACHE_READ_ACCESS 1186739 L2CACHE_WRITE_ACCESS 265840 : 1186738 L2CACHE_READ_MISS

L2CACHE_WRITE_MISS 0

L2CACHE_READ_MISS_PERC 100.000 L2CACHE WRITE MISS PERC 0.000

L2CACHE DIRTY EVICTS : 263498

DRAM_READ_ACCESS : 1186738 DRAM WRITE ACCESS : 263498 DRAM_READ_DELAY_AVG 79.364 DRAM_WRITE_DELAY_AVG : 145.000