

Small Trace

PART B1:

B1.1: Schedule in-order, 1-cycle load latency

LAB3_NUM_INST : 100
LAB3_NUM_CYCLES : 106
LAB3_CPI : 1.060
LAB3_Cache HR : nan

B1.2: Schedule OoO-oldest first, 1-cycle load latency

LAB3_NUM_INST : 100
LAB3_NUM_CYCLES : 106
LAB3_CPI : 1.060
LAB3_Cache HR : nan **Part**

B2:

B2.1: Schedule in-order, with cache

LAB3_NUM_INST : 100
LAB3_NUM_CYCLES : 125
LAB3_CPI : 1.250
LAB3_Cache HR : 0.947

B2.1: Schedule OoO-oldest first, with cache

LAB3_NUM_INST : 100
LAB3_NUM_CYCLES : 125
LAB3_CPI : 1.250 LAB3_Cache
HR : 0.947

PART B3:

B3.1: Schedule in-order, with cache, with exceptions

LAB3_NUM_INST : 100

LAB3_NUM_CYCLES : 125

LAB3_CPI : 1.250

LAB3_Cache HR : 0.947

B3.2: Schedule OoO-oldest first, with cache, with exceptions

LAB3_NUM_INST : 100

LAB3_NUM_CYCLES : 125

LAB3_CPI : 1.250

LAB3_Cache HR : 0.947

PART C (2-wide Superscalar)

C.0: Schedule in-order, 1-cycle load latency

LAB3_NUM_INST : 100

LAB3_NUM_CYCLES : 56

LAB3_CPI : 0.560

LAB3_Cache HR : nan

C.1: Schedule in-order, with cache

LAB3_NUM_INST : 100

LAB3_NUM_CYCLES : 82

LAB3_CPI : 0.820

LAB3_Cache HR : 0.947

C.2: Schedule OoO-oldest first, with cache

LAB3_NUM_INST : 100

LAB3_NUM_CYCLES : 82

LAB3_CPI : 0.820

LAB3_Cache HR : 0.947