# <u>Lab 4 Parts D + E Reference Outputs (mix1: libg.mtr.gz + bzip2.mtr.gz)</u>

#### Part D:

## D.mix1.res

: 217775286 CYCLES

CORE\_0\_INST : 100000000 CORE\_0\_CYCLES : 12728822 CORE\_0\_IPC : 0.786 : 127288220

CORE\_1\_INST : 100000000 CORE\_1\_CYCLES : 21777528 CORE\_1\_IPC : 0.459 : 217775285

MEMSYS\_IFETCH\_ACCESS : 200000000 MEMSYS\_LOAD\_ACCESS : 75980799
MEMSYS\_STORE\_ACCESS : 23386645
MEMSYS\_IFETCH\_AVGDELAY : 1.000
MEMSYS\_LOAD\_AVGDELAY : 2.909
MEMSYS\_STORE\_AVGDELAY : 1.920

ICACHE\_0\_READ\_ACCESS : 100000000
ICACHE\_0\_WRITE\_ACCESS : 0
ICACHE\_0\_READ\_MISS : 222
ICACHE\_0\_WRITE\_MISS : 0
ICACHE\_0\_READ\_MISS\_PERC : 0.0000
ICACHE\_0\_WRITE\_MISS\_PERC : 0.0000
ICACHE\_0\_DIRTY\_EVICTS : 0

DCACHE\_0\_READ\_ACCESS : 39627441
DCACHE\_0\_WRITE\_ACCESS : 15885877
DCACHE\_0\_READ\_MISS : 655444
DCACHE\_0\_WRITE\_MISS : 218653
DCACHE\_0\_READ\_MISS\_PERC : 1.654
DCACHE\_0\_WRITE\_MISS\_PERC : 1.376
DCACHE\_0\_DIRTY\_EVICTS : 325242

ICACHE\_1\_READ\_ACCESS : 100000000
ICACHE\_1\_WRITE\_ACCESS : 0
ICACHE\_1\_READ\_MISS : 17
ICACHE\_1\_WRITE\_MISS : 0
ICACHE\_1\_READ\_MISS\_PERC : 0.000
ICACHE\_1\_WRITE\_MISS\_PERC : 0.000
ICACHE\_1\_DIRTY\_EVICTS : 0

DCACHE\_1\_READ\_ACCESS : 36353358

DCACHE\_1\_WRITE\_ACCESS : 7500768 DCACHE\_1\_READ\_MISS : 1186718 DCACHE\_1\_WRITE\_MISS : 4

DCACHE\_1\_READ\_MISS\_PERC : 3.264 DCACHE\_1\_WRITE\_MISS\_PERC : 0.000 DCACHE\_1\_DIRTY\_EVICTS : 265840

L2CACHE\_READ\_ACCESS : 2061058 L2CACHE\_WRITE\_ACCESS : 591082 L2CACHE\_READ\_MISS : 1474240 L2CACHE\_WRITE\_MISS : 264

L2CACHE\_READ\_MISS\_PERC : 71.528 L2CACHE\_WRITE\_MISS\_PERC : 0.045

L2CACHE\_DIRTY\_EVICTS : 428716

DRAM\_READ\_ACCESS : 1474240 DRAM\_WRITE\_ACCESS : 428716 DRAM\_READ\_DELAY\_AVG : 99.007 DRAM\_WRITE\_DELAY\_AVG : 144.995

## Part E:

#### E.Q1.mix1.res

CYCLES : 216659826

CORE\_0\_INST : 100000000 CORE\_0\_CYCLES : 1362 CORE\_0\_IPC : 0.734 : 136269395

CORE\_1\_INST : 100000000 CORE\_1\_CYCLES : 216659825

CORE\_1\_IPC 0.462

MEMSYS IFETCH ACCESS : 200000000 MEMSYS\_LOAD\_ACCESS : 75980799 MEMSYS\_STORE\_ACCESS : 23386645 MEMSYS\_IFETCH\_AVGDELAY : 1.000
MEMSYS\_LOAD\_AVGDELAY : 3.012
MEMSYS\_STORE\_AVGDELAY : 2.012

: 100000000 : 0 ICACHE 0 READ ACCESS : 222
.OACHE\_0\_WRITE\_MISS : 0
ICACHE\_0\_READ\_MISS\_PERC : 0.
ICACHE\_0\_WRITE\_MISS\_PERC : 0
ICACHE\_0\_DIRTY\_EVICTS ICACHE\_0\_WRITE\_ACCESS

0.000 0.000

DCACHE\_0\_READ\_ACCESS : 39627441 DCACHE\_0\_WRITE\_ACCESS DCACHE\_0\_WRITE\_ACCESS : 158858
DCACHE\_0\_READ\_MISS : 655444
DCACHE\_0\_WRITE\_MISS : 218653
DCACHE\_0\_READ\_MISS\_PERC : 1.6 : 15885877 1.654 DCACHE\_0\_WRITE\_MISS\_PERC 1.376 : 325242 DCACHE\_0\_DIRTY\_EVICTS

ICACHE 1 READ ACCESS : 100000000 ICACHE\_1\_KEAD\_AGGESS
ICACHE\_1\_WRITE\_ACCESS :
ICACHE\_1\_READ\_MISS :
ICACHE 1 WRITE MISS : 0 ICACHE\_1\_KEAD\_WISS
ICACHE\_1\_WRITE\_MISS : (
ICACHE\_1\_READ\_MISS\_PERC :
ICACHE\_1\_WRITE\_MISS\_PERC :
ICACHE\_1\_WRITE\_MISS\_PERC : 17 0 0.000 0.000 0

DCACHE\_1\_READ\_ACCESS : 36353358 DCACHE\_1\_WRITE\_ACCESS : 7500768 DCACHE\_1\_READ\_MISS : 1186718

: 4

DCACHE\_1\_WRITE\_MISS : 4
DCACHE\_1\_READ\_MISS\_PERC : 3.264 DCACHE\_1\_WRITE\_MISS\_PERC 0.000 DCACHE\_1\_DIRTY\_EVICTS : 265840

L2CACHE\_READ\_ACCESS : 2061058 L2CACHE\_WRITE\_ACCESS : 591082 L2CACHE READ MISS : 1557005 L2CACHE\_WRITE\_MISS : 1211

L2CACHE READ MISS PERC 75.544 L2CACHE\_WRITE\_MISS\_PERC : 0.205

L2CACHE\_DIRTY\_EVICTS : 450414

DRAM\_READ\_ACCESS : 1557005 DRAM WRITE ACCESS : 450414 DRAM\_READ\_DELAY\_AVG : 100.176 DRAM\_WRITE\_DELAY\_AVG : 144.994 : 144.994