

### **Set for Team 43**

**Construct the necessary truth table(s), Boolean functions and Simulation file with the appropriate IC diagram comprising the basic logic gates that represent the following scenario:**

Suppose you are employed at a robotics company, where your job is to identify the best functioning robots and label them as fit for sell. Four high speed imaging cameras are continuously observing 4 different movements of individual robot. For better throughput in your work, if suppose, you have been instructed to build a hardware component that will receive inputs as binary values of 0/1 from the 4 cameras for individual robot, and that component is supposed to sperate those robots that fail to perform any one or more of the movements. How are you supposed to design the underlying logic for the hardware design, and simulate your output with IC diagram accordingly?