

## CUDA Kernel Execution Time Analysis on GTX TITAN Black

### 1. Work per Kernel Launch

- Grid: (5,5) = 25 blocks
- Block: (32,32) = 1024 threads
- Each block computes one Euclidean distance.
- k = 100 dimensions.

Work:

- threadIdx.x covers offsets 0–99 once.
- 32 threadIdx.y values repeat work → 32x redundancy.
- Per threadIdx.y row: 200 float loads (100 from A, 100 from B).
- Per block:  $200 * 32 = 6400$  float loads.
- 25 blocks:  $160,000$  float loads =  $640,000$  bytes  $\approx 0.61$  MB.
- FLOPs:  $200 * 32 * 3 \approx 19,200$  FLOPs/block → 480,000 FLOPs total.

### 2. GTX TITAN Black Capabilities

- Peak compute: 5.1 TFLOPS (single precision)
- Peak memory bandwidth: 336 GB/s

### 3. Theoretical Lower Bounds

Compute-bound:

$$480k \text{ FLOPs} / (5.1e12 \text{ FLOPs/s}) \approx 0.094 \mu\text{s}$$

Memory-bound:

$$640k \text{ bytes} / (336e9 \text{ bytes/s}) \approx 1.9 \mu\text{s}$$

### 4. Expected Real Performance

- Kernel extremely small → dominated by launch overhead.
- Kepler launch overhead = several microseconds.
- With only 25 blocks, GPU underutilized.

### 5. Final Prediction

Average per-kernel time:

$$\approx 10\text{--}20 \mu\text{s}$$

Best estimate  $\approx 15 \mu\text{s}$