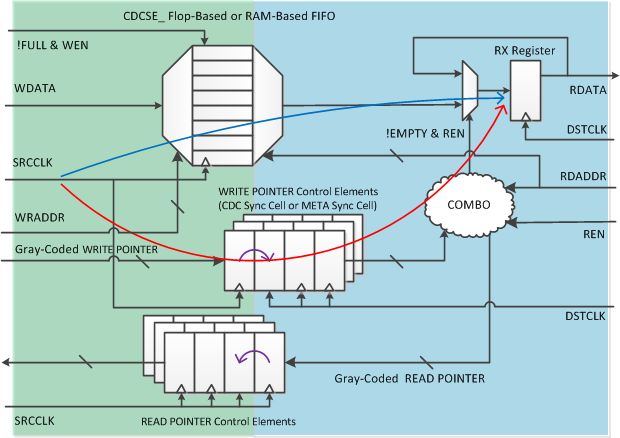
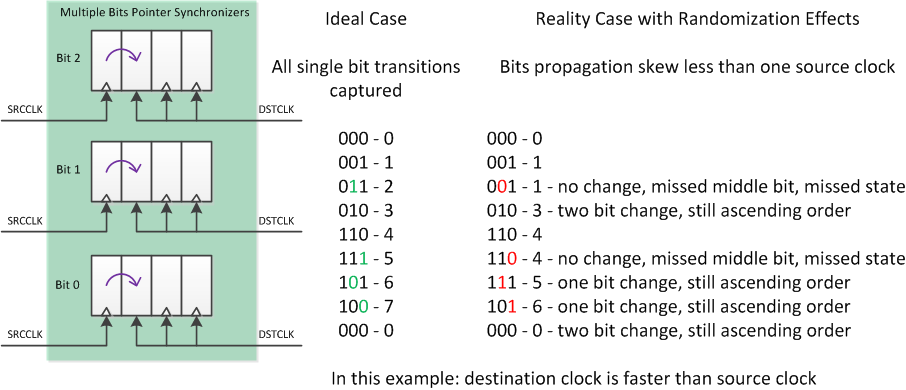


**Two Special Timing Checks**

**Async FIFO data/control race check**

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**Async FIFO gray-coded pointer bus skew check**

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