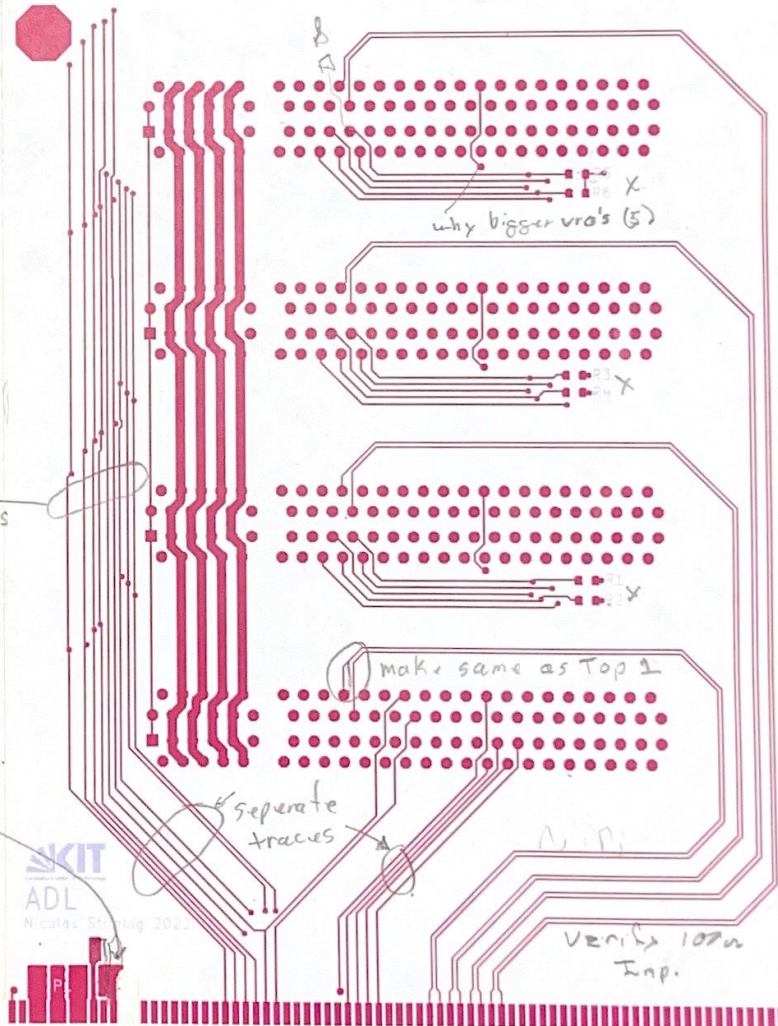


* Rule is termination resistor's
they should be at trace end of last
connection. NO "T" connections. Each
resistor should have its own via to gnd

seperate
Traces & Via's
move closer to
board edge

Trim back GND copper
to make trace next larger.



SKIT
ADL

Nicolas Strohriegel 2022

Veri 102n
Imp.

for all traces
put as much space
between for reduction
in x talk

