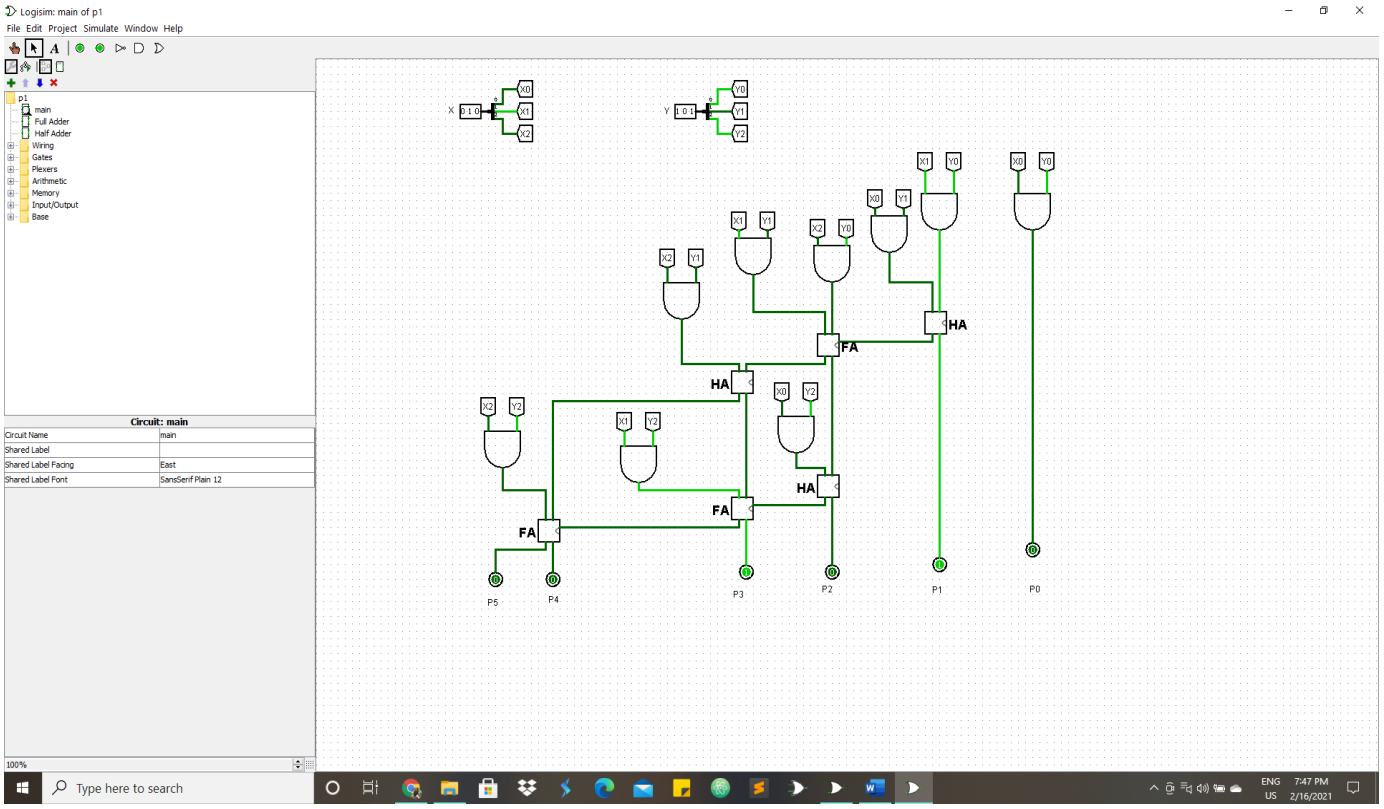
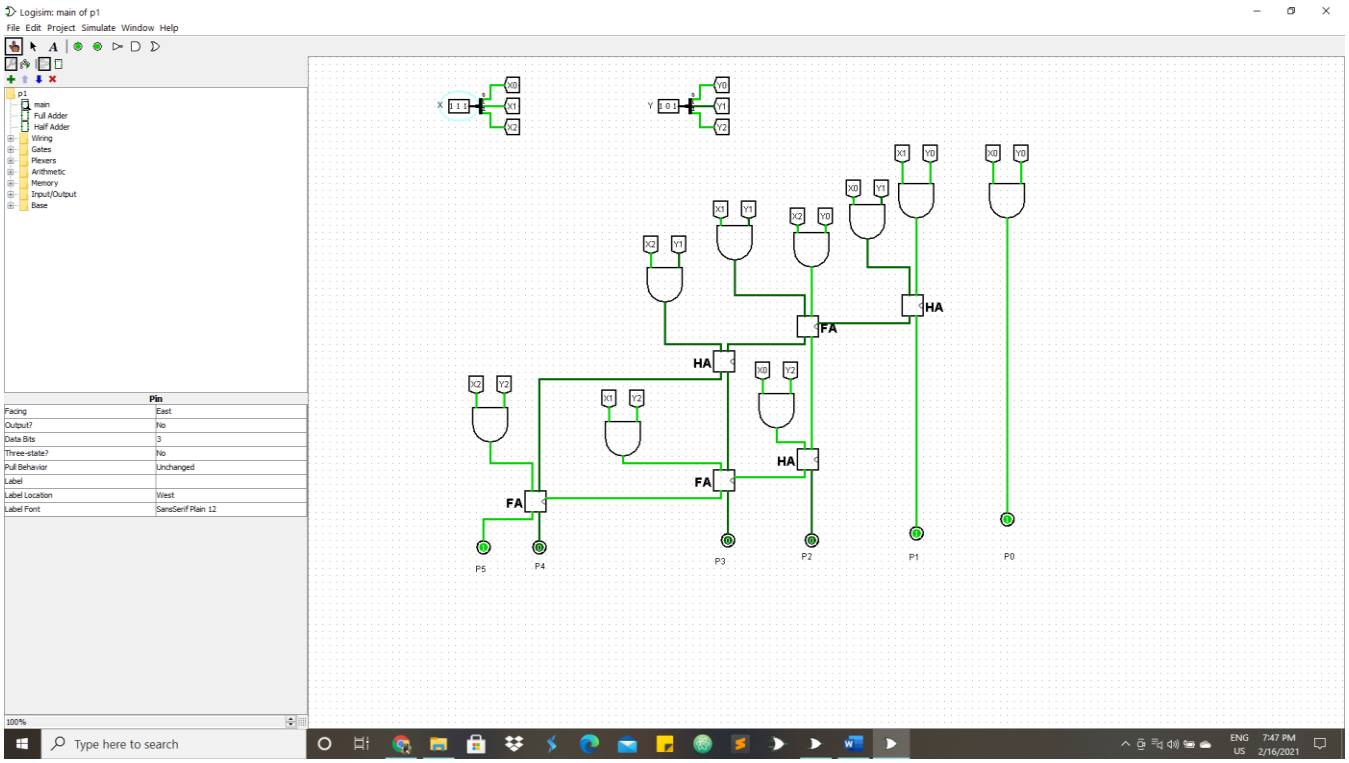
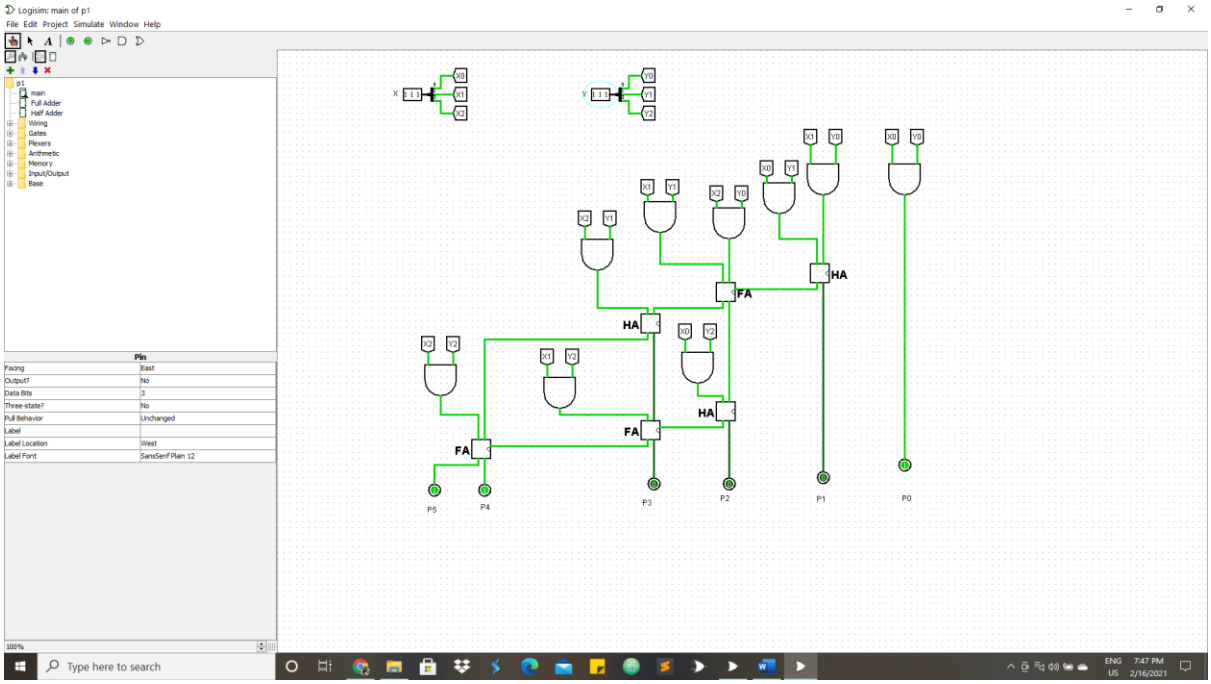
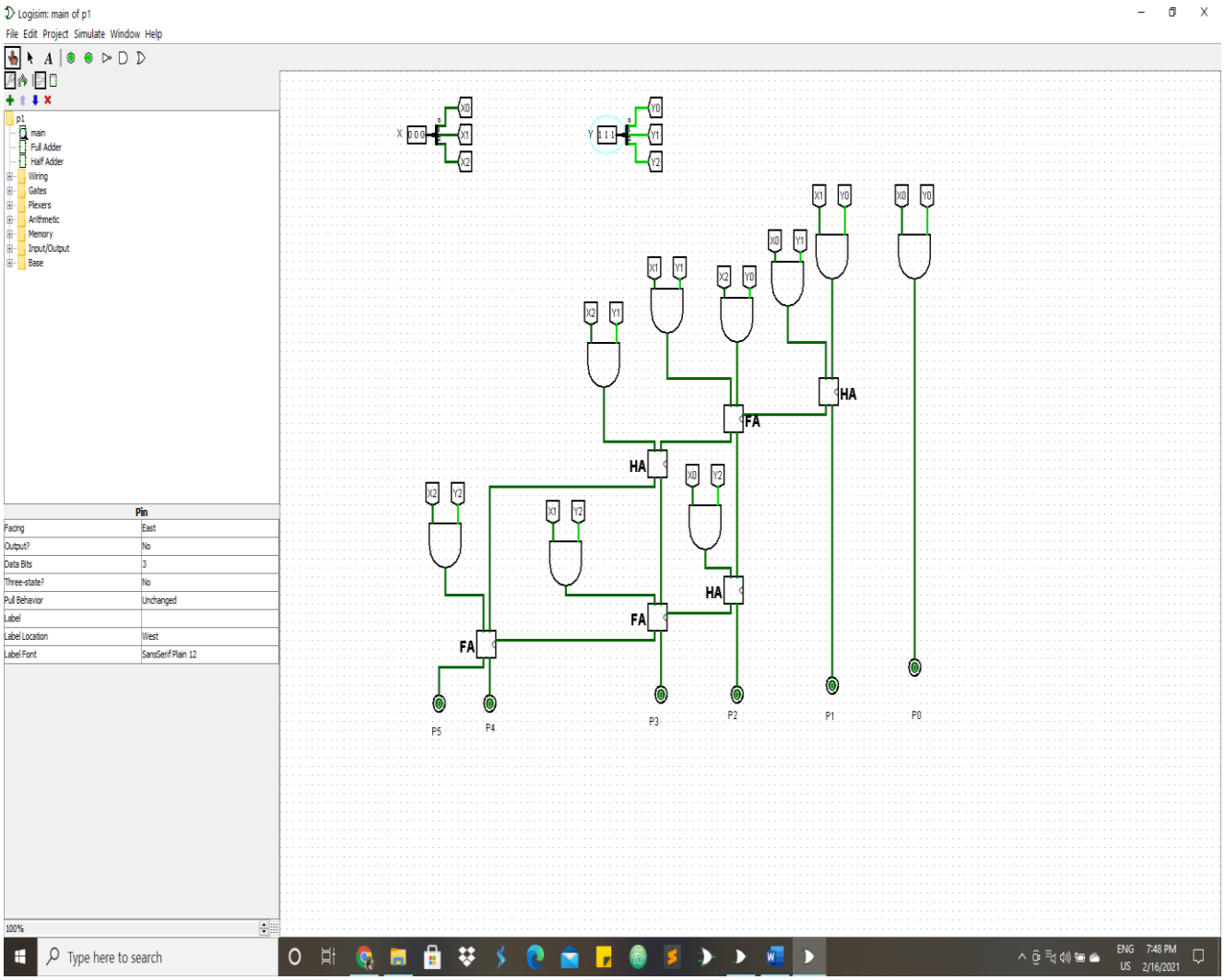
QUES 1:

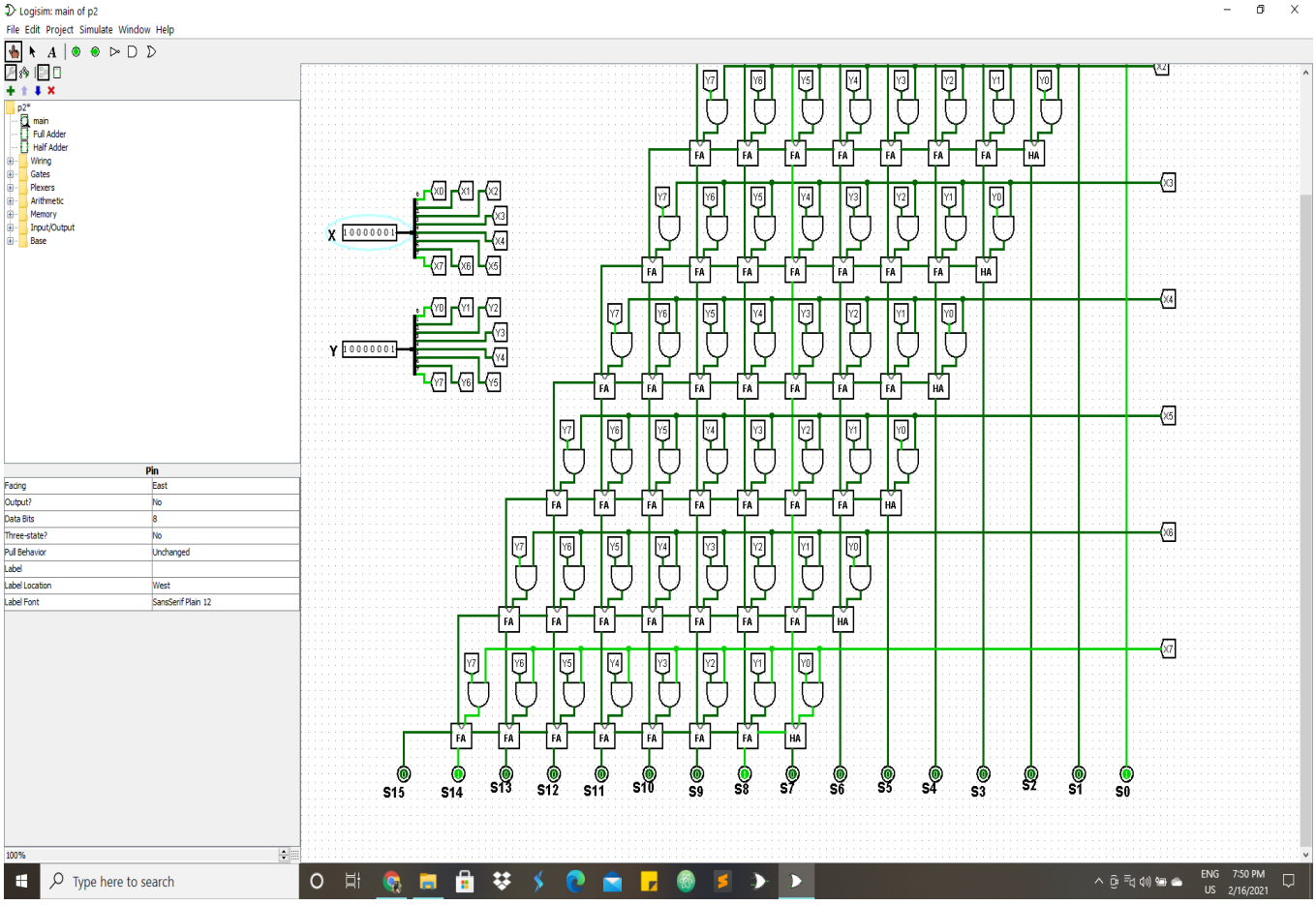

Logisim: main of p1
File Edit Project Simulate Window Help
p1
main
Full Adder
Half Adder
Wiring
Gates
Plexers
Arithmetic
Memory
Input/Output
Base
Circuit: main
Circuit Name main
Shared Label
Shared Label Facing East
Shared Label Font SansSerif Plain 12
HA
FA
HA
FA
HA
FA
P5
P4
P3
P2
P1
P0
100%
Type here to search
ENG US 7:47 PM 2/16/2021

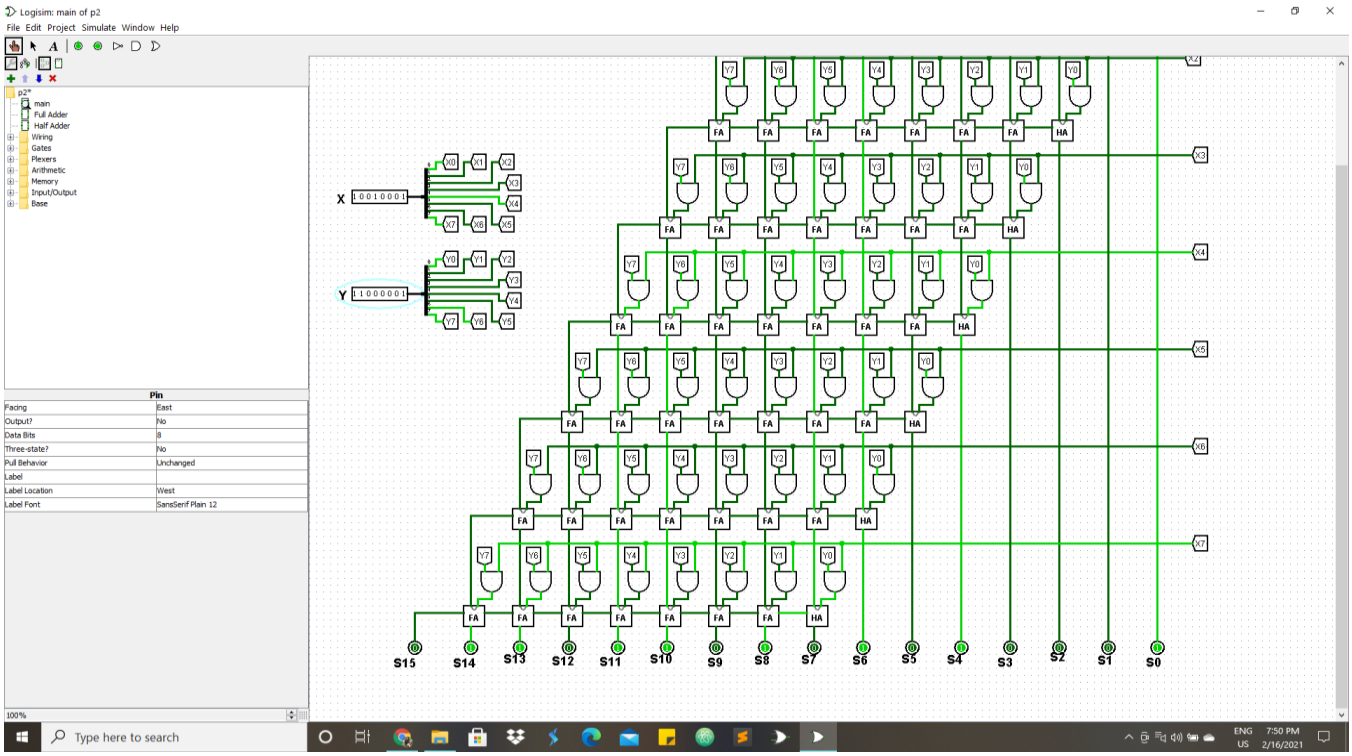

Logisim: main of p1
File Edit Project Simulate Window Help
p1
main
Full Adder
Half Adder
Wiring
Gates
Plexers
Arithmetic
Memory
Input/Output
Base
Pin
Facing East
Output? No
Data Bits 3
Three-state? No
Pull Behavior Unchanged
Label
Label Location West
Label Font SansSerif Plain 12
HA
FA
HA
FA
HA
FA
P5
P4
P3
P2
P1
P0
100%
Type here to search
ENG US 7:47 PM 2/16/2021

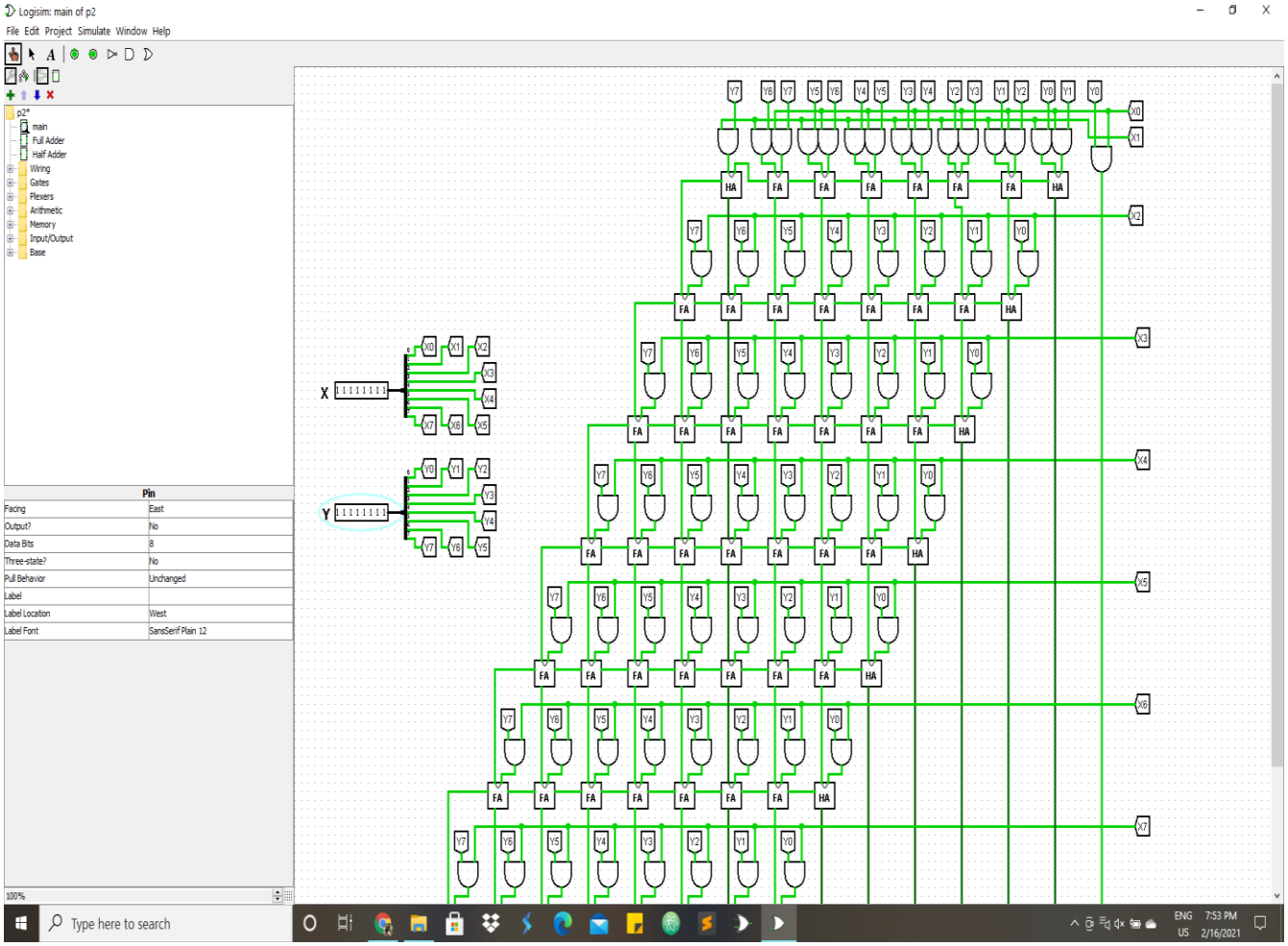

Logisim: main of p1
File Edit Project Simulate Window Help
p1
main
Full Adder
Half Adder
Wiring
Gates
Plexers
Arithmetic
Memory
Input/Output
Base
Pin
Facing East
Output? No
Data Bits 3
Three-state? No
Pull Behavior Unchanged
Label
Label Location West
Label Font SansSerif Plain 12
100%
HA
FA
HA
FA
HA
FA
P5
P4
P3
P2
P1
P0
Type here to search
ENG US 7:47 PM 2/16/2021

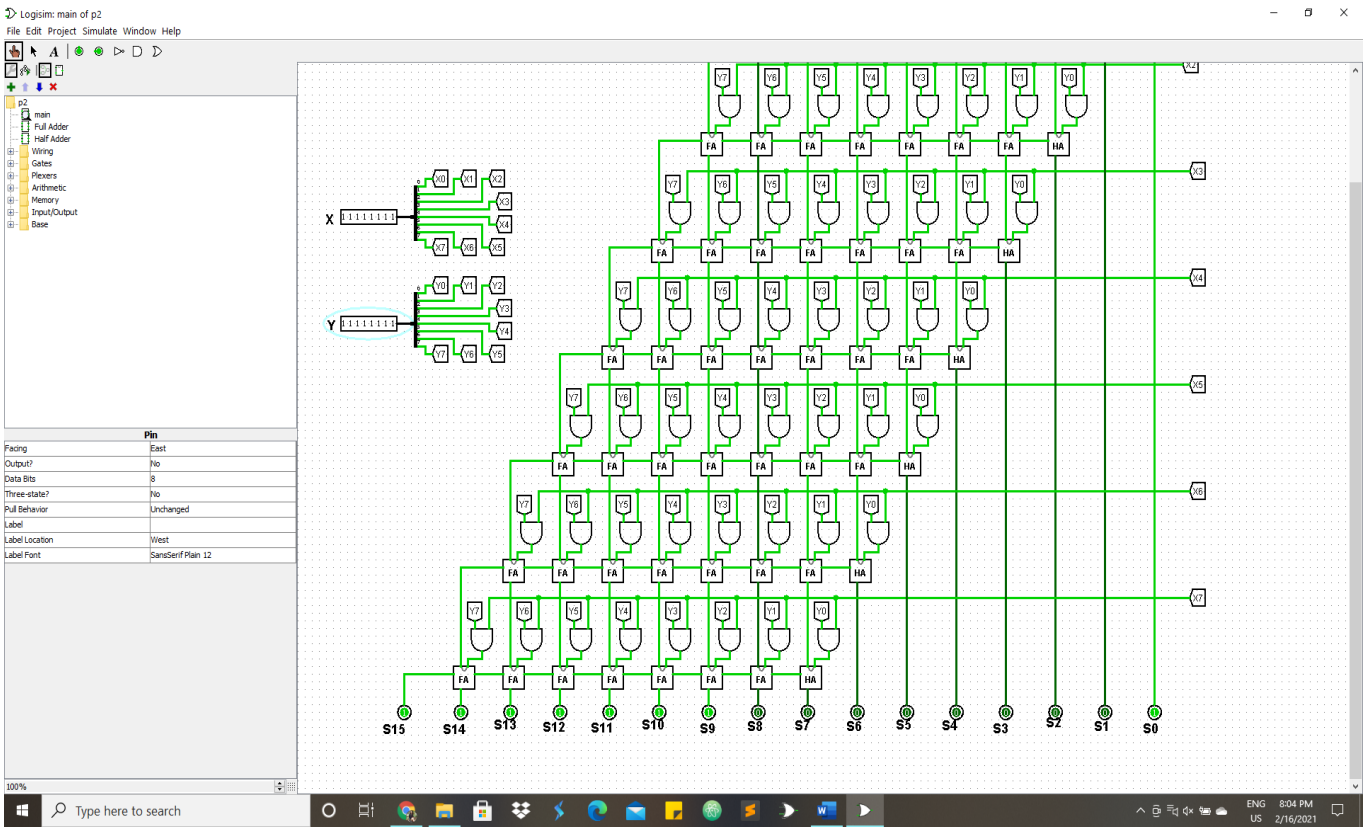

Logisim: main of p1
File Edit Project Simulate Window Help
p1
main
Full Adder
Half Adder
Wiring
Gates
Plexers
Arithmetic
Memory
Input/Output
Base
Pin
Facing East
Output? No
Data Bits 3
Three-state? No
Pull Behavior Unchanged
Label
Label Location West
Label Font SansSerif Plain 12
100%
HA
FA
HA
FA
HA
FA
P5
P4
P3
P2
P1
P0
Type here to search
ENG US 7:48 PM 2/16/2021

QUES 2:


Logisim: main of p2
File Edit Project Simulate Window Help
p2*
main
Full Adder
Half Adder
Wiring
Gates
Plexers
Arithmetic
Memory
Input/Output
Base
X
Y
Pin
Facing East
Output? No
Data Bits 8
Three-state? No
Pull Behavior Unchanged
Label
Label Location West
Label Font SansSerif Plain 12
100%
S15 S14 S13 S12 S11 S10 S9 S8 S7 S6 S5 S4 S3 S2 S1 S0
Type here to search
ENG US 7:50 PM 2/16/2021


Logisim: main of p2
File Edit Project Simulate Window Help
p2*
main
Full Adder
Half Adder
Wiring
Gates
Plexers
Arithmetic
Memory
Input/Output
Base
X
Y
Pin
Facing East
Output? No
Data Bits 8
Three-state? No
Pull Behavior Unchanged
Label
Label Location West
Label Font SansSerif Plain 12
100%
S15 S14 S13 S12 S11 S10 S9 S8 S7 S6 S5 S4 S3 S2 S1 S0
Type here to search
ENG US 7:50 PM 2/16/2021


Logisim: main of p2
File Edit Project Simulate Window Help
p2*
main
Full Adder
Half Adder
Wiring
Gates
Plexers
Arithmetic
Memory
Input/Output
Base
Pin
Facing East
Output? No
Data Bits 8
Three-state? No
Pull Behavior Unchanged
Label
Label Location West
Label Font SansSerif Plain 12
X
Y
FA
HA
Type here to search
ENG US 7:53 PM 2/16/2021


Logisim: main of p2
File Edit Project Simulate Window Help
p2
main
Full Adder
Half Adder
Wiring
Gates
Plexers
Arithmetic
Memory
Input/Output
Base
Pin
Facing East
Output? No
Data Bits 8
Three-state? No
Pull Behavior Unchanged
Label
Label Location West
Label Font SansSerif Plain 12
X
Y
FA
HA
S15 S14 S13 S12 S11 S10 S9 S8 S7 S6 S5 S4 S3 S2 S1 S0
Type here to search
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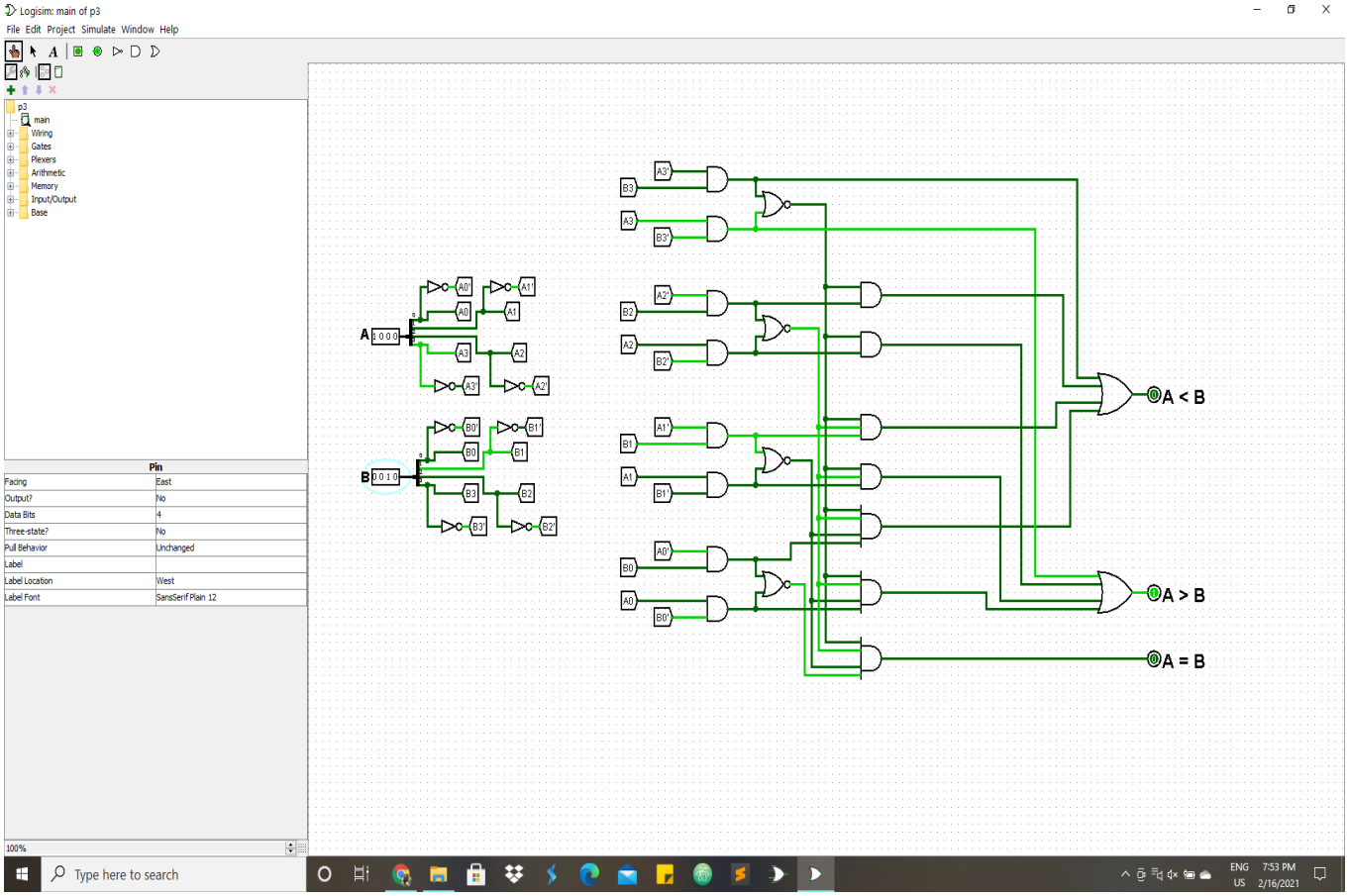
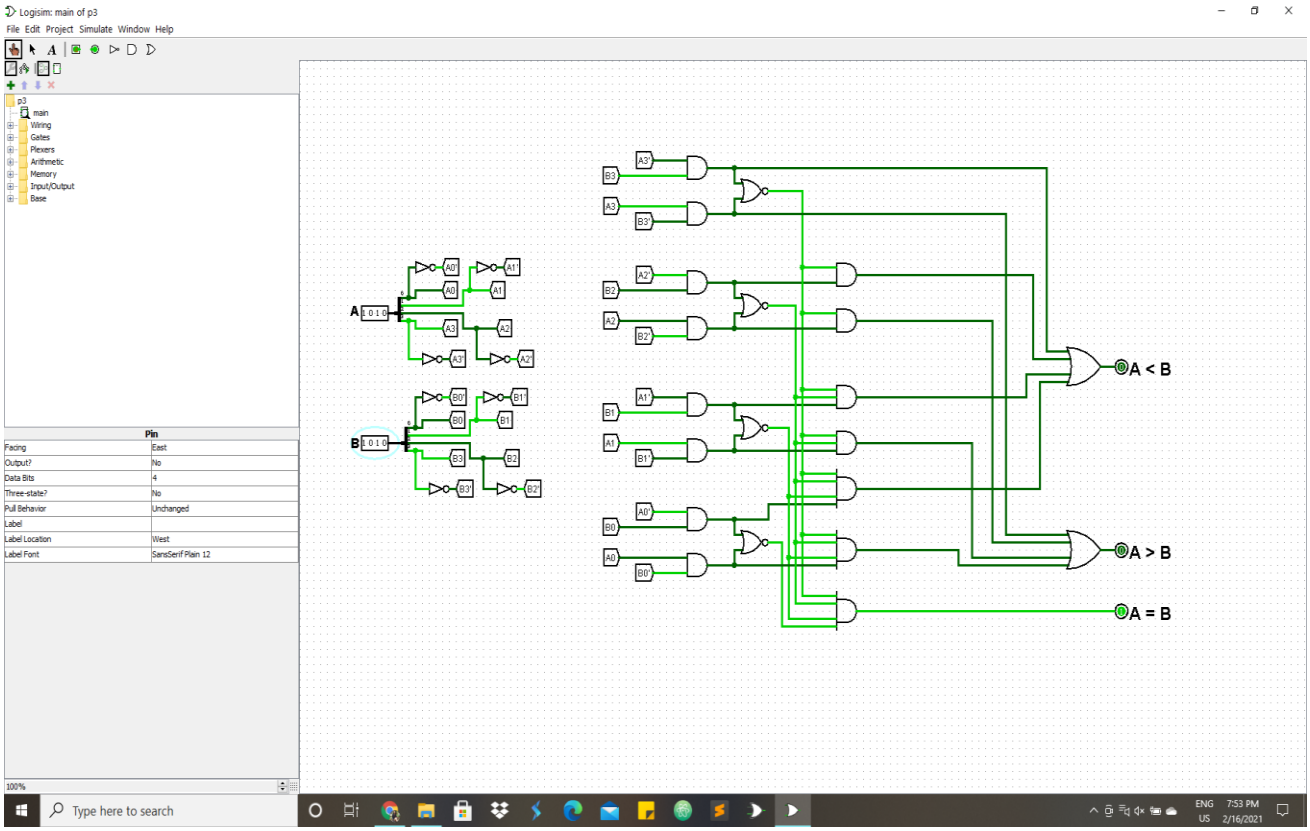
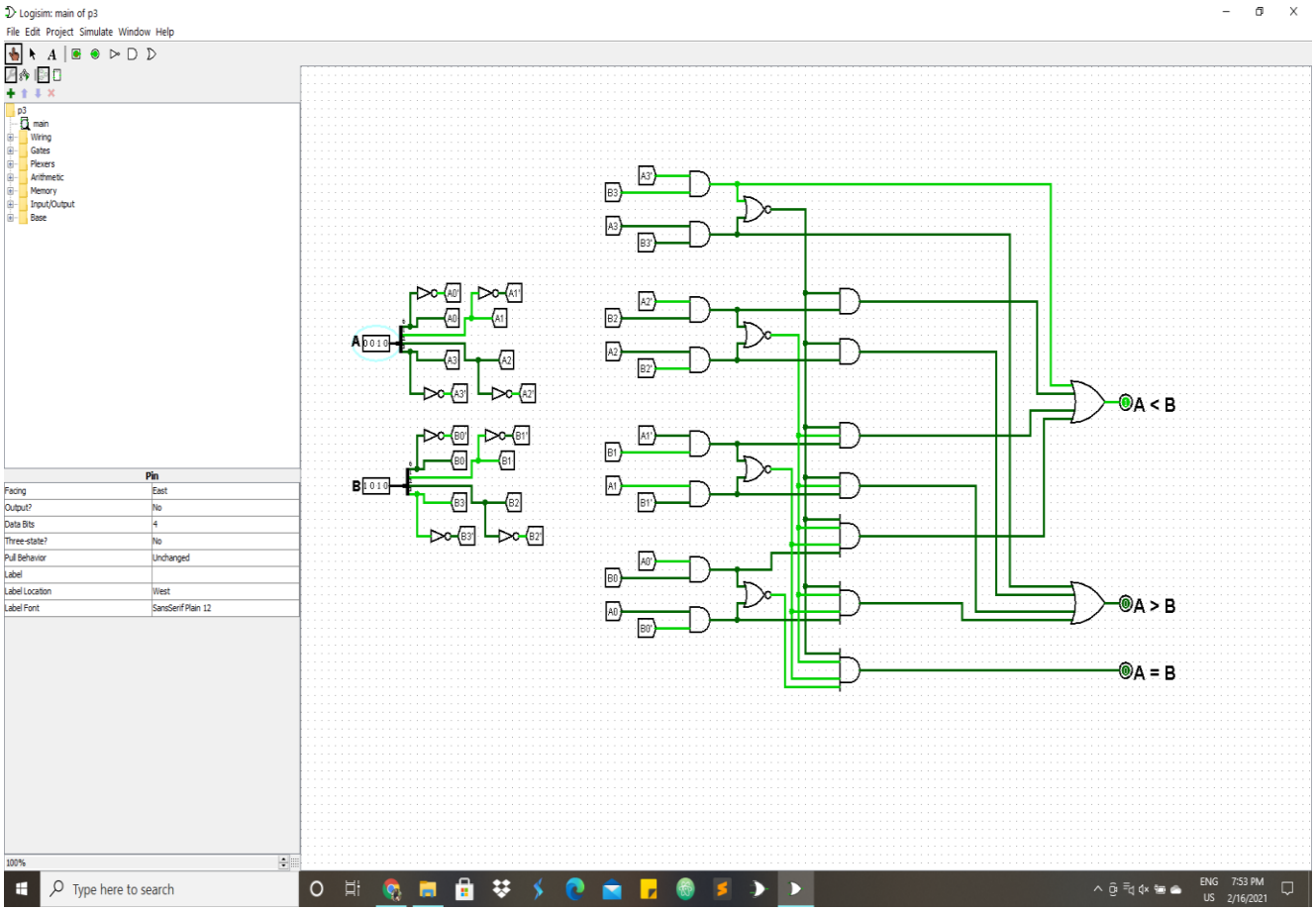
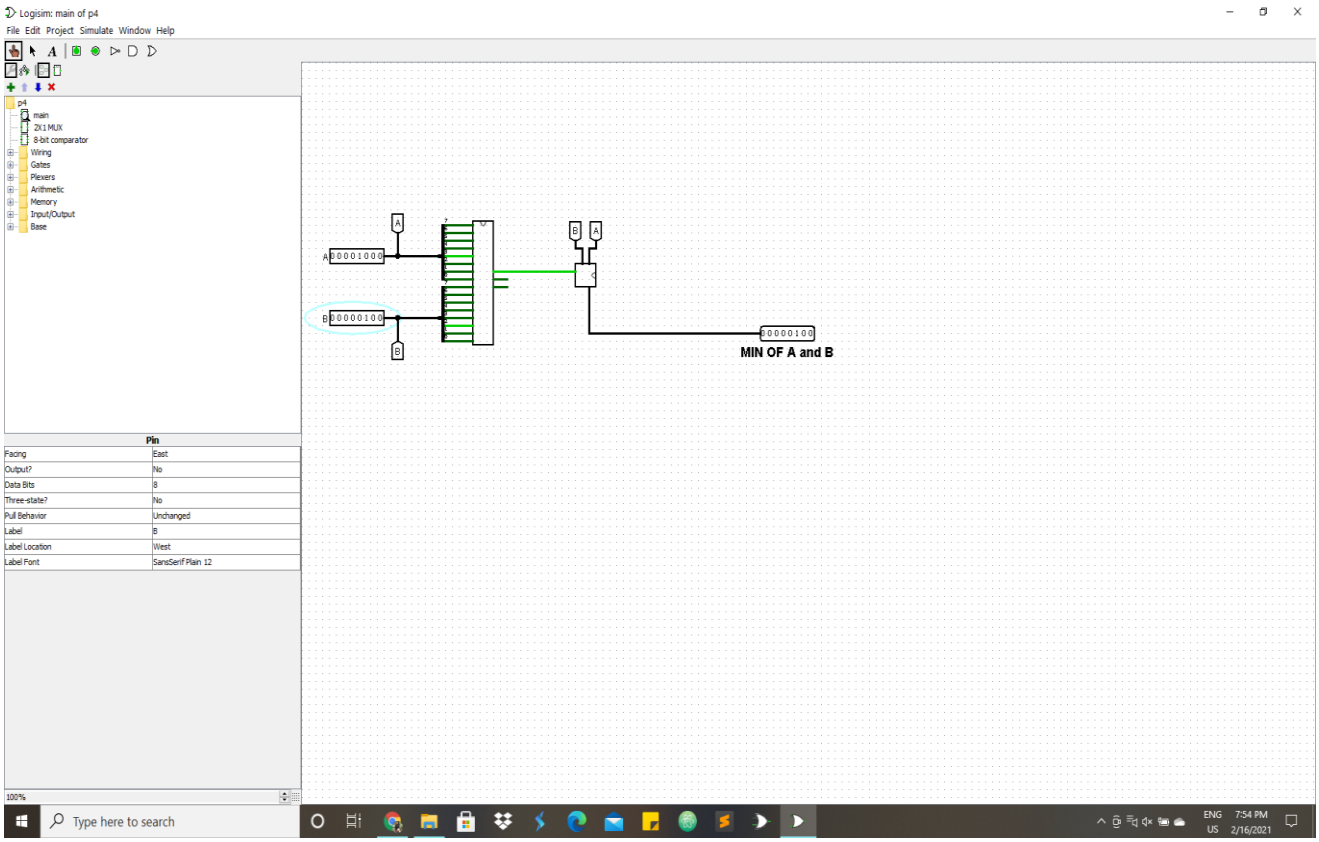
QUES 3:

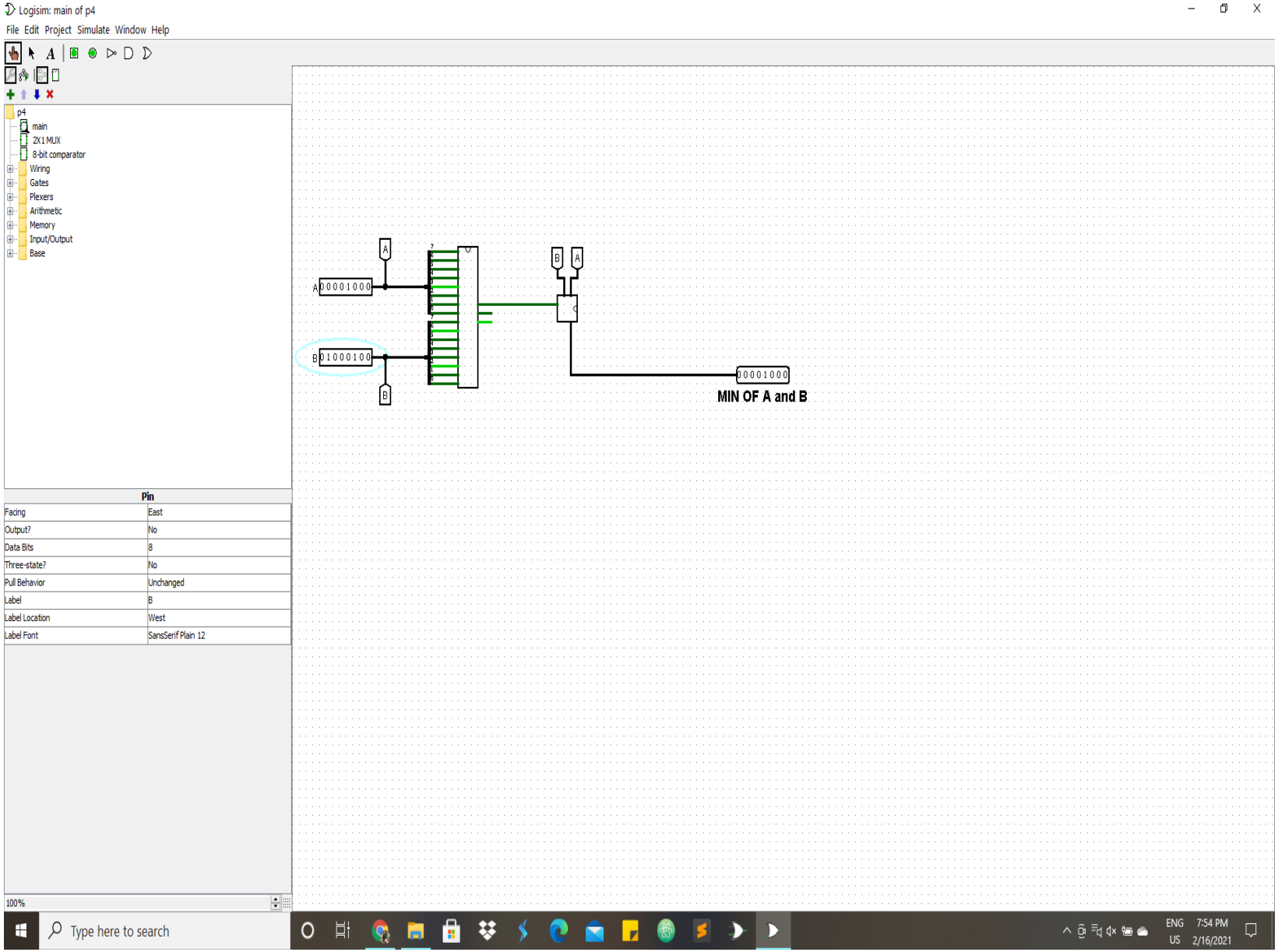

Logisim: main of p3
File Edit Project Simulate Window Help
p3
main
Wiring
Gates
Plexers
Arithmetic
Memory
Input/Output
Base
Pin
Facing East
Output? No
Data Bits 4
Three-state? No
Pull Behavior Unchanged
Label
Label Location West
Label Font SansSerif Plain 12
A
B
A < B
A > B
A = B
Type here to search
ENG US 7:53 PM 2/16/2021


Logisim: main of p3
File Edit Project Simulate Window Help
p3
main
Wiring
Gates
Plexers
Arithmetic
Memory
Input/Output
Base
Pin
Facing East
Output? No
Data Bits 4
Three-state? No
Pull Behavior Unchanged
Label
Label Location West
Label Font SansSerif Plain 12
A
B
A < B
A > B
A = B
Type here to search
ENG US 7:53 PM 2/16/2021


Logisim: main of p3
File Edit Project Simulate Window Help
p3
main
Wiring
Gates
Plexers
Arithmetic
Memory
Input/Output
Base
Pin
Facing East
Output? No
Data Bits 4
Three-state? No
Pull Behavior Unchanged
Label
Label Location West
Label Font SansSerif Plain 12
A
B
A < B
A > B
A = B
100%
Type here to search
ENG US 7:53 PM 2/16/2021

QUES 4:

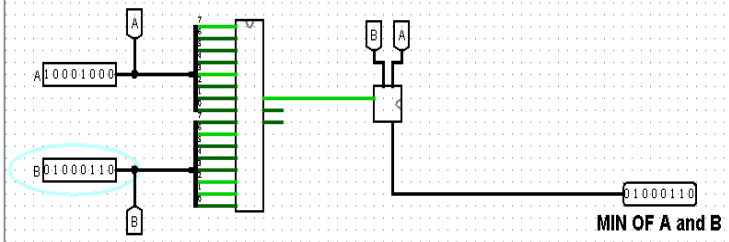

Logisim: main of p4
File Edit Project Simulate Window Help
p4
main
2X1 MUX
8-bit comparator
Wiring
Gates
Plexers
Arithmetic
Memory
Input/Output
Base
Pin
Facing East
Output? No
Data Bits 8
Three-state? No
Pull Behavior Unchanged
Label B
Label Location West
Label Font SansSerif Plain 12
A
B
MIN OF A and B
100%
Type here to search
ENG US 7:54 PM 2/16/2021


Logisim: main of p4
File Edit Project Simulate Window Help
p4
main
2X1 MUX
8-bit comparator
Wiring
Gates
Plexers
Arithmetic
Memory
Input/Output
Base
Pin
Facing East
Output? No
Data Bits 8
Three-state? No
Pull Behavior Unchanged
Label B
Label Location West
Label Font SansSerif Plain 12
100%
A
B
00001000
01000100
00001000
MIN OF A and B
Type here to search
ENG US
7:54 PM
2/16/2021

Logisim: main of p4

File Edit Project Simulate Window Help

- p4
  - main
  - 2X1 MUX
  - 8-bit comparator
  - Wiring
  - Gates
  - Plexers
  - Arithmetic
  - Memory
  - Input/Output
  - Base



Pin	
Facing	East
Output?	No
Data Bits	8
Three-state?	No
Pull Behavior	Unchanged
Label	B
Label Location	West
Label Font	SansSerif Plain 12

100%

Type here to search

ENG US 7:54 PM 2/16/2021