

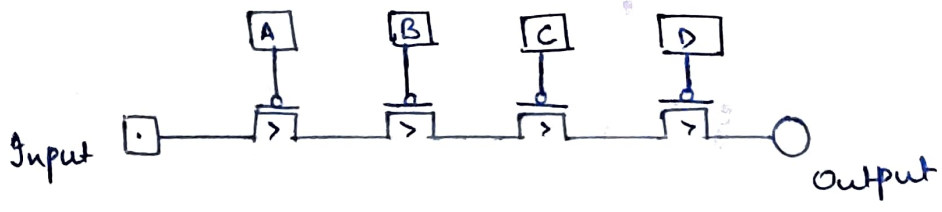
CS-226

SWITCHING THEORY LAB

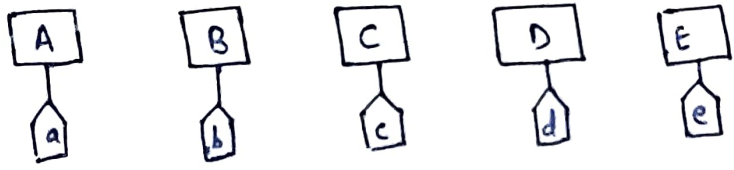
LAB-1

- TARUSI MITTAL
- 1901CS65
- Pamphlet
- 19/01/2021

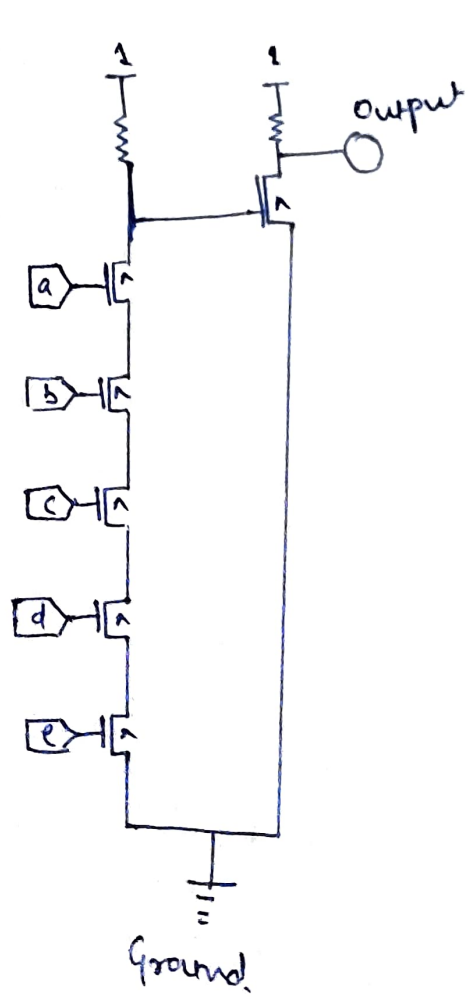
Ans 1: Series structure with 4 PMOS transistor...



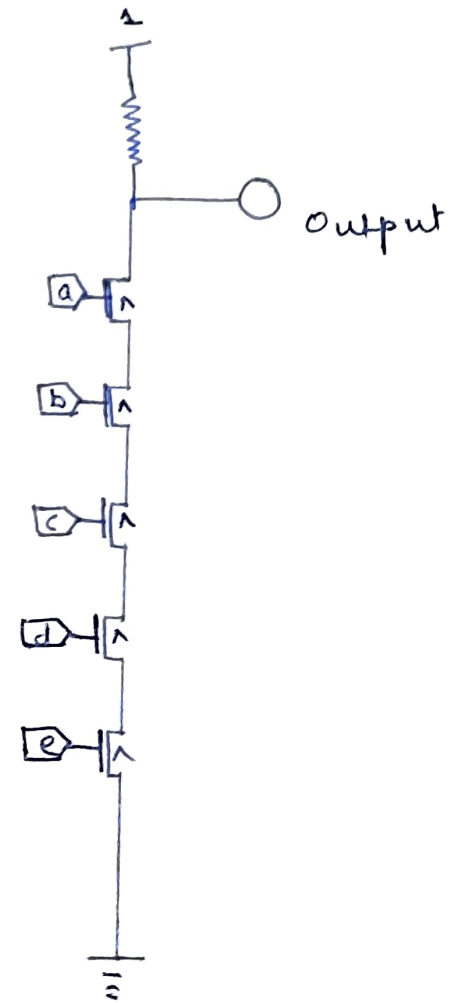
Ans 2:-



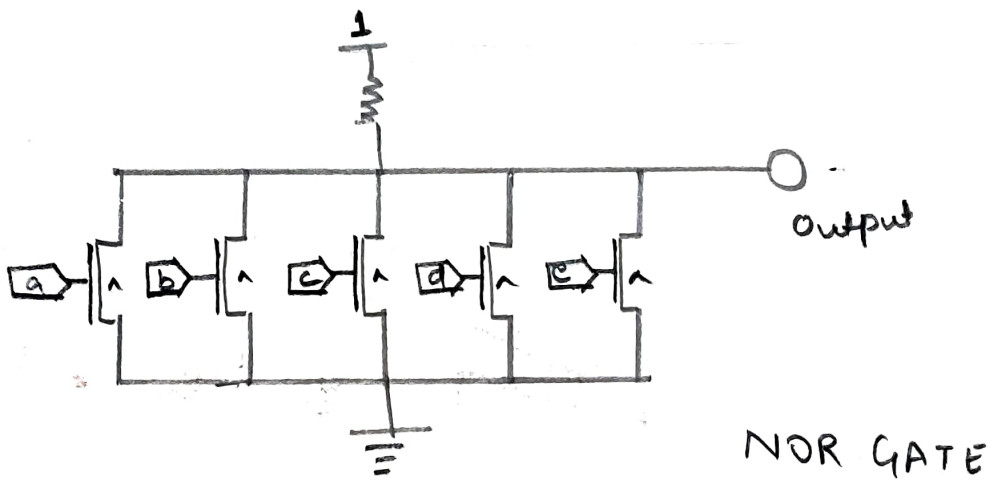
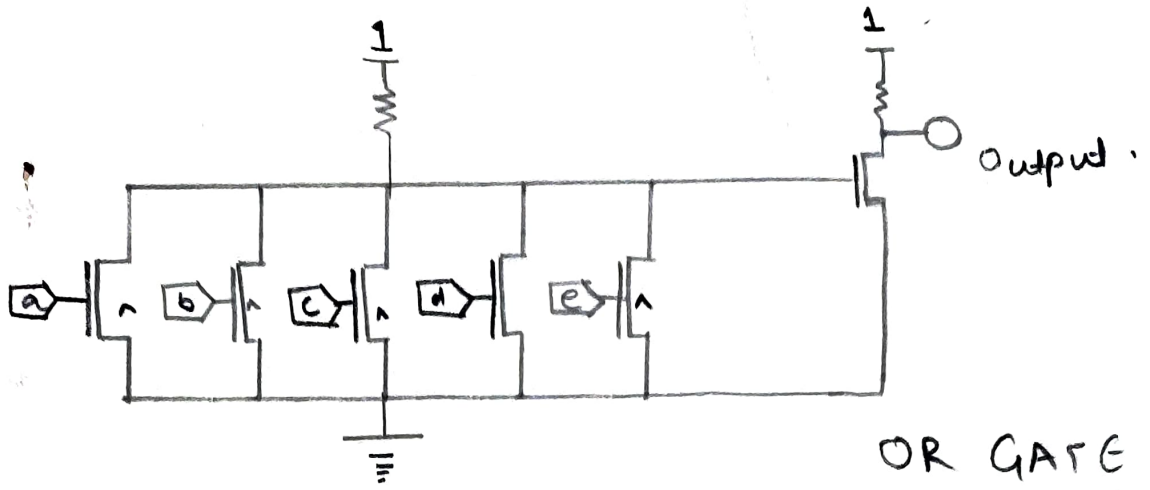
These tunnels will be used in 2nd, 3rd and 4th question.



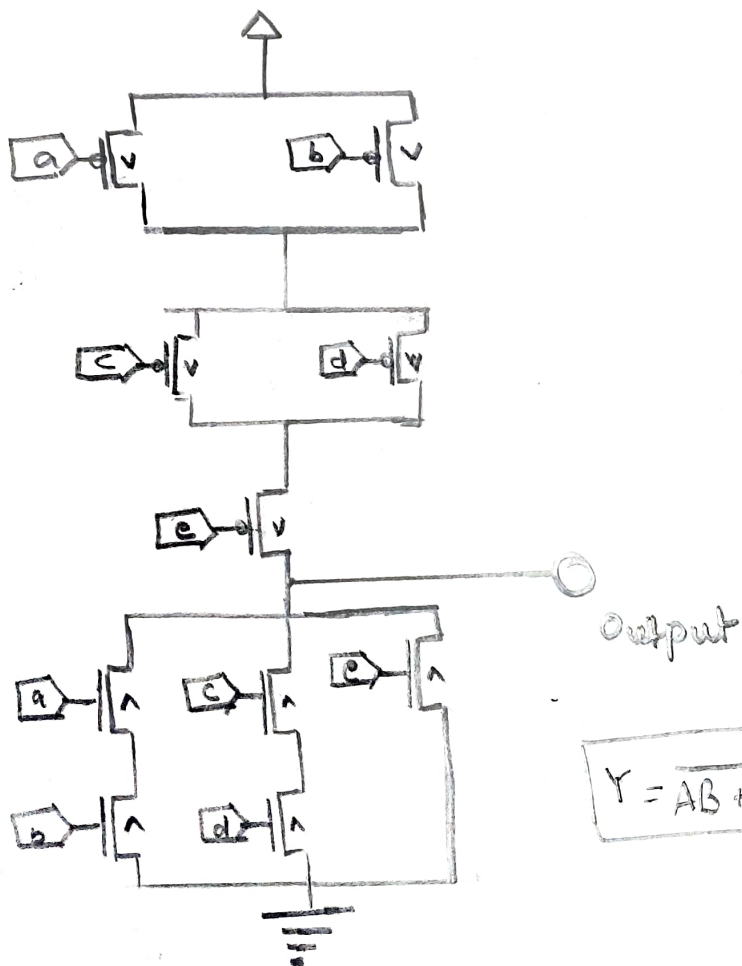
AND GATE



NAND GATE

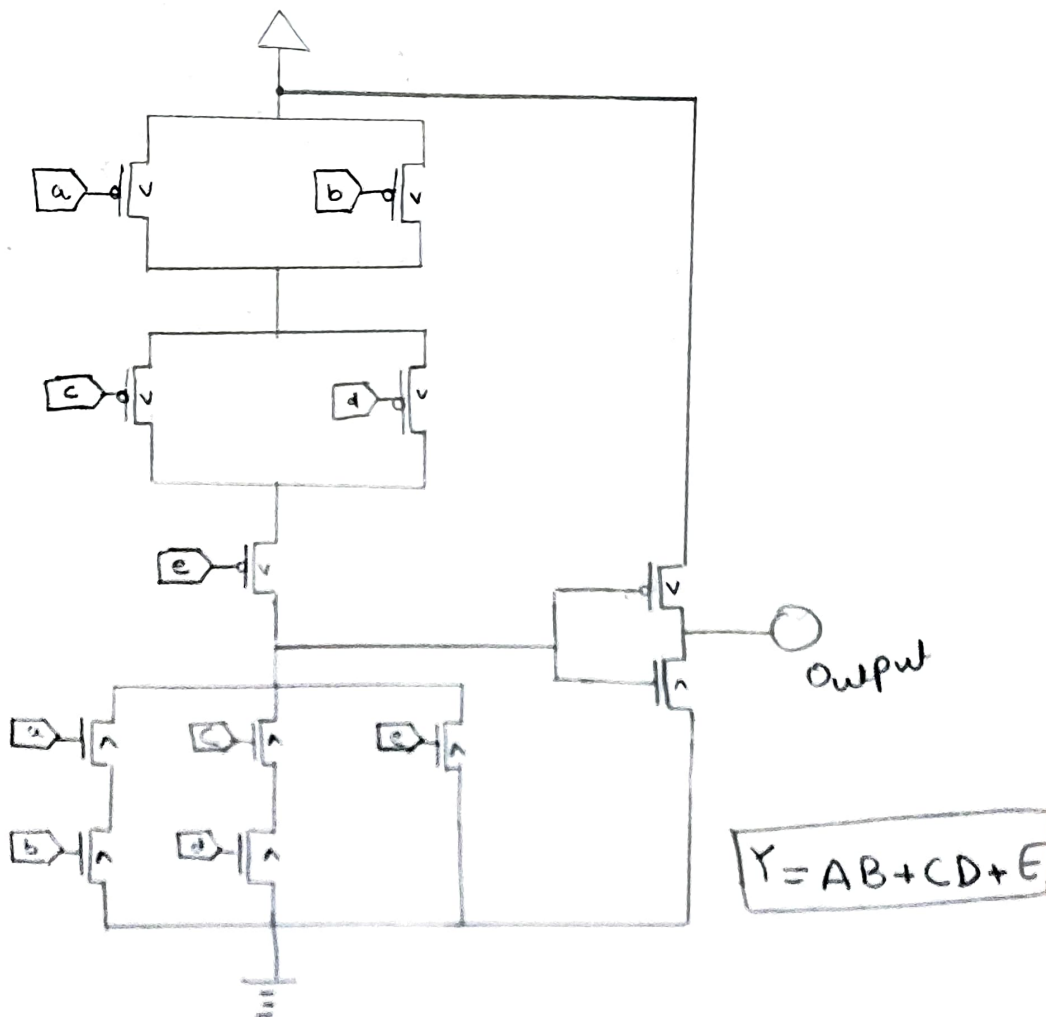


Ans 3:

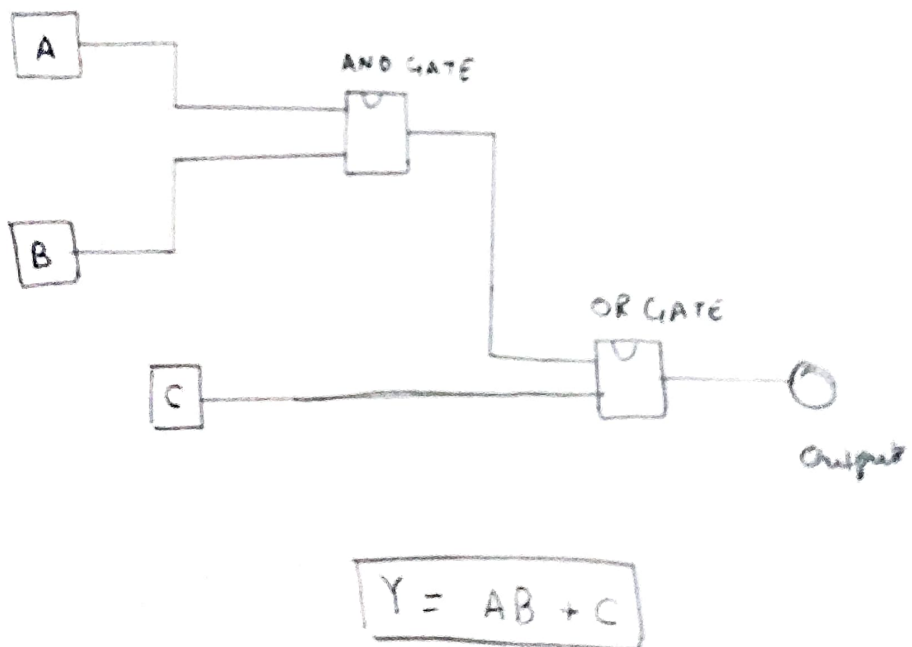


$$Y = AB + CD + E$$

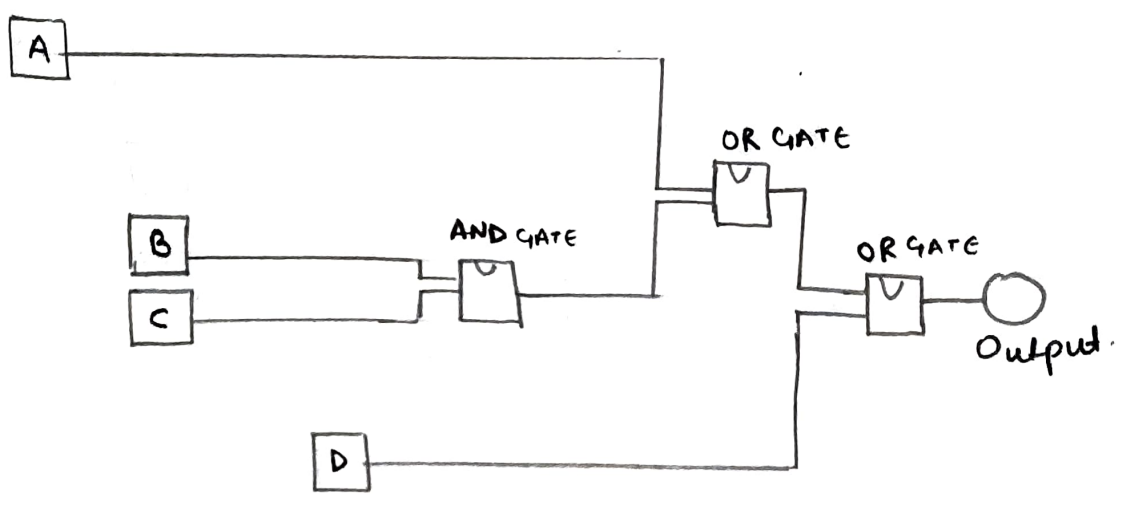
Ans 4:-



Ans 5:-



Aus 5:-



$$Y = AB + CD + E$$