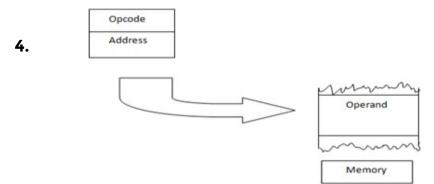
Name	
Student ID	
Section _	
Subject: CSI	F 331 _Fall 2022

Quiz 2 (Fall 2022)

Score _____

- 1. MOV AX, [0500] is an example of
- (A) direct addressing mode
- (B) immediate addressing mode
- (c) indexed addressing mode
- **D** register mode
- **2.** MOV CX, [481d]; assuming DS=2162H, logical address will be:
- (A) 2162:01E1
- **B**) 481:2162
- **(c**) 26301H
- **D** 2162:481
- **3.** MOV AL, 4929H; what will be the value of AL after execution?
- (**A**) 29H
- **B**) 49H
- **(c)** 01001001b
- **D** 4929H



which type of addressing mode the figure refers to?

- (A) immediate addressing mode
- (B) direct addressing mode
- c both A and B
- (**D**) none

- 5. What is the command used
- A) PUSH

At the beginning.

(B) POP

00F2H	
00F4H	
00F6H	
00F8H	
00FAH	
00FCH	
00FEH	
0100H	S

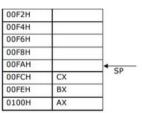
After the call, ____AX

00F2H		
00F4H		
00F6H		
00F8H		
00FAH		
00FCH		
00FEH		4
0100H	AX	SP

- 6. what is the command used
- (A) PUSH CX

At the beginning.

- **B**) POP CX
- (c) POP [00FCH]
- **D** Both B and C



After the call,

00F2H		
00F4H	3 6	
00F6H		
00F8H		
00FAH		
00FCH	10	◆ SP
00FEH	BX	
0100H	AX	

- 7. Whatisthevalue of BX after execution
- **A** 1212H

MOV AX, 1212h ; store 1212h in AX.

B 3434H

MOV BX, 3434h ; store 3434h in BX

(c) 1212

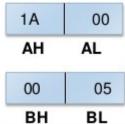
(D) 3434

PUSH AX; store value of AX in stack.
PUSH BX; store value of BX in stack.

POP AX ; set AX to original value of BX. POP BX ; set BX to original value of AX.



- (A) 05, 1A
- XCHG AH, BL
- **B**) 00,05
- **(c)** 1A,00
- **(D)** 1A,05



9. what is the value of AL after execution?

- (A) 35H
- (**B**) 7CH
- **(c**) 29
- (**D**) none
- AL = 53CL = 29 ADD AL, CL

10. what is the value of CF after execution?

ADD AL, CL

DAA

- (\mathbf{B}) 0

AL = 73CL = 29

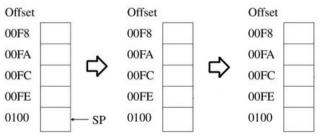
DAA

11. What are the value at 00FE and 00FC memory locations after execution?

(A) 5678,234

(B) 1234, 5678





Empty Stack

After PUSH AX

AFTER PUSH BX

- 12. Which is false?
- (A) Bus interface unit (BIU) is responsible for transferring the data addresses on the buses necessary for execution unit
- **B** Execution Unit receives program instruction codes and data from BIU, executes these instructions and store the result in general registers
- **(c)** Both
- (**D**) none

1.	a b c d 4.	a b c d 7.	a b c d 10.	
2.	a b c d 5.	a b c d 8.	(a) (b) (c) (d) 11.	abcd
3	\bigcirc	\bigcirc	\bigcirc \bigcirc \bigcirc \bigcirc \bigcirc \bigcirc \bigcirc 12	(a) (b) (c) (d)

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