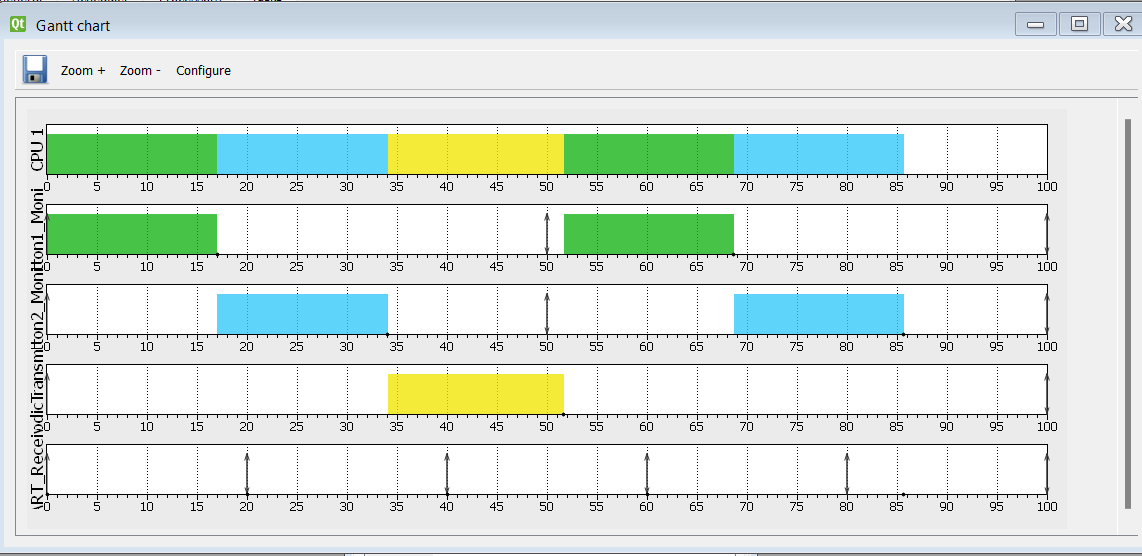
1. Using analytical methods calculate the following for the given set of tasks:

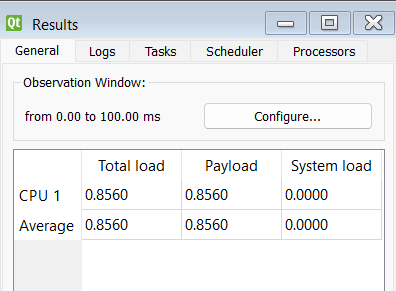
* System Hyper-period = LCM(10, 20, 50, 100) = 100 milliseconds
* Calculate the CPU load
  + For only four tasks = ((17\*2 + 17\*2 + 17.6\*1 + 0.00084\*5) /100)\*100 = 85.6%
  + After adding heavier loads = ((17\*2 + 17\*2 + 17.6\*1 + 0.00084\*5 + 5\*10 + 12\*1) /100)\*100 = 147.6%

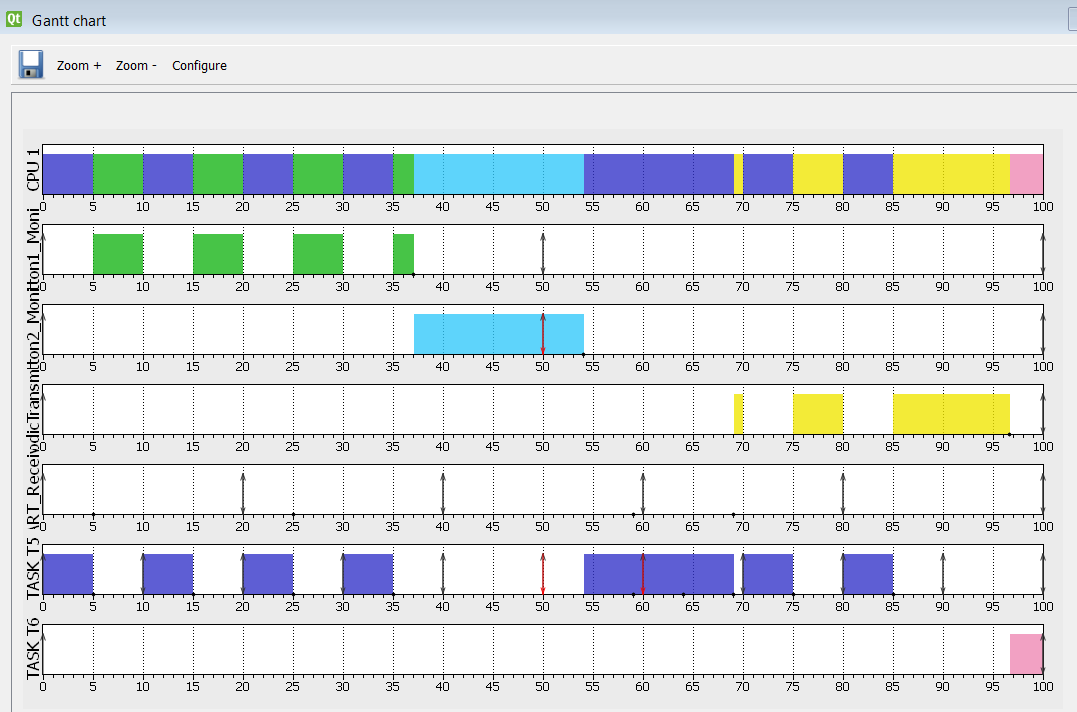
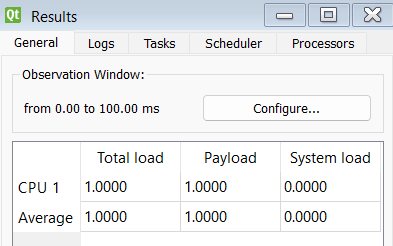
Comment: After adding 2 heavier loads the system became overloaded and unschedulable as tasks will definitely start missing their deadline.

* Check system schedualability using URM (Assuming the given set of tasks are scheduled using a fixed priority rate -monotonic scheduler)
  + For only four tasks:
    - U = ( 17/50 + 17/50 + 17.6/100 + 0.00084/20) =0.856
    - Urm = n\*(2^(1/n) -1) = 4\*( 2^(1/4) – 1 ) = 0.7568
    - U>Urm
    - Comment: It is guaranteed the system is unschedualable for a fixed priority rate-monotonic scheduler.
  + After adding heavier loads:
    - U = ( 17/50 + 17/50 + 17.6/100 + 0.00084/20 + 5/10 + 12/100) = 1.476
    - Urm = n\*(2^(1/n) -1) = 6\*( 2^(1/6) – 1 ) = 0.7348
    - U>Urm
    - Comment: It is guaranteed the system is unschedualable for a fixed priority rate-monotonic scheduler.
* Check system schedualability using time demand analysis technique (Assuming the given set of tasks are scheduled using a fixed priority rate -monotonic scheduler)
  + For only four tasks:
    - Tasks priority are in this order: Uart\_Receiver, Button1\_Monitor, Button2\_Monitor, Periodic\_Transmitter
    - Tasks’ Calculations are as follows:
      * Uart\_Receiver: W(20) = 0.00084 + 0 =0.00084<20; therefore, schedualable.
      * Button1\_Monitor: W(50) = 17 + ((50/20)\*0.00084) = 17.00252<50; therefore, schedualable.
      * Button1\_Monitor: W(50) = 17 + ((50/50)\*17) + ((50/20)\*0.00084) = 34.00252<50; therefore, schedualable.
      * Periodic\_Transmitter: W(100) = 17.6 + ((100/50)\*17) + ((100/50)\*17) + ((100/20)\*0.00084) = 85.6042<100; therefore, shedualable.
    - Comment: The system is schedualable with the previous four tasks as no task misses its deadline. All calculations are taken at the instance of task deadline as it’s the critical point at which if time needed is less than time provided then task can be scheduled.
  + After adding heavier loads:
    - Tasks priority are in this order: Load1\_Simulation, Uart\_Receiver, Button1\_Monitor, Button2\_Monitor, Periodic\_Transmitter, Load2\_Simulation
    - Tasks’ Calculations are as follows:
      * Load1\_Simulation: W(10) = 5 + 0 = 5<10; therefore, schedualable.
      * Uart\_Receiver: W(20) = 0.00084 + ((20/10)\*5) = 10.00084<20; therefore, schedualable.
    - Button1\_Monitor: W(50) = 17 + ((50/20)\*0.00084) + ((50/10)\*5) = 42.00252< Tasks priority are in this order: Uart\_Receiver, Button1\_Monitor, Button2\_Monitor, Periodic\_Transmitter
    - Tasks’ Calculations are as follows:
      * Uart\_Receiver: W(20) = 0.00084 + 0 =0.00084<20; therefore, schedualable.
      * Button1\_Monitor: W(50) = 17 + ((50/20)\*0.00084) = 17.00252<50; therefore, schedualable.
      * Button1\_Monitor: W(50) = 17 + ((50/50)\*17) + ((50/20)\*0.00084) = 34.00252<50; therefore, schedualable.
      * Periodic\_Transmitter: W(100) = 17.6 + ((100/50)\*17) + ((100/50)\*17) + ((100/20)\*0.00084) = 85.6042<100; therefore, shedualable.
      * ; therefore, schedualable.
      * Button1\_Monitor: W(50) = 17 + ((50/50)\*17) + ((50/20)\*0.00084) = 34.00252<50; therefore, schedualable.
      * Periodic\_Transmitter: W(100) = 17.6 + ((100/50)\*17) + ((100/50)\*17) + ((100/20)\*0.00084) = 85.6042<100; therefore, shedualable.
      * Load2\_Simulation: W(100)
    - Comment: It is guaranteed the system is unschedualable for a fixed priority rate-monotonic scheduler.

2. Using Simso offline simulator, simulate the given set of tasks assuming:

* Fixed priority rate monotonic scheduler
  + For only four tasks:

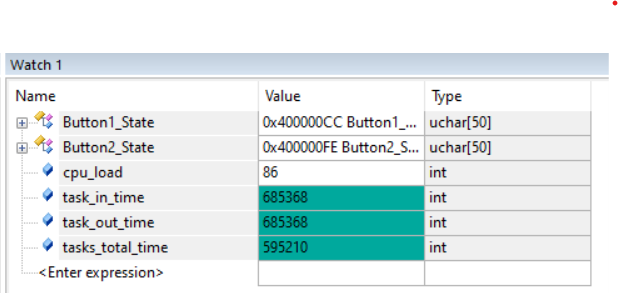


* + After adding heavier loads:

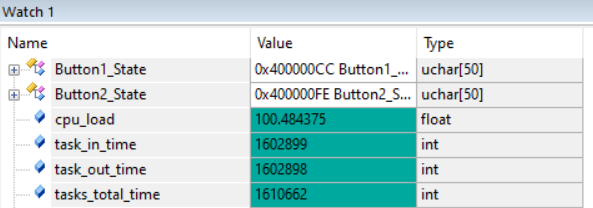
Comment: Analytical method and Simso offline simulator both got the same results, 85.6% when only four tasks are in the system and the system being overloaded after adding 2 heavier loads simulated as empty for loops for 5 and 12 milliseconds, tasks start missing their deadline.

3. Using Keil simulator in run-time and the given set of tasks:

* Calculate the CPU usage time using timer 1 and trace macros
  + For only four tasks: cpu\_load = 86%

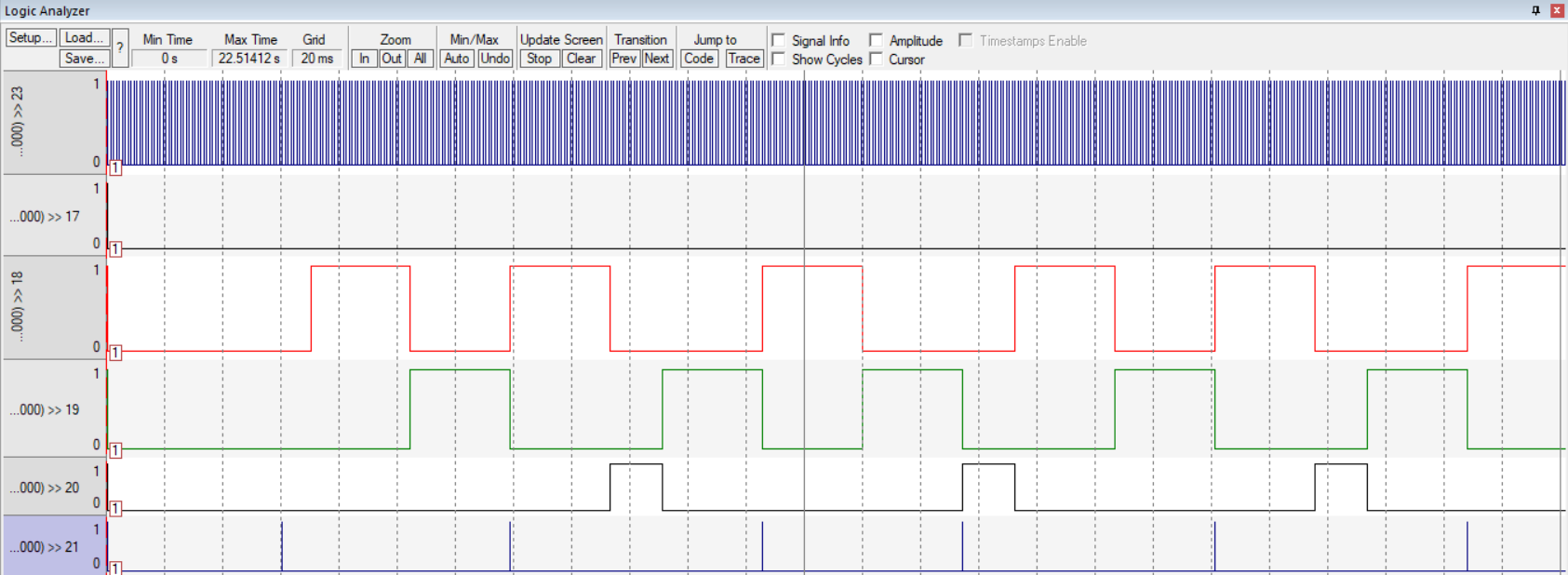


* + After adding heavier loads: cpu\_load = 100.48% (overloaded)

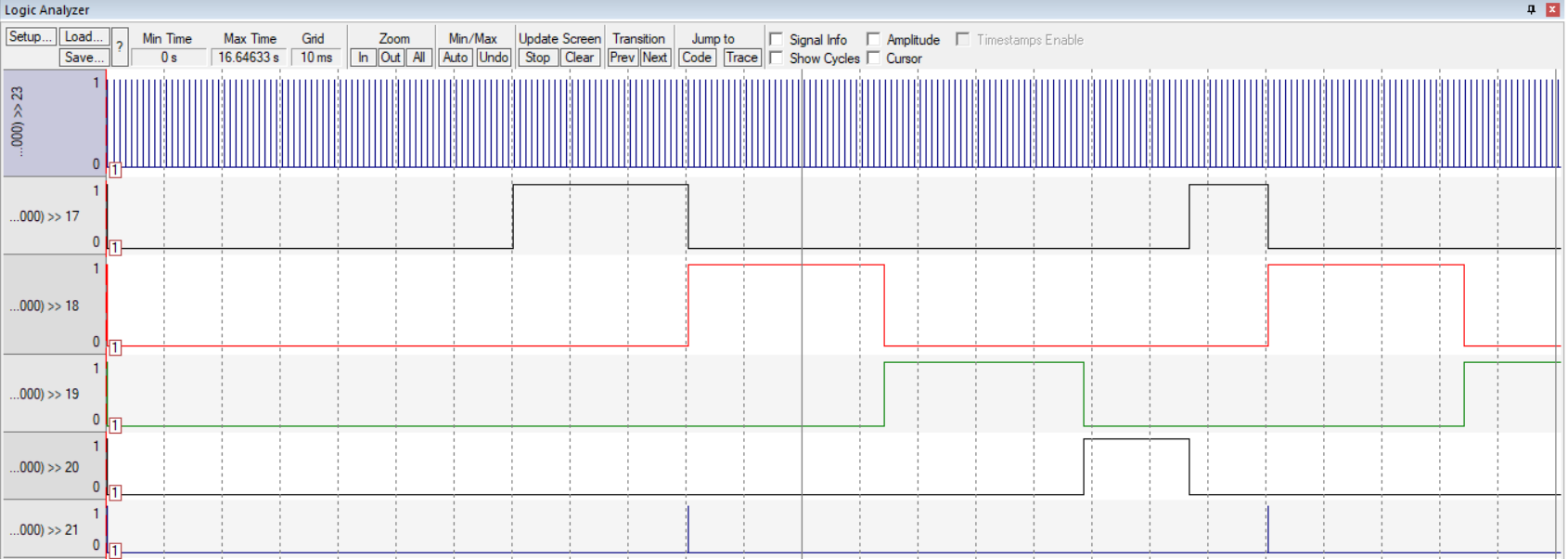


Comment: Same CPU Load numbers from analytical method and Simso Offline Simulator.

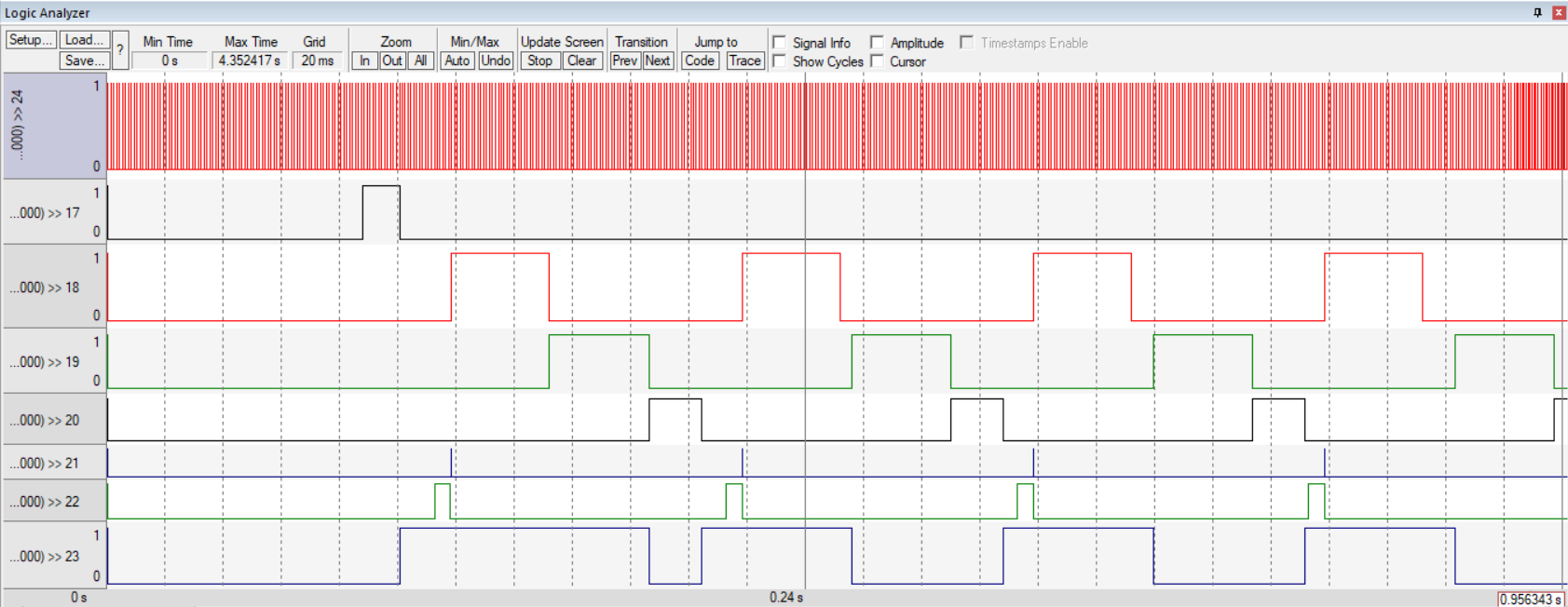
* Using trace macros and GPIOs, plot the execution of all tasks, tick, and the idle task on the logic analyzer:
  + For only four tasks: (Tick Hook, Idle Task, Button1-Monitor, Button2-Monitor, Periodic-Transmitter, Uart-Receiver, Load1-Simulaion and Load2-Simulation respectively)



Comment: System starts with scheduling Uart\_Receiver task since it has the nearest deadline at 10 milliseconds then Button1\_Monitor and Button2\_Monitor at the same deadline of 50 milliseconds but since Button1\_Monitor was pushed first to the EDF\_ReadyList so it got scheduled first. Then finally ending by Periodic\_Transmitter Task at the farthest deadline which is 100 milliseconds and then repeating all over again throughout the timeline. It’s also observed that the Tick Hook is so cloudy as it comes every 1 millisecond. Since the Periodic\_Transmitter Task has got the farthest deadline and each task is set to come every 60 milliseconds using vTaskDelayUntil() function, it constantly gets interrupted throughout the timeline. To fix this we can increase the period at which each task comes to be 100 milliseconds and now no preemption would take place, yet the idle task would be schedules as there will be periods of no task running as shown below:



* + After adding heavier loads: (Tick Hook, Idle Task, Button1-Monitor, Button2-Monitor, Periodic-Transmitter, Uart-Receiver, Load1-Simulaion and Load2-Simulation respectively)



Comment: Now the system starts with Load1\_Simulation as it has the nearest deadline at 10 milliseconds then Uart\_Receiver at a deadline of 20 milliseconds and so on according to nearest deadline, yet the system is already overloaded so many tasks miss their deadline making the system unschedualable as was previously observed from Simso Offline Simulator and the analytical method calculations. Tick Hook is still cloudy as it comes every 1 millisecond.