Summary for CPEN 412

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1 Memory Systems

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                    ┌─────────────────────────┐
                    │   Sequential Memory     │
                    └─────────────────────────┘
                       /                    \
                      /                      \
         ┌──────────────┐              ┌──────────────┐
         │   Volatile   │              │  Non-Volatile│
         └──────────────┘              └──────────────┘
            /       \                   /      │      \
           /         \                 /       │       \
    ┌─────────┐  ┌─────────┐  ┌──────────────┐ ┌────────┐ ┌────────┐
    │ SPI Ram │  │ I²C RAM │  │NAND Flash EPROM│ │ I²C ROM│ │ SPI ROM│
    └─────────┘  └─────────┘  └──────────────┘ └────────┘ └────────┘
```
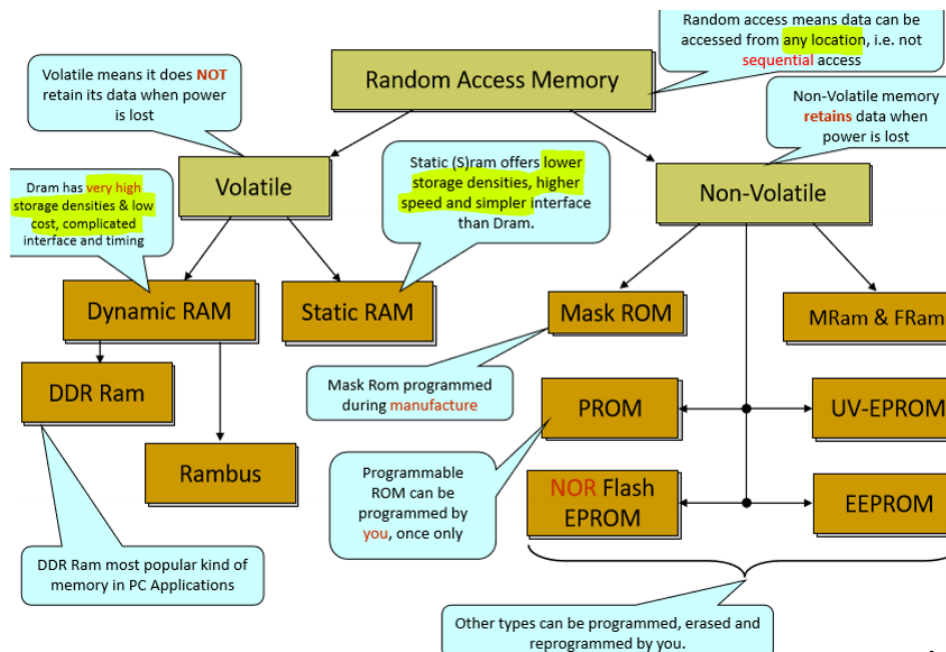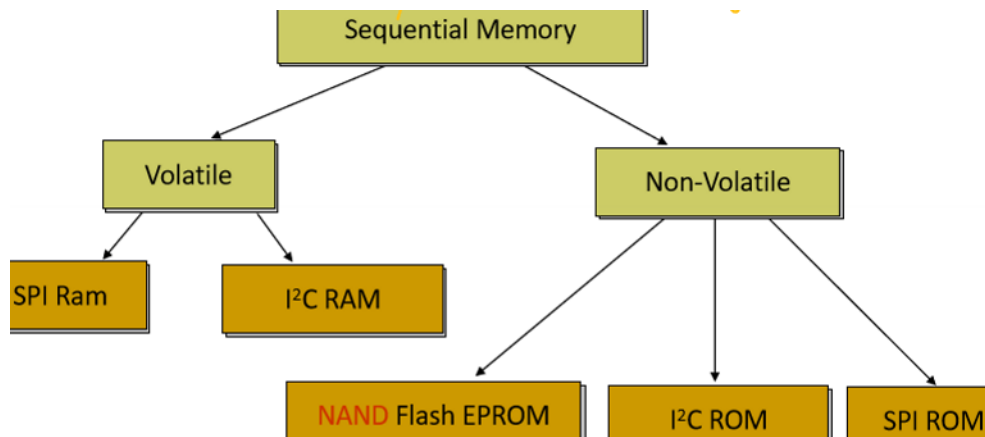
Sequential Access Memory (SAM) is not randomly accessible. An access is made by first writing an address to them, they respond with data from successive locations with each subsequent read/write access.