

Large Scale Quantum Chemistry with Tensor Processing Units

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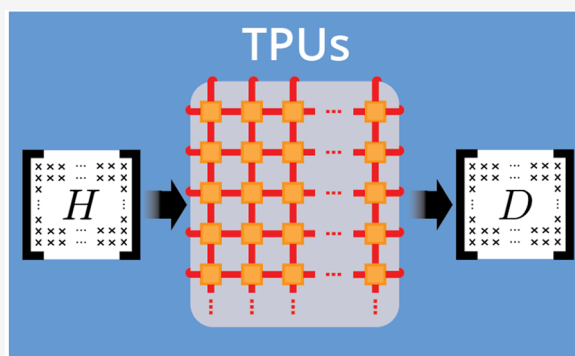


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ABSTRACT: We demonstrate the use of Google's cloud-based Tensor Processing Units (TPUs) to accelerate and scale up conventional (cubic-scaling) density functional theory (DFT) calculations. Utilizing 512 TPU cores, we accomplish the largest such DFT computation to date, with 247848 orbitals, corresponding to a cluster of 10327 water molecules with 103270 electrons, all treated explicitly. Our work thus paves the way toward accessible and systematic use of conventional DFT, free of any system-specific constraints, at unprecedented scales.



INTRODUCTION

Computational methods for quantum chemistry and quantum physics have proven to be invaluable tools in modern scientific research and technological innovation. The application space of such methods is vast, ranging from the prediction of novel high-temperature superconductors¹ to the acceleration of drug discovery² and from the study of catalytic processes for e.g. CO₂ sequestration³ and plastic recycling⁴ to the design of nanomaterials,⁵ solar cells,⁶ and batteries.⁷

In the landscape of quantum-based computational methods, density functional theory (DFT) especially stands out due to its ability to produce accurate results for a wide range of systems at a relatively low computational cost.⁸ Accordingly, an impressive amount of computational research utilizes DFT calculations each year. For instance, the US National Energy Research Scientific Computing Center (NERSC) reported that nearly 30% of their supercomputer resources in 2018 were spent on DFT calculations alone.⁹ Widespread research and development efforts are continuously devoted toward optimizing the performance and accuracy of DFT calculations, giving rise to a plethora of open-source and commercial DFT software packages.¹⁰ Several packages can leverage specialized hardware, such as general-purpose graphics processing units (GPUs), for most of the workload.^{11–17} However, in conventional DFT implementations, i.e., without specific sparsity assumptions for the density matrix or Hamiltonian matrix, the computational cost scales as the third power of the number N of orbitals used to describe the system (referred to as $O(N^3)$ DFT throughout this work), and this cubic scaling often makes simulating large systems, such as protein–ligand complexes or metal–organic frameworks,¹⁸ prohibitively expensive.

Google's Tensor Processing Units (TPUs) are application-specific integrated circuits originally designed to accelerate large-scale machine learning workloads.^{19–23} By leveraging the JAX library,^{21–23} it is nevertheless possible to repurpose TPUs for other computational tasks.^{24–35} In this work, we demonstrate the use of TPUs as quantum chemistry supercomputers by accelerating the $O(N^3)$ computational bottleneck of DFT approaches which use an auxiliary single-particle kinetic energy approximation, such as Kohn–Sham (KS)^{36,37} and generalized KS (gKS)³⁸ DFT, where gKS admits hybrid DFT functionals. This enables the systematic study of quantum chemistry problems at unprecedented scales. As a concrete demonstration, we performed end-to-end $O(N^3)$ DFT calculations on large clusters of water molecules, reaching a total of $N = 247\,848$ DFT orbitals, corresponding to 10 327 water molecules with 103 270 electrons, see Figure 1 and Table 1. To our knowledge, this is the largest $O(N^3)$ DFT calculation to date, with the previously largest computation consisting of a single $O(N^3)$ DFT iteration with $N \approx 230\,000$ orbitals on Fujitsu's K computer.³⁹

Some variants of DFT, most notably linear-scaling DFT,^{40–43} avoid the $O(N^3)$ bottleneck altogether and can thus reach an even larger number of orbitals. However, these variants rely on additional approximations and conditions, such

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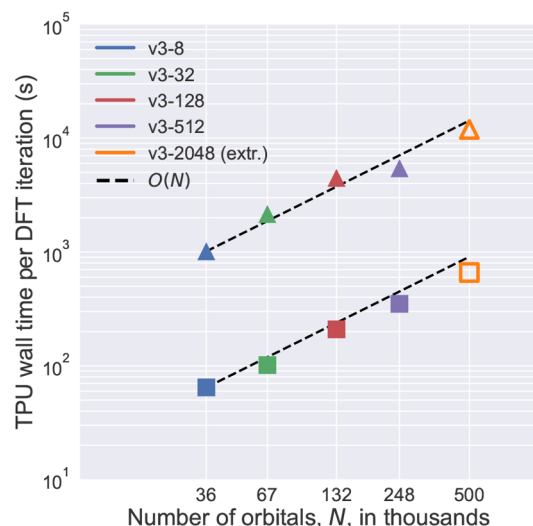


Figure 1. TPU v3 wall time for $O(N^3)$ density matrix purification, eqs 5–7, as a function of the number N of orbitals, for clusters of water molecules, both in single (squares) and double (triangles) precision. A full TPU v3 pod with 2048 cores and 32 TB of memory is expected to handle $N \sim 500\,000$ orbitals in our current implementation (extrapolated results necessitated by temporary resource unavailability).

as truncating density matrix elements,⁴⁴ or on special properties of only a subset of density functionals (such as semilocal density functional approximations). In turn, this results in restricted applicability, with e.g. linear-scaling DFT being suitable for insulating systems but not for metals or systems with a small energy gap.⁴⁰ In practice, conventional $O(N^3)$ DFT is a more preferable choice since it alleviates technical complexity and problem space restrictions associated with current lower-scaling methods, greatly extending the domain of problems to which DFT can be applied reliably and with relative ease.

There are many aspects that go into an $O(N^3)$ DFT calculation. Throughout, we focus on atom-centered basis sets with all electrons treated explicitly, that is we do not consider e.g. plane waves or pseudopotentials. At a high level, one can identify two main computational steps: (a) building the DFT Hamiltonian matrix (with cost $O(N)$ to $O(N^2)$) and (b) computing the ground state density matrix ($O(N^3)$), see Figure 2.

(a) *DFT Hamiltonian build*: Given a choice of N atom-centered basis functions $\chi_i(\mathbf{r})$, one needs to compute the DFT Hamiltonian matrix H and the overlap matrix S , with coefficients given by

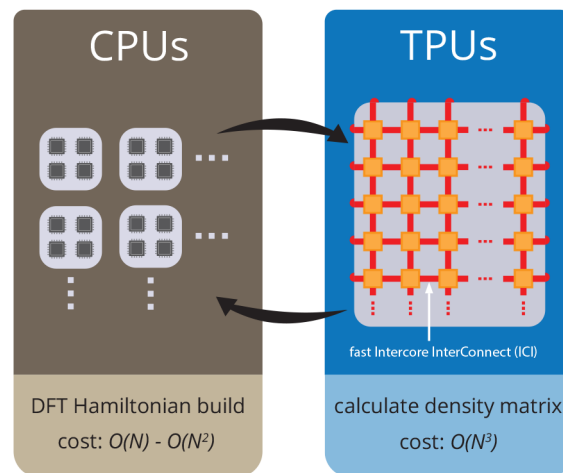


Figure 2. Two main steps of our implementation of an $O(N^3)$ DFT computation, the Hamiltonian build and computing the ground state density matrix, which we run on CPUs and TPUs, respectively. The DFT code FHI-aims^{46,47} is used to set up the Hamiltonian, and the ELSI library^{48,49} is used to facilitate the integration of the TPU-based solver to FHI-aims.

$$H_{ij} = \langle \chi_i | \mathcal{H} | \chi_j \rangle, \quad S_{ij} = \langle \chi_i | \chi_j \rangle \quad (1)$$

where \mathcal{H} represents the DFT Hamiltonian in the continuum, and each matrix coefficient requires computing one or several integrals. Over the past few decades, much effort has been devoted to optimizing the build of the $N \times N$ matrix H . Naively, the computational time here scales as $O(N^4)$; however, in many implementations, the scaling is effectively reduced to $O(N^2)$ due to two-electron integral screening methods. The scaling can be further reduced to almost $O(N)$ if other strategies, such as fast multipole methods⁴⁵ or fast Fourier transform based methods,⁴³ such as the Ewald method for periodic systems, are employed. In this work, we do *not* attempt to accelerate the Hamiltonian or overlap matrix build times with TPUs. Instead, we simply use a well-established all-electron DFT package, the *Fritz Haber Institute ab initio molecular simulations package* (FHI-aims),^{11,46,47} which we run using CPUs.

(b) *Density matrix purification*: The pair of matrices H and S defines a generalized eigenvalue problem, the so-called KS equations

$$H|\phi_\alpha\rangle = e_\alpha S|\phi_\alpha\rangle \quad (2)$$

where $|\phi_\alpha\rangle$ and e_α are the KS orbitals and energies. Our goal is to compute the ground state density matrix

Table 1. Tabulated Results in Figure 1, Including Also the Number of Atoms and Electrons^a

no. of orbitals	no. of atoms	no. of electrons	TPU configuration	TPU wall time (s)		relative energy per molecule (mHa)
				FP32	FP64	
35 544	4 443	14 810	v3-8	65	1 012	0.934
65 668	8 211	27 370	v3-32	102	2 150	0.531
131 544	16 443	54 810	v3-128	209	4 465	0.291
247 848	30 981	103 270	v3-512	350	5 434	0

^aWall times for the matrix purification step are shown both for single (FP32) and double (FP64) precision. Energies are relative to the largest calculation, $E[(\text{H}_2\text{O})_{N_{\text{mol}}}] / N_{\text{mol}} - E[(\text{H}_2\text{O})_{10327}] / 10327$, where N_{mol} is the total number of water molecules. In this sequence, we used a number of TPU cores that grow roughly as N^2 . As a result, walltimes are seen to roughly scale linearly in N , instead of the expected $O(N^3)$ scaling.

$$D \equiv \sum_{\alpha=1}^N \theta(\mu - \epsilon_{\alpha}) |\phi_{\alpha}\rangle \langle \phi_{\alpha}| \quad (3)$$

where $\theta(x)$ is the step function, and μ is the chemical potential, chosen such that $\sum_{\alpha=1}^N \theta(\mu - \epsilon_{\alpha}) = N_e$, where N_e is the number of electrons in the system. The density matrix D can be obtained by solving the KS equations (2) using standard linear algebra libraries, such as LAPACK⁵⁰ or Intel MKL.⁵¹ An alternative route, which we follow in this work, is to use a density matrix purification scheme.^{52,53} First, by computing the inverse square root of S ,

$$S \mapsto S^{-\frac{1}{2}} \quad (4)$$

we can write the Hamiltonian in an orthonormal basis

$$H \mapsto \tilde{H} \equiv S^{-\frac{1}{2}} H S^{-\frac{1}{2}}. \quad (5)$$

and re-express (2) as a standard eigenvalue problem $\tilde{H}|\tilde{\phi}_{\alpha}\rangle = \epsilon_{\alpha}|\tilde{\phi}_{\alpha}\rangle$, where $|\tilde{\phi}_{\alpha}\rangle \equiv S^{1/2}|\phi_{\alpha}\rangle$. Next, we compute the density matrix \tilde{D}

$$\tilde{H} \rightarrow \tilde{D} \equiv \theta(\mu I - \tilde{H}) = \sum_{\alpha=1}^{N_e} \theta(\mu - \epsilon_{\alpha}) |\tilde{\phi}_{\alpha}\rangle \langle \tilde{\phi}_{\alpha}| \quad (6)$$

and finally re-express it in the original basis

$$\tilde{D} \mapsto D \equiv S^{-\frac{1}{2}} \tilde{D} S^{-\frac{1}{2}}. \quad (7)$$

The transformation in eq 6 is obtained using a standard density matrix purification scheme that is suitable for TPUs, namely the hole-particle canonical purification scheme,⁵² which we elaborate on later in the paper.

If no further modifications are made (e.g., density matrix truncations in linear-scaling DFT), then the cost of computing D , whether by solving eq 2 or performing the four matrix transformations in eqs 4–7, scales as $O(N^3)$. This constitutes what is known as the *cubic wall* of DFT.

The density matrix D is used to derive several important quantities. The real-space electron density $n(\mathbf{r})$ is given by

$$n(\mathbf{r}) = \sum_{i,j} \chi_i(\mathbf{r}) D_{ij} \chi_j(\mathbf{r}) \quad (8)$$

which can be computed on a real-space grid.⁴⁶ The sum of occupied KS eigenvalues, given by $\text{Tr}(H D) = \text{Tr}(\tilde{H} \tilde{D})$, is also used to compute the total ground-state energy. Additionally, the *energy weighted density matrix* Q

$$Q = D H D \quad (9)$$

is also useful to compute atomic forces analytically.⁴⁶

■ RESULTS

DFT with TPUs. The main result of our work is the successful use of TPUs to perform the four matrix transformations 4–7, thereby tackling the $O(N^3)$ computational bottleneck of DFT. We employed TPUs of the third generation, denoted v3. A single TPU v3 core contains two matrix multiply units (MXUs) to formidably accelerate matrix–matrix multiplication (matmul), resulting in about 10 teraFLOPS (floating point operations per second) of measured single-core matmul performance in single precision. Importantly for our purposes, matmuls are also available in double precision using a software-emulated 57-bit floating point format. In this approach, utilized algorithms require many more single precision floating point operations when operating

in our emulated double precision than in single precision, and as a result, matmuls in double precision take $\sim 11\times$ longer than in single precision.

The smallest available TPU configuration consists of 8 TPU v3 cores with a total of 128 GB of dedicated high bandwidth memory (HBM), controlled by a single host with 48 CPU cores. The largest configuration is a pod with 2048 TPU v3 cores and 32 TB of HBM, controlled by 256 hosts. Given a choice of configuration, the available TPU cores are directly connected to nearest neighbors in a 2D torus network through fast intercore interconnects (ICIs), see Figure 2. The ICIs are critical to maintaining high performance when distributing matmuls and other dense linear algebra operations over all available TPU cores. In this work, we used the JAX library^{21–23} to write *single program multiple data* (SPMD) code and executed it on configurations made of p TPU cores, denoted v3- p , for $p = 8, 32, 128$, and 512.

The TPU hardware architecture is especially suited for dense large-scale matmuls, which we perform in distributed form using the SUMMA algorithm,⁵⁴ as recently demonstrated in ref 33. Here, it was shown that for sufficiently large matrices a v3-512 TPU can perform dense matmuls at near-optimal efficiency: the performance per TPU core (measured in single-precision FLOPS) is maintained at roughly 93% of the single TPU core maximum performance.³³ It is important to emphasize that TPUs are often ill-suited for other tasks, and hence the algorithms utilized in this work and those in ref 33 had to be picked carefully and may differ from more conventional choices used in CPUs or GPUs. The use of DM purification algorithms, rather than direct diagonalization, is especially attractive for TPUs since all steps can be evaluated from a series of matmuls. Clearly, transformations 5 and 7 require large-scale matmuls. Transformations 4 and 6 are implemented by an iteration involving matrix polynomials of small degree, where each polynomial requires a short sequence of matrix additions and multiplications. Specifically, the matrix inverse square root in 4 is implemented using a standard Newton-Schulz iteration,⁵⁵ whereas for the density matrix purification in 6, we implemented the hole-particle canonical purification scheme.⁵² Further algorithm details can be found in the [Supporting Information](#).

For benchmarking purposes, we have performed end-to-end DFT computations on a sequence of increasingly large water clusters with geometries obtained from standard molecular dynamics simulations (see the [Supporting Information](#)). We leverage the DFT code FHI-aims^{46,47} to set up and drive calculations using CPUs and then use the TPUs to tackle the $O(N^3)$ dense linear algebra bottlenecks 4–7. We also utilize the *ELectronic Structure Infrastructure* (ELSI) library^{48,49} to facilitate the integration of FHI-aims and the TPU solver. In particular, the DFT Hamiltonian build time and associated computational scaling and parallelization are dictated exclusively by the FHI-aims code, which uses numeric atom-centered orbitals (NAOs) with an explicit finite spatial extent, and a truncated multipole expansion to accomplish low prefactor and efficient scaling of the Hamiltonian matrix build. While the computational time required to build the DFT Hamiltonian may vary greatly between different systems with the same total number of orbitals N (due to possible differences in the resulting sparsity in the systems), the computational time required for the $O(N^3)$ DM purification step performed on the TPU has much less variability since dense matrix operations are assumed, which do not utilize any

sparsity present (see the [Supporting Information](#) for more discussion). Thus, we emphasize that the TPU wall times, which are reported throughout only for water clusters, are fairly robust with respect to different systems with the same total number of orbitals.

Throughout this work, we perform all-electron calculations using the PBE exchange-correlation functional and utilize an NAO basis set such that each H₂O molecule contributes 10 electrons, represented by 24 orbitals (5 for each hydrogen atom and 14 for the oxygen atom). First we consider a single TPU board with 8 TPU v3 cores controlled by a host with 48 CPU cores, and we run FHI-aims on the host. [Figure 3](#) shows

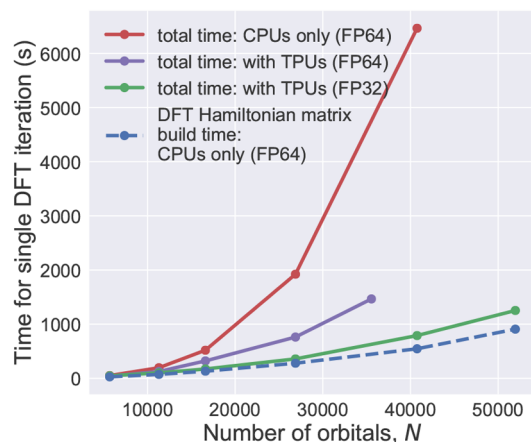


Figure 3. Wall times for a single DFT iteration on water clusters, using a TPU board composed of a CPU host (48 CPU cores) and 8 TPU cores with 128 GB of HBM. Green and purple curves correspond to using single and double precision in the TPU solver, respectively. The dashed blue curve corresponds to the CPU time spent on FHI-aims (always in double precision) and should be subtracted from the other curves in order to obtain the time spent on the TPU solvers. For reference, in red we also plot the time required for a CPU-only computation using the *eigenvalue solvers for Petaflop Applications* (ELPA), a highly parallelized eigensolver library,^{56,57} run on 48 CPU cores.

the wall time for a single DFT iteration (including both Hamiltonian build on CPUs and density matrix purification on TPUs) as a function of the size of the water cluster, which ranges from a few thousand to $N \approx 50\,000$ orbitals. When using the TPU solver in single precision (green curve), we see that the $O(N)$ Hamiltonian build on 48 CPU cores takes longer than the $O(N^3)$ density matrix purification run on 8 TPU cores, thus shifting the bottleneck. Using the TPU solver in double precision is an order of magnitude slower and saturates the TPU's HBM for $N \approx 36\,000$ orbitals.

Then we consider larger TPU configurations, of up to 512 TPU v3 cores, to perform end-to-end DFT computations on larger clusters, of up to 10 327 water molecules (or $N = 247\,848$ orbitals). [Figure 1](#) shows the TPU wall time for the $O(N^3)$ density matrix purification for one DFT iteration. These include 350 (5 434) seconds for a density matrix purification in single (double) precision on the largest cluster, demonstrating feasibility of DFT computations at that scale of a quarter of a million orbitals.

Dynamic Precision on TPUs. In our implementation, early DFT iterations are treated with single precision, and later ones are treated with double precision. This *dynamic precision* approach allows us to cut down on the use of double precision

matmuls on TPUs (which are significantly slower than single precision ones) without sacrificing accuracy of the final converged DFT result. Our criteria to switch precision is based on relative density changes, using the L^1 norm, defined as $L^1[f(\mathbf{r})] \equiv \int d^3r |f(\mathbf{r})|$, and relative energy changes

$$\frac{1}{N_e} L^1[n^{[i]}(\mathbf{r}) - n^{[i-1]}(\mathbf{r})] < \epsilon \quad \text{and} \quad (10)$$

$$|E^{[i]} - E^{[i-1]}|/|E^{[i]}| < \epsilon \quad (11)$$

where $n^{[i]}(\mathbf{r})$ is the real-space density at DFT iteration i , and $E^{[i]}$ is the corresponding total ground-state energy, and we use $\epsilon = 5 \times 10^{-7}$ for single precision.

[Figure 4](#) shows the convergence trajectory of a dynamic precision DFT calculation for the largest cluster we have

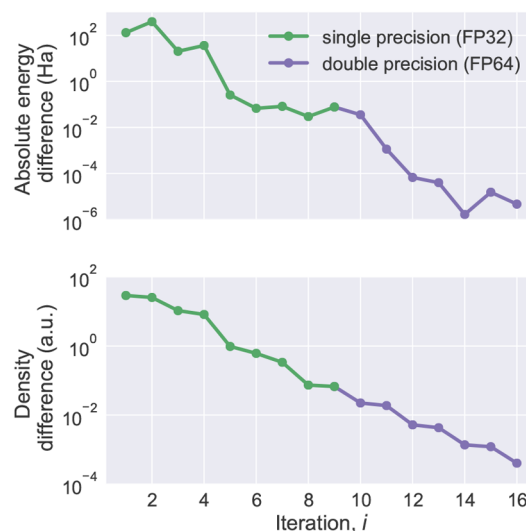


Figure 4. Convergence trajectory of an end-to-end dynamic precision DFT calculation on a $(\text{H}_2\text{O})_{10327}$ cluster. The absolute total energy differences between subsequent DFT iterations, i and $i - 1$, are plotted (top). The corresponding difference in real-space densities within the L^1 norm is plotted (bottom).

considered. We are able to converge such a DFT calculation to a fairly tight convergence threshold using first 9 single precision DFT iterations, followed by 7 double precision DFT iterations. In a smaller cluster, $(\text{H}_2\text{O})_{1481}$ with $N = 35\,544$ orbitals, a smaller number of double precision iterations are required for convergence, resulting in an overall DFT calculation time that is under 5 h on a single TPU board (v3–8), see the [Supporting Information](#).

DISCUSSION

This work has successfully demonstrated that TPUs can both *accelerate* and *scale up* DFT computations. Significant *acceleration* is already achieved using only a single TPU board with 8 TPU v3 cores, see [Figure 3](#). For instance, an end-to-end dynamic precision DFT computation with $N = 35\,544$ orbitals consisting of 12 iterations in single precision and 4 iterations in double precision yields converged results in under 5 h. For context, using double precision only and the highly optimized ELPA $O(N^3)$ solver with 48 CPUs, the same water cluster calculation required 20 h to achieve 16 DFT iterations.

In order to *scale up* the size of DFT computations while retaining high performance, two main ingredients are involved:

(i) a larger amount of high bandwidth memory, scaling as $O(N^2)$, to be able to store dense $N \times N$ matrices, and (ii) a larger number of cores, with state-of-the-art intercore connectivity, to more effectively execute the $O(N^3)$ floating point operations involved in the required distributed matrix transformations. As shown in Figure 1, by using a number of cores that scales as $O(N^2)$ and commensurate amounts of HBM, we can scale up to $N = 500\,000$ orbitals with wall times that only grow proportional to N .

Once the main ingredients (i) and (ii) are satisfied, the Input/Output (IO) time, i.e. the end-to-end communication time between the TPU and CPU, can become an important (and possibly limiting) factor. This IO step is not exclusive to TPUs, for instance, it is also relevant and analogous in GPU setups that require communication with CPUs. This step is parallelizable, such that by using a number of TPU and CPU cores that scale as $O(N^2)$, the total IO time remains constant $O(1)$. However, in this context, obtaining such optimality will depend on specific implementation details of the DFT code utilized, such as the matrix distribution pattern on the CPU processor grid. Our current prototype implementation is modular and generalizable, and IO times scale unfavorably as $O(N^2)$, even becoming the rate-limiting step in some cases (see the Supporting Information). Less general (but straightforward) engineering approaches are expected to be much more optimal but are beyond the scope of this work which aims to demonstrate the use of TPUs in a more general context.

We emphasize that other hardware accelerators, most notably GPUs, can also accelerate and scale up DFT computations in a similar manner as discussed above, and ref 58 recently presented a useful and positive development in this direction.

Modern distributed GPU configurations are expected to achieve similar performance to TPUs in this regard; however, a direct comparison is complicated by the highly diverse nature of distributed GPU configurations found in practice. On the Summit supercomputer configuration, it has been reported that 432 distributed Nvidia V100 GPUs can perform matmuls for dense $N > 500\,000$ matrices with a performance per GPU (measured in FLOPS) that is roughly 85% of the single V100 GPU maximum performance.⁵⁹

Here we have focused, for simplicity, on applying DFT to clusters of water molecules. More complicated systems may present additional difficulties. For instance, protein–ligand complexes often require more elaborate schemes, such as including solvation to facilitate the convergence of the DFT iteration.^{60,61} Work in progress shows that our TPU-based large-scale DFT computations can also successfully address protein–ligand complexes with explicit solvents, as well as in a variety of other large systems, including DNA segments, carbon nanotubes, and graphene surfaces. In addition to single-point DFT energy calculations, analytical forces can also be extracted from the TPU-calculated *energy-weighted density matrix*, enabling large-scale geometry optimization or ab initio molecular dynamics calculations.

DFT is a highly successful quantum-based method, but it is ultimately a consistent-field approximation, which may not be accurate enough for certain applications. Fortunately, TPUs can also accelerate and scale up other, more accurate quantum chemistry approaches where the computational bottleneck is again given by dense linear algebra operations. For example, in density matrix renormalization group (DMRG)⁶² calculations,

TPUs can be used to reach an unprecedentedly large bond dimension $D = 65\,536$.³⁵ Similarly, we anticipate that TPUs will thrive in other methods such as coupled cluster⁶³ and Moller–Plesset perturbation theory.⁶⁴ Even when applying such higher-level methods, large-scale DFT may still be a crucial piece in simulations that require a quantum-mechanically treated region that embeds a subsystem treated with higher-level correlated methods.⁶⁵

To conclude, in this work we have successfully repurposed TPUs as quantum chemistry supercomputers by tackling the $O(N^3)$ computational bottleneck of density functional theory. We demonstrated performance and scalability with a water cluster with $N = 247\,848$ orbitals, which to our knowledge is the largest $O(N^3)$ DFT computation to date. We remark that cloud-based TPUs, and other hardware accelerators such as GPUs, are more accessible and affordable than traditional supercomputer resources. Our work thus paves the way toward accessible and straightforward use of quantum chemistry computational methods for much larger systems than were previously possible.

■ ASSOCIATED CONTENT

SI Supporting Information

The Supporting Information is available free of charge at <https://pubs.acs.org/doi/10.1021/acs.jctc.2c00876>.

Orthogonalization details, purification details, FHI-AIMS TPU integration details, and dynamic precision on smaller water clusters (PDF)

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Notes

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