

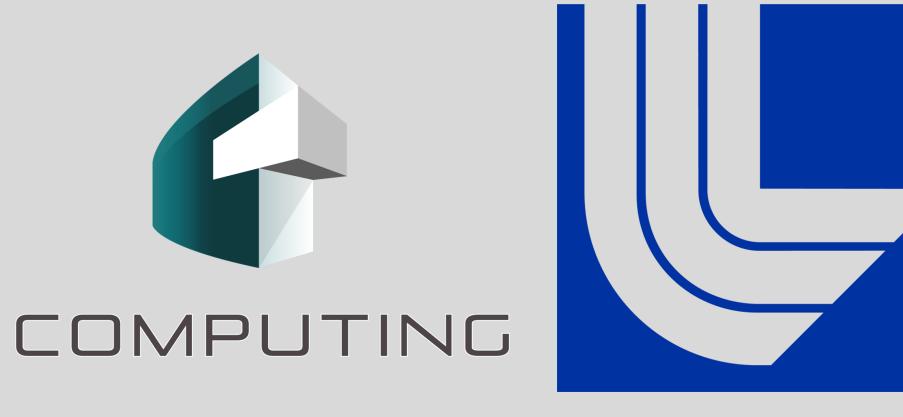


Analytics4X: General-Purpose Framework for Analysis and Optimization of **HPC Data Movement**

lan Lumsden¹ (Student), Michela Taufer¹ (PhD Advisor)

Other Mentors: Stephanie Brink², Olga Pearce², Hariharan Devarajan², Jae-Seung Yeom², Tom Scogland²

¹University of Tennessee, Knoxville ²Lawrence Livermore National Laboratory



Dissertation Statement

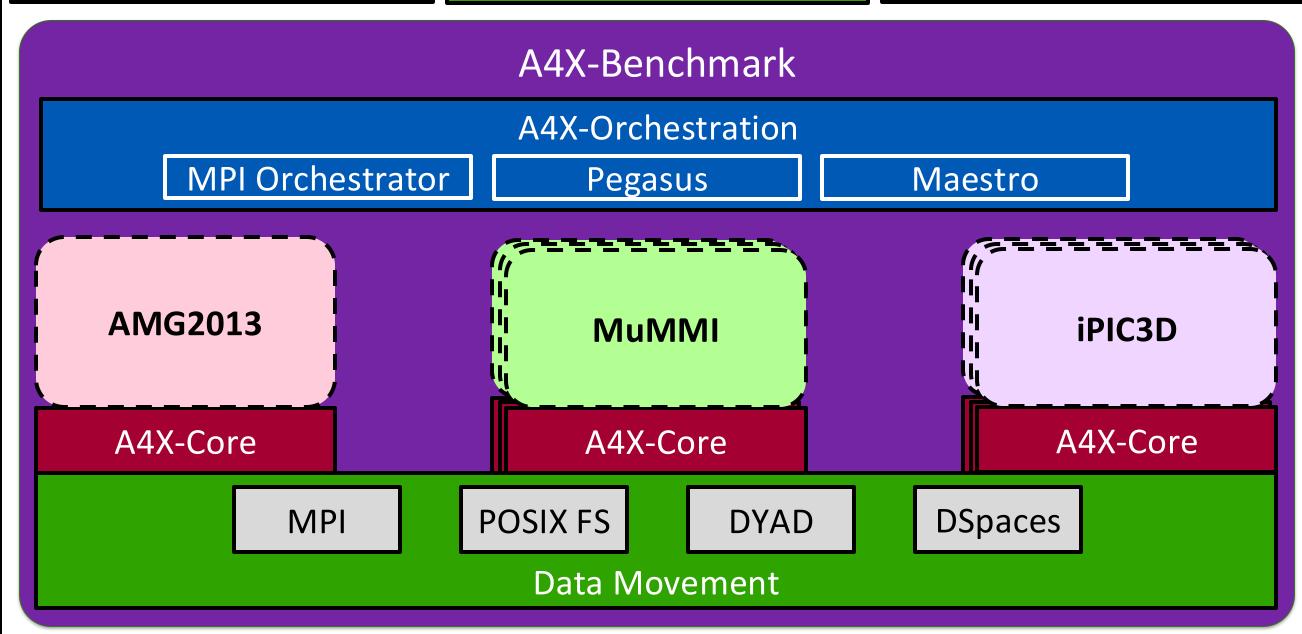
To continue to accelerate scientific discovery in the exascale era and beyond, we need a general-purpose, adaptable analytic framework for optimizing data movement in both monolithic and modular workflow-based applications.

To design this tool, we first aim to understand and optimize I/O and data movement across diverse HPC applications:

- 3 applications (i.e., AMG2013, MuMMI, iPIC3D)
- 3 adapted tools (i.e., Caliper, Thicket, IOR benchmarking)
- 3 lessons learned (i.e., reveal data movement patterns, align I/O with workflow execution, use phase-aware monitoring)

We integrate features derived from these lessons learned into a unified Analytics4X framework that supports diverse application types and I/O patterns Community Challenges Our Solutions Develop and integrate a novel Call Path Query Language into LLNL's Thicket Profiling tools can capture compute performance analysis library to enable performance well, but they lack the complex filtering of performance profiles ability to filter and focus on data and focused analysis on data movement movement and I/O layers at scale and I/O layers Workflow applications introduce Instrument workflow execution with complex and irregular I/O patterns that middleware-level I/O tracking using are often not well-optimized, causing Thicket to capture detailed I/O patterns data movement inefficiencies and longer and guide workflow-aware optimizations time-to-solution In simulations, I/O phases vary in Deploy performance monitoring and IOR frequency, size, and access pattern; benchmarking to identify application I/O static storage configurations fail to phases and dynamically match them to provide optimal performance for all the most suitable storage configurations phases, causing bottlenecks Analytics4X Framework Feature: **Feature: Feature:** Automated phase Built-in capability to Support for fine-grained detection and benchmark integrate middlewarelevel performance data I/O layer filtering within driven mapping to performance profiles

into workflow optimal storage configurations optimization

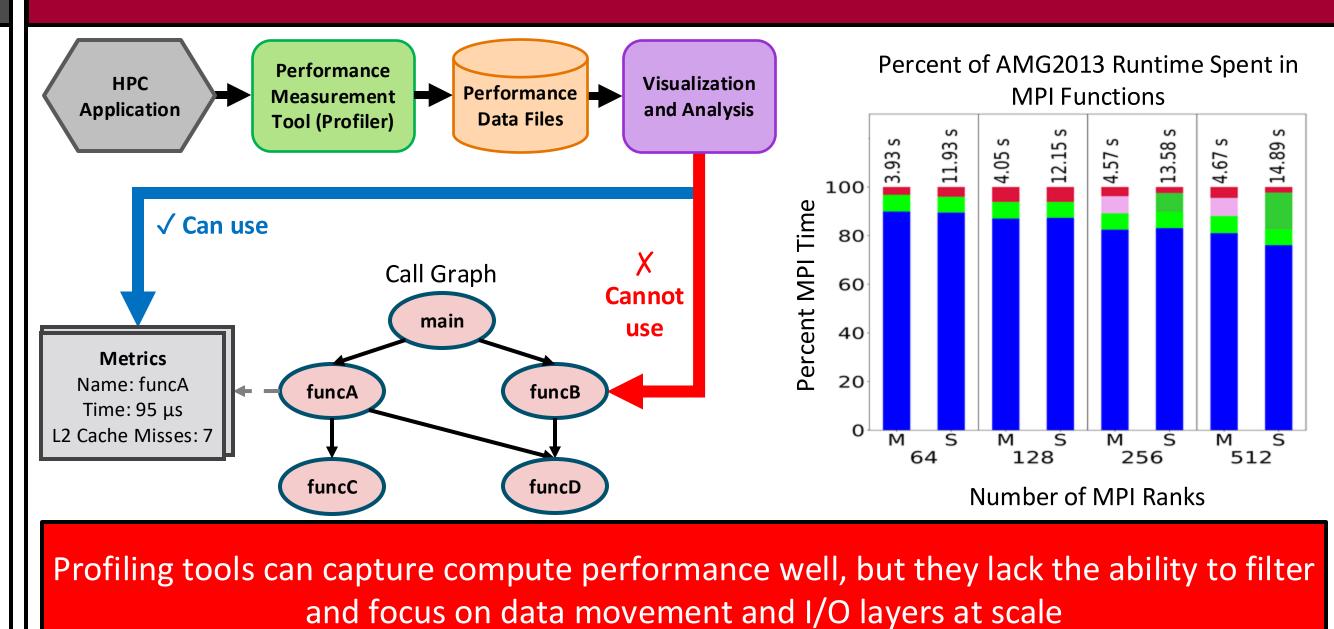


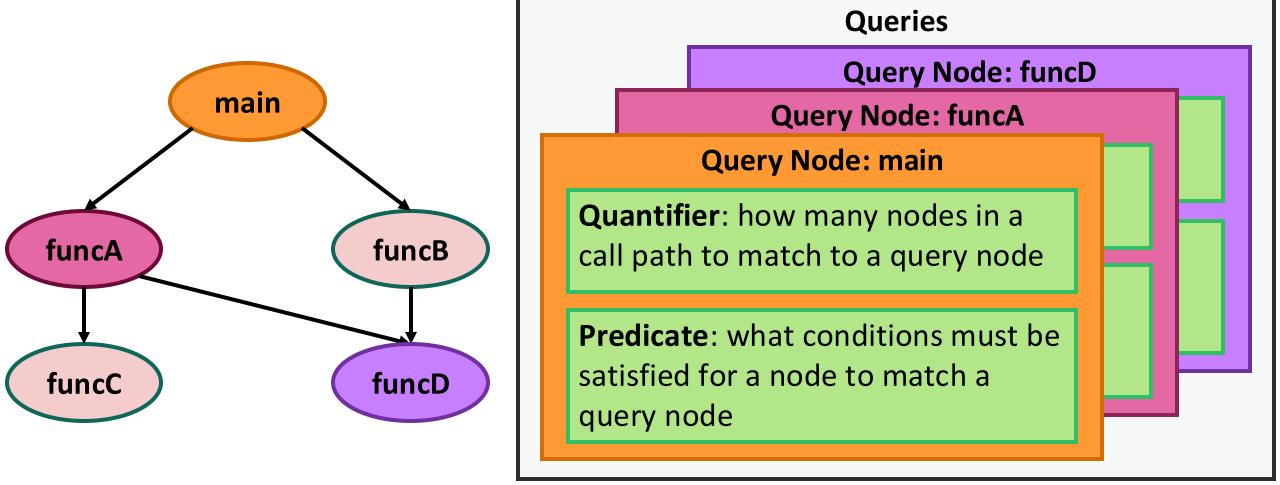
A4X-Core: | Common abstractions and built-in performance monitoring for data movement tools

A4X-Orchestration: Common abstractions for configuring different workflow management systems

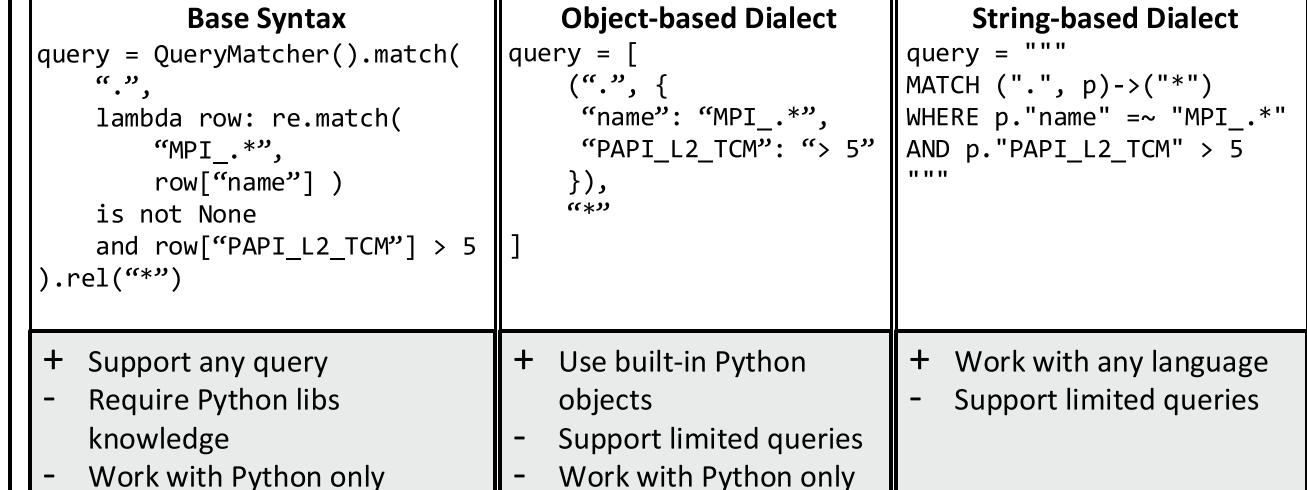
A4X-Benchmark: Benchmark comprised of proxies representing common data movement motifs in applications

AMG2013: Proxy for Fluid Dynamics

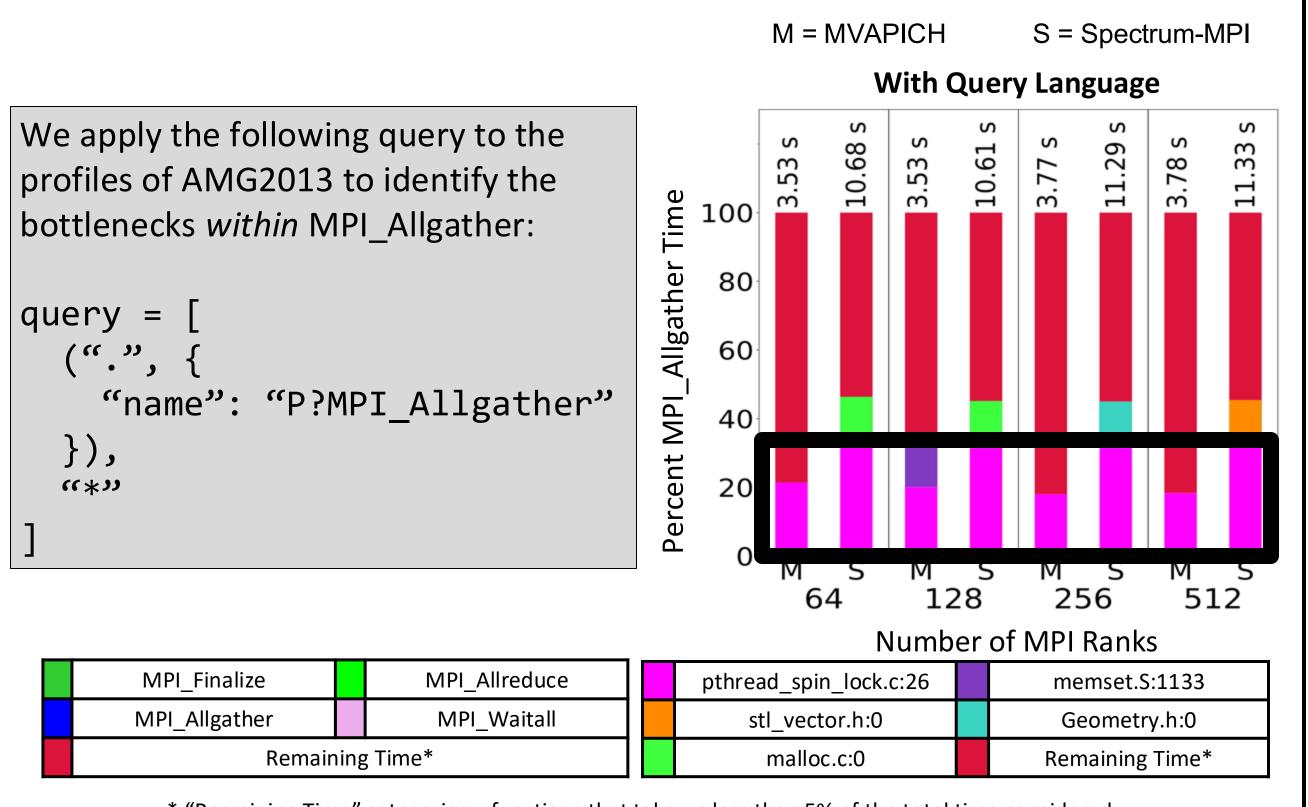




Query Example: Find all subgraphs rooted at a MPI node with more than 5 L2 cache misses



We develop a novel Call Path Query Language and integrate it into LLNL's Thicket performance library to enable complex filtering of profiles using the call graph



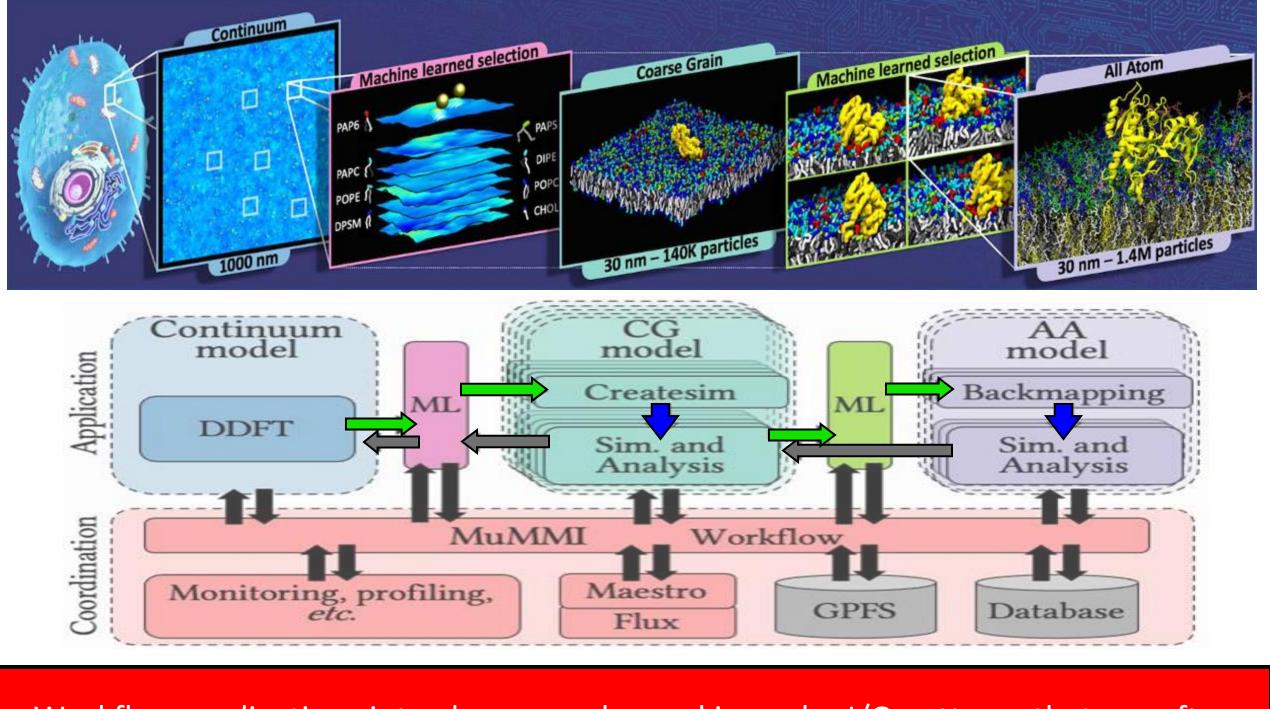
* "Remaining Time" categories = functions that take up less than 5% of the total time considered

By applying the query language to runs of AMG2013 with both MVAPICH and Spectrum-MPI, we can not only identify that MPI_Allgather is the largest bottleneck, but we can also identify that pthread_spin_lock is the largest single bottleneck within MPI_Allgather

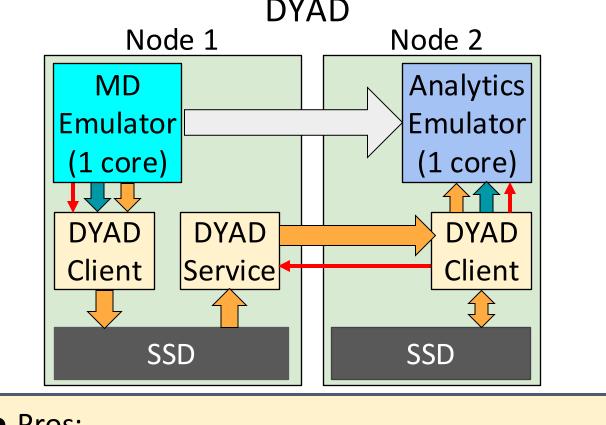
Lesson Learned:

Profile-level performance analysis can be extended to reveal detailed data movement patterns

MuMMI: Workflow for Molecular Dynamics

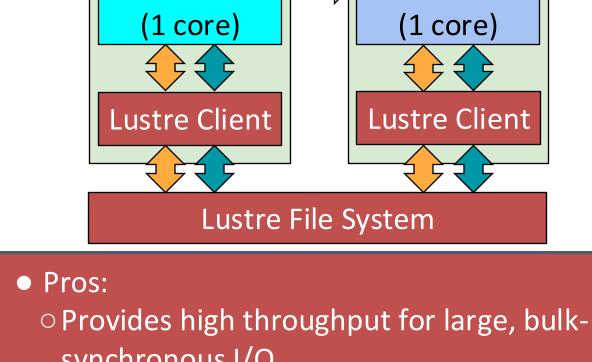


Workflow applications introduce complex and irregular I/O patterns that are often not well-optimized, causing data movement inefficiencies and longer time-to-solution



Provides easy use of local storage

Provides built-in sync



Node 2

Analytics

Emulator

28.5 MiB Frames

28 stride

synchronous I/O Supports only write-once, read-many I/O Struggles with small or unsynchronized I/O

8.8 MiB Frames

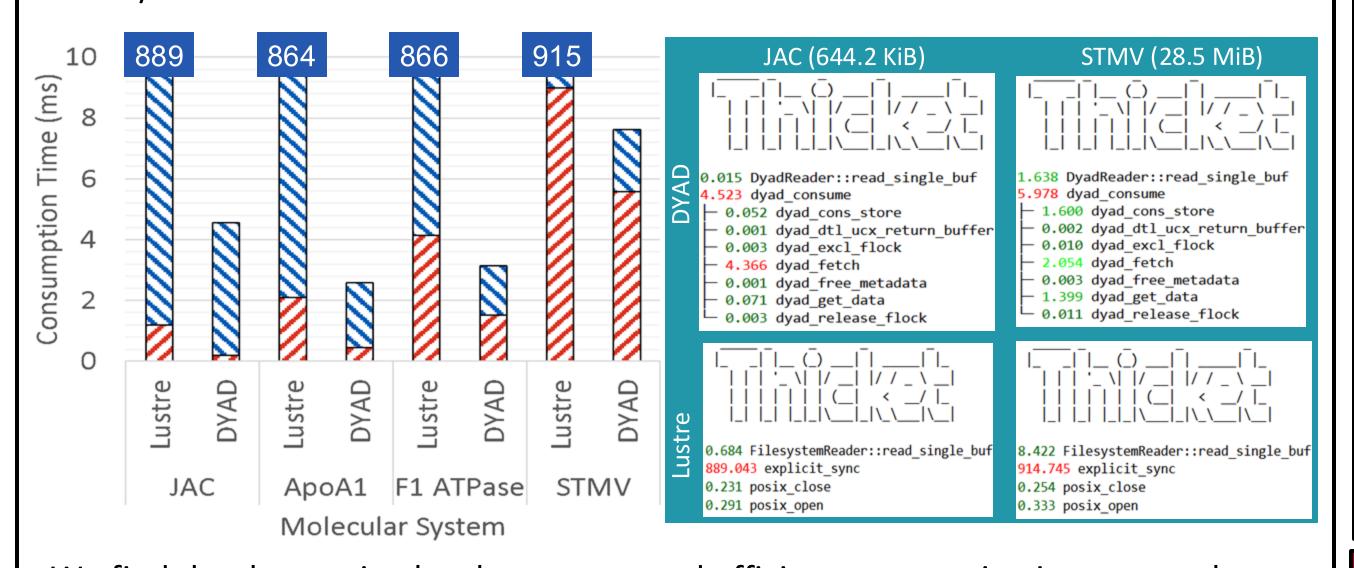
• 92 stride

Does not provide built-in sync

Node 1

--- Small Cluster --- Supercomputer ApoA1: 92.2K atoms • 23.5K atoms £ 150 -ApoA' • 2.5 MiB Frames 644.2 KiB Frames 294 stride • 880 stride 1 ATPase: 1.1M atoms • 327.5K atoms

System Size (# Atoms) We instrument a molecular dynamics workflow with an ensemble of one-to-one I/O patterns using LLNL's Caliper profiler and analyze the resulting performance data with Thicket and our Call Graph Query Language to study the behavior of two I/O tools: Lustre and LLNL's DYAD middleware

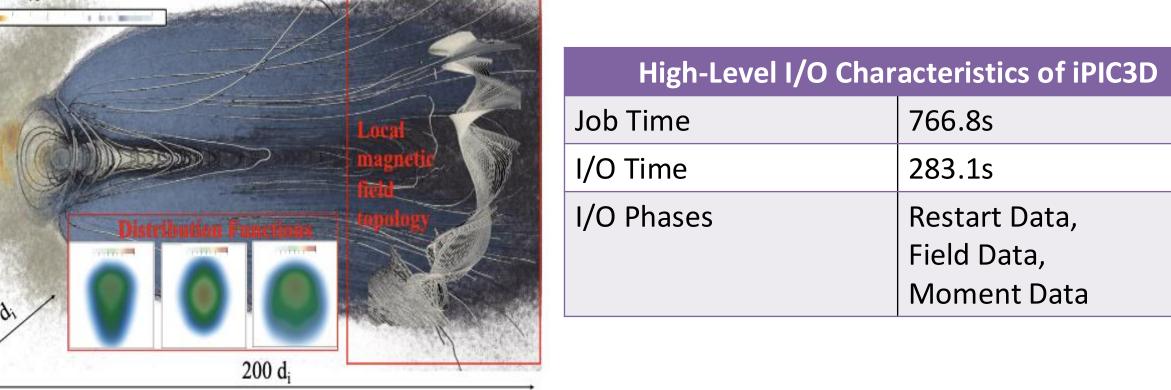


We find that leveraging local resources and efficient communication protocols enables better scalability as data sizes (represented by molecular model) increase

Lesson Learned:

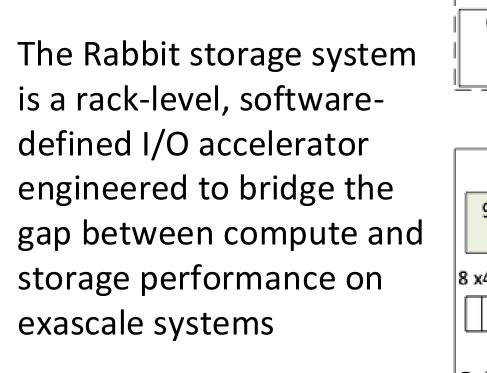
Middleware can substantially improve data movement efficiency for workflows by aligning I/O with workflow execution patterns

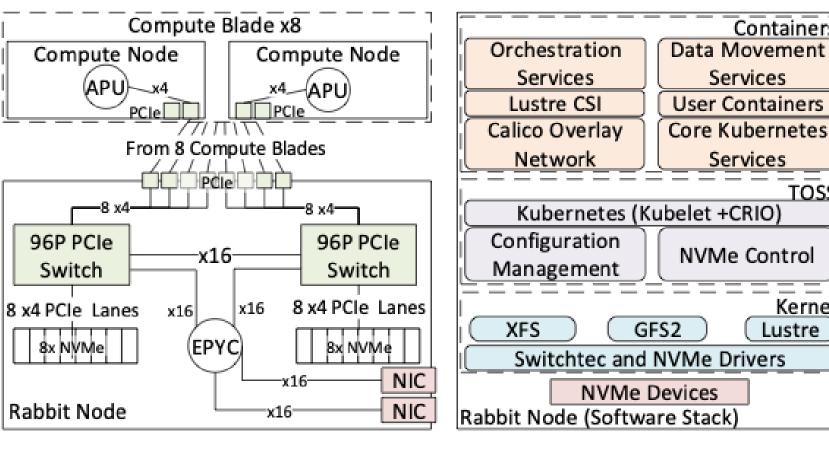
iPIC3D: Simulation of Planetary-Scale Plasma Physics



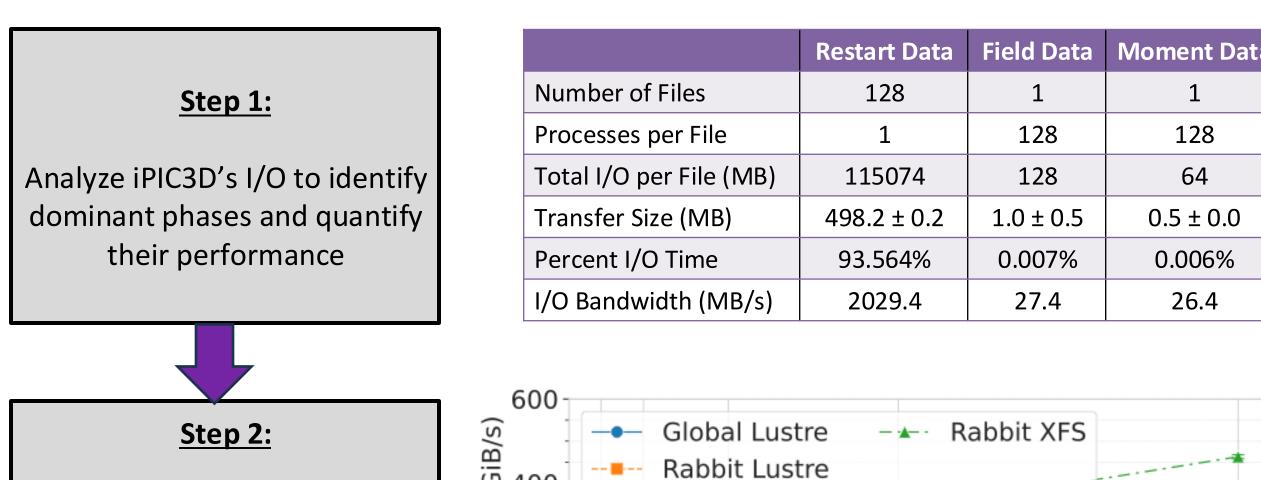
283.1s Restart Data, Field Data, **Moment Data**

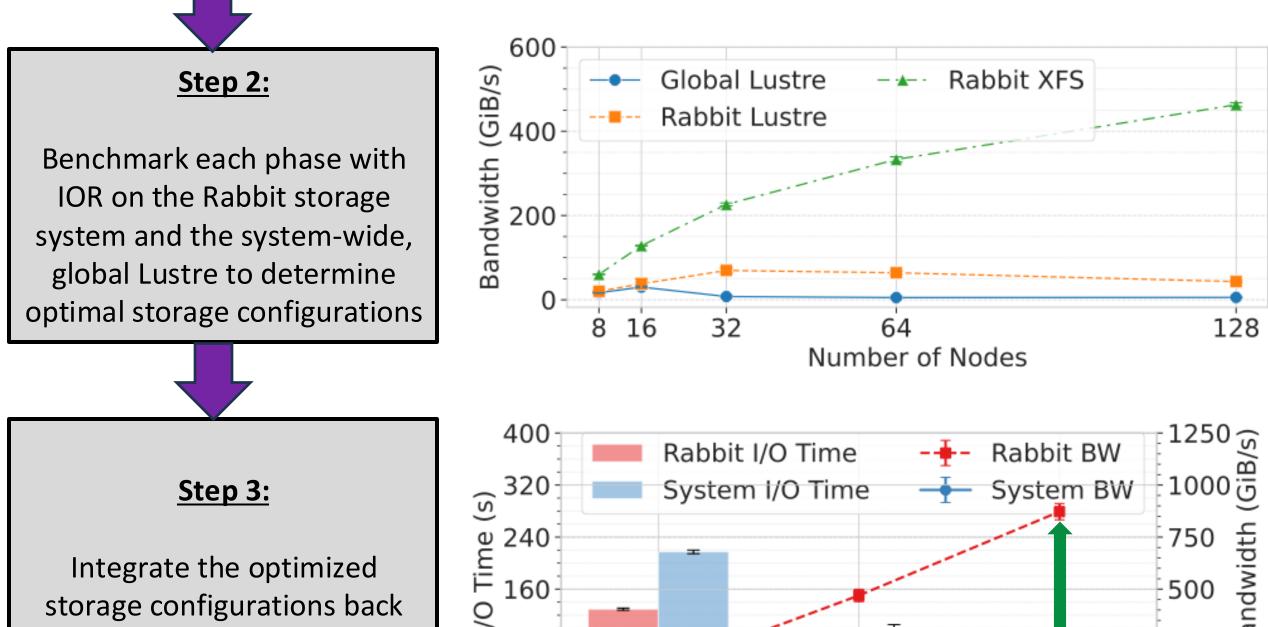
In simulations, I/O phases vary in frequency, size, and access pattern; static storage configurations fail to provide optimal performance for all phases, causing bottlenecks





We deploy LLNL's Caliper and DFTracer performance monitoring tools, LLNL's DFAnalyzer performance analysis tool, and IOR benchmarking to identify iPIC3D's I/O phases and dynamically match them to the most suitable storage configurations on LLNL's Tuolumne supercomputer





Number of Nodes

By mapping each I/O phase of iPIC3D to its optimal Rabbit configuration based on IOR benchmarking, we achieve up to a 4.85x I/O throughput improvement and up to a 1.45x overall application speedup

Lesson Learned:

Matching I/O phases to targeted storage systems can yield substantial performance gains, but requires phase-aware monitoring and tuning



Step 3:

into iPIC3D

IPDPS 2024

Cluster 2025 Workshop Paper: Workshop Paper:





CV:

0.006%



We acknowledge the feedback and support of the following colleagues: Andong Hu (KTH Royal Institute of Technology, USA), Bronis R. de Supinski (Lawrence Livermore National Laboratory, USA), Connor Scully-Allison (University of Chicago, USA), David Boehme (Lawrence Livermore National Laboratory, USA), Dewi Yokelson (Advanced Micro Devices, Inc., USA), Ivy Peng (KTH Royal Institute of Technology, USA), Izzet Yildirim (Illinois Institute of Technology, USA), Jack Marquez (University of Tennessee, USA), Jakob Luettgau (Inria, France), Katherine E. Isaacs (University of Utah, USA), Stefano Markidis (KTH Royal Institute of Technology, USA), Vanessa Lama (Oak Ridge National Laboratory, USA), Xian-He Sun (Illinois Institute of Technology, USA).