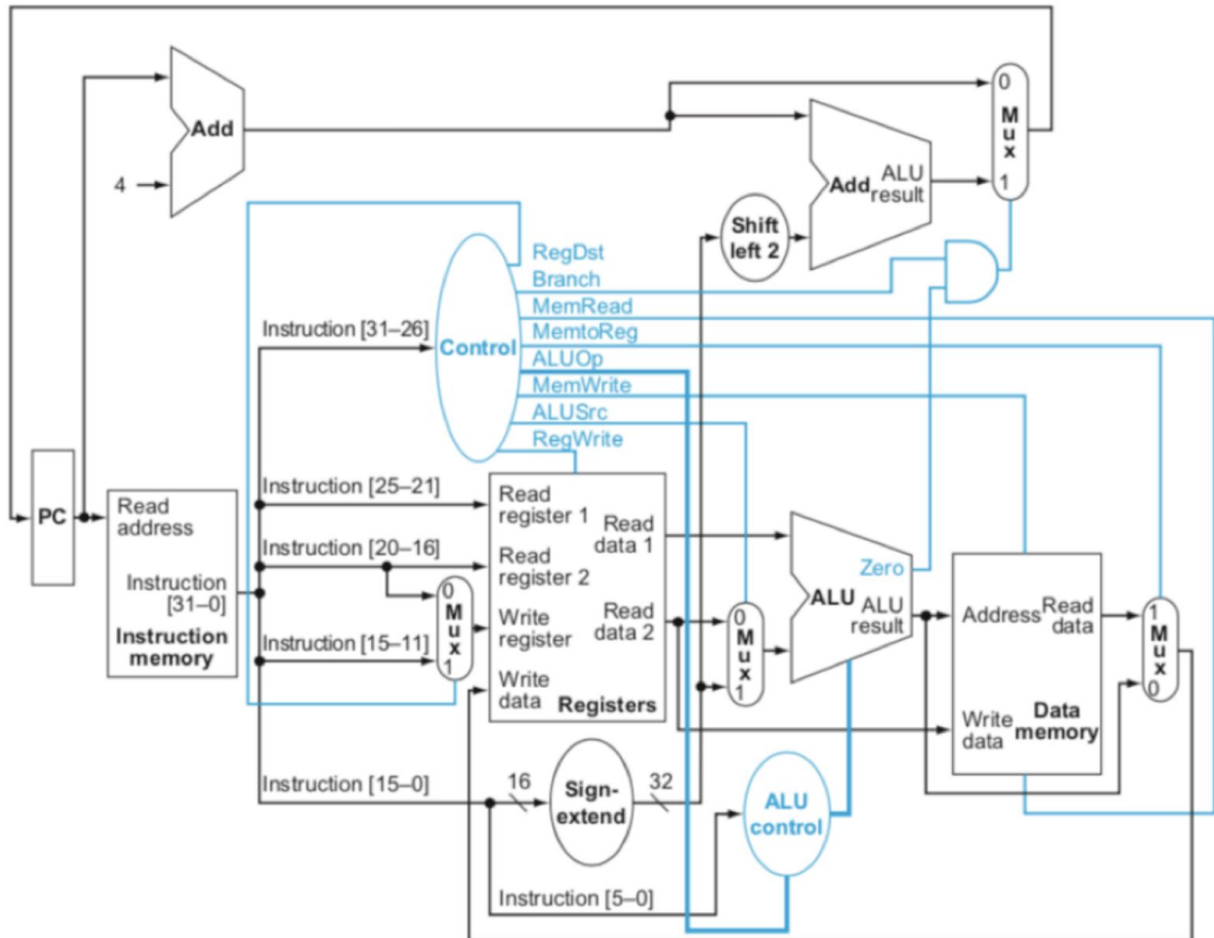


ข้อ 1



RegDst	ALUSrc	MemtoReg	RegWrite	MemRead	MemWrite	Branch	ALUControl
ID	EXE	WB	WB	MEM	MEM	EXE	EXE

ข้อ 2

IF

Input

1. In_next_line
2. In_op
3. Jump
4. Jump_value
5. Branch
6. Will_branch
7. Enable
8. Clock
9. is_jump_register

Output

1. Op
2. next_line

ID

Input

1. Op
2. Write_value (WB)
3. Next_line (EXE)
4. Is_jump_register_save (EXE)
5. Clock
6. Is_write_register (WB)
7. Is_immediate (WB)
8. Is_immediate
9. Op (WB)

output

1. A
2. B
3. SA
4. sw_value

EXE

Input

1. isSetLess
2. A
3. B
4. SA
5. ALU_Op

Output

1. Is_branch
2. value

MEM

input

1. Address
2. sw_value

Output

1. mem_value

WB

input

1. Load
2. ALU_value
3. Mem_value

Output

1. WriteValue