

PROJECT THESIS DOCUMENTATION

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Form Academic year	4BHELS / 2022/2023
Topic	An 8-BIT CPU
Co-operation partners	
Assignment of tasks (conceptual formulation/job definition)	A type of feasibility study, to see if conceptualizing, developing and building an 8-Bit CPU, in the given time frame – about three quarters of a year – would be possible.
Implementation	A prototype is built using breadboards, jumpers and integrated circuits; bigger circuit are realized using PCBs. The program is written using a custom compiler and assembler and will be uploaded to the EEPROM via an EEPROM programmer.
Results	

<p>Illustrative graph, photo (incl. explanation)</p>		
<p>Participation in competitions Awards</p>		
<p>Accessibility of diploma thesis</p>		
<p>Approval (Date / Sign)</p>	<p>Examiner</p>	<p>Dipl.-Ing. W. U. KURAN Head of Department</p>