

Physical Memory 101

- SRAM - static RAM \leftarrow cache \rightarrow CMOS

- DRAM - dynamic RAM \leftarrow capacitor

 - \hookrightarrow lowest column has 1 bit \Rightarrow 2Kb per row

 - \hookrightarrow row needs to be charged and column needs to be charged

 - \hookrightarrow pick a particular byte

- balanced

- latency \Rightarrow bound by charging rows

 - \hookrightarrow not applied by Moore's Law