



## 1. Description

### 1.1. Project

Project Name	TFT_touch_xpt2046_calib
Board Name	custom
Generated with:	STM32CubeMX 6.13.0
Date	12/29/2024

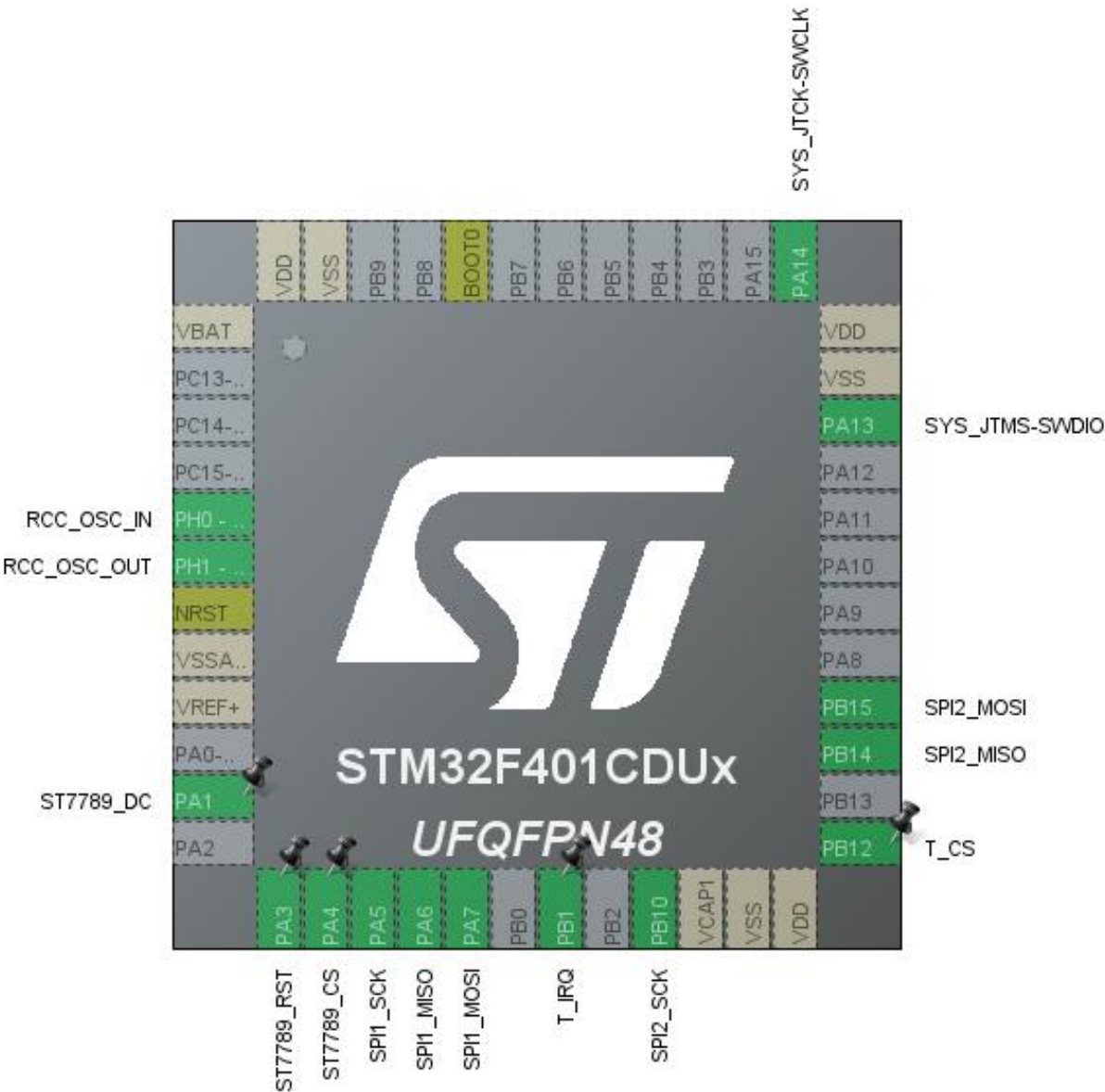
### 1.2. MCU

MCU Series	STM32F4
MCU Line	STM32F401
MCU name	STM32F401CDUx
MCU Package	UFQFPN48
MCU Pin number	48

### 1.3. Core(s) information

Core(s)	Arm Cortex-M4
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## 2. Pinout Configuration



### 3. Pins Configuration

Pin Number UFQFPN48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
5	PH0 - OSC_IN	I/O	RCC_OSC_IN	
6	PH1 - OSC_OUT	I/O	RCC_OSC_OUT	
7	NRST	Reset		
8	VSSA/VREF-	Power		
9	VREF+	Power		
11	PA1 *	I/O	GPIO_Output	ST7789_DC
13	PA3 *	I/O	GPIO_Output	ST7789_RST
14	PA4 *	I/O	GPIO_Output	ST7789_CS
15	PA5	I/O	SPI1_SCK	
16	PA6	I/O	SPI1_MISO	
17	PA7	I/O	SPI1_MOSI	
19	PB1	I/O	GPIO_EXTI1	T_IRQ
21	PB10	I/O	SPI2_SCK	
22	VCAP1	Power		
23	VSS	Power		
24	VDD	Power		
25	PB12 *	I/O	GPIO_Output	T_CS
27	PB14	I/O	SPI2_MISO	
28	PB15	I/O	SPI2_MOSI	
34	PA13	I/O	SYS_JTMS-SWDIO	
35	VSS	Power		
36	VDD	Power		
37	PA14	I/O	SYS_JTCK-SWCLK	
44	BOOT0	Boot		
47	VSS	Power		
48	VDD	Power		

\* The pin is affected with an I/O function



## 1. Power Consumption Calculator report

### 1.1. Microcontroller Selection

Series	STM32F4
Line	STM32F401
MCU	STM32F401CDUx
Datasheet	DS10086_Rev3

### 1.2. Parameter Selection

Temperature	25
Vdd	3.3

### 1.3. Battery Selection

Battery	Li-SOCL2(A3400)
Capacity	3400.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	100.0 mA
Max Pulse Current	200.0 mA
Cells in series	1
Cells in parallel	1

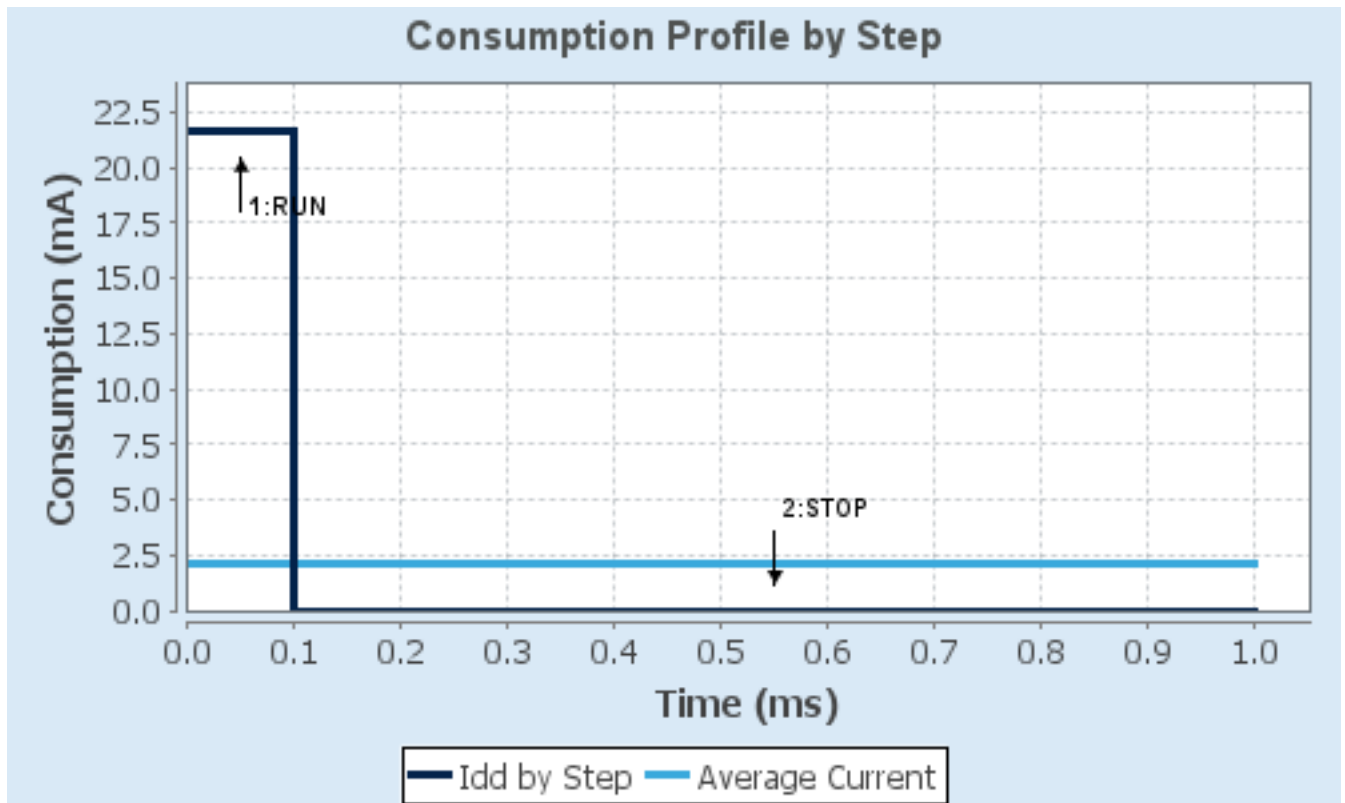
#### 1.4. Sequence

<b>Step</b>	Step1	Step2
<b>Mode</b>	RUN	STOP
<b>Vdd</b>	3.3	3.3
<b>Voltage Source</b>	Battery	Battery
<b>Range</b>	Scale2-Medium	No Scale
<b>Fetch Type</b>	FLASH/ART/PREFETCH	n/a
<b>CPU Frequency</b>	84 MHz	0 Hz
<b>Clock Configuration</b>	HSE PLL	Regulator_LPLV Flash-PwrDwn
<b>Clock Source Frequency</b>	4 MHz	0 Hz
<b>Peripherals</b>		
<b>Additional Cons.</b>	0 mA	0 mA
<b>Average Current</b>	21.6 mA	10 $\mu$ A
<b>Duration</b>	0.1 ms	0.9 ms
<b>DMIPS</b>	105.0	0.0
<b>Ta Max</b>	102.72	105
<b>Category</b>	In DS Table	In DS Table

#### 1.5. Results

Sequence Time	1 ms	Average Current	2.17 mA
Battery Life	2 months, 4 days, 8 hours	Average DMIPS	105.0 DMIPS

#### 1.6. Chart





## 2. Software Project

### 2.1. Project Settings

Name	Value
Project Name	TFT_touch_xpt2046_calib
Project Folder	D:\TAI_LIEU\20241\AI_and_App\HandWritingRecognition_on_STM32\Impliment_
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_F4 V1.28.1
Application Structure	Advanced
Generate Under Root	Yes
Do not generate the main()	No
Minimum Heap Size	0x200
Minimum Stack Size	0x400

### 2.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No
Enable Full Assert	No

### 2.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name
1	SystemClock_Config	RCC
2	MX_GPIO_Init	GPIO
3	MX_DMA_Init	DMA
4	MX_SPI1_Init	SPI1
5	MX_SPI2_Init	SPI2

## 3. Peripherals and Middlewares Configuration

### 3.1. RCC

#### High Speed Clock (HSE): Crystal/Ceramic Resonator

##### 3.1.1. Parameter Settings:

###### System Parameters:

VDD voltage (V)	3.3
Instruction Cache	Enabled
Prefetch Buffer	Enabled
Data Cache	Enabled
Flash Latency(WS)	2 WS (3 CPU cycle)

###### RCC Parameters:

HSI Calibration Value	16
TIM Prescaler Selection	Disabled
HSE Startup Timeout Value (ms)	100
LSE Startup Timeout Value (ms)	5000

###### Power Parameters:

Power Regulator Voltage Scale	Power Regulator Voltage Scale 2
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### 3.2. SPI1

#### Mode: Full-Duplex Master

##### 3.2.1. Parameter Settings:

###### Basic Parameters:

Frame Format	Motorola
Data Size	8 Bits
First Bit	MSB First

###### Clock Parameters:

Prescaler (for Baud Rate)	<b>4</b> *
Clock Polarity (CPOL)	<b>High</b> *
Clock Phase (CPHA)	1 Edge

###### Advanced Parameters:

CRC Calculation	Disabled
NSS Signal Type	Software

3.3. SPI2

Mode: Full-Duplex Master

3.3.1. Parameter Settings:

Basic Parameters:

Frame Format	Motorola
Data Size	<b>16 Bits *</b>
First Bit	MSB First

Clock Parameters:

Prescaler (for Baud Rate)	<b>8 *</b>
Clock Polarity (CPOL)	Low
Clock Phase (CPHA)	1 Edge

Advanced Parameters:

CRC Calculation	Disabled
NSS Signal Type	Software

3.4. SYS

Debug: Serial Wire

Timebase Source: SysTick

\* User modified value

## 4. System Configuration

### 4.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
RCC	PH0 - OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	PH1 - OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SPI1	PA5	SPI1_SCK	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PA6	SPI1_MISO	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PA7	SPI1_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
SPI2	PB10	SPI2_SCK	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PB14	SPI2_MISO	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
	PB15	SPI2_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	Very High *	
SYS	PA13	SYS_JTMS-SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK-SWCLK	n/a	n/a	n/a	
GPIO	PA1	GPIO_Output	<b>Output Open Drain *</b>	No pull-up and no pull-down	<b>High *</b>	ST7789_DC
	PA3	GPIO_Output	<b>Output Open Drain *</b>	No pull-up and no pull-down	<b>High *</b>	ST7789_RST
	PA4	GPIO_Output	<b>Output Open Drain *</b>	No pull-up and no pull-down	<b>High *</b>	ST7789_CS
	PB1	GPIO_EXTI1	External Interrupt Mode with Rising edge trigger detection	No pull-up and no pull-down	<b>n/a</b>	T_IRQ
	PB12	GPIO_Output	<b>Output Open Drain *</b>	No pull-up and no pull-down	Very High *	T_CS

## 4.2. DMA configuration

DMA request	Stream	Direction	Priority
SPI1_TX	DMA2_Stream3	Memory To Peripheral	Low

### SPI1\_TX: DMA2\_Stream3 DMA request Settings:

Mode: Normal  
Use fifo: Disable  
Peripheral Increment: Disable  
Memory Increment: **Enable \***  
Peripheral Data Width: Byte  
Memory Data Width: Byte

### 4.3. NVIC configuration

#### 4.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Pre-fetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
System service call via SWI instruction	true	0	0
Debug monitor	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	15	0
DMA2 stream3 global interrupt	true	0	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		
EXTI line1 interrupt	unused		
SPI1 global interrupt	unused		
SPI2 global interrupt	unused		
FPU global interrupt	unused		

#### 4.3.2. NVIC Code generation

Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable interrupt	false	true	false
Hard fault interrupt	false	true	false
Memory management fault	false	true	false
Pre-fetch fault, memory access fault	false	true	false
Undefined instruction or illegal state	false	true	false
System service call via SWI instruction	false	true	false
Debug monitor	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true
DMA2 stream3 global interrupt	false	true	true

\* User modified value

## 5. System Views

### 5.1. Category view

#### 5.1.1. Current

Middleware

System Core	Analog	Timers	Connectivity	Multimedia	Computing
DMA ✓			SPI1 ✓		
GPIO ✓			SPI2 ✓		
IIVIC ✓					
RCC ✓					
SYS ✓					

## 6. Docs & Resources

Type	Link
BSDL files	<a href="https://www.st.com/resource/en/bsdl_model/stm32f401_bsdl.zip">https://www.st.com/resource/en/bsdl_model/stm32f401_bsdl.zip</a>
IBIS models	<a href="https://www.st.com/resource/en/ibis_model/stm32f401_ibis.zip">https://www.st.com/resource/en/ibis_model/stm32f401_ibis.zip</a>
System View Description	<a href="https://www.st.com/resource/en/svd/stm32f4-svd.zip">https://www.st.com/resource/en/svd/stm32f4-svd.zip</a>
Presentations	<a href="https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf">https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf</a>
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Presentations	<a href="https://www.st.com/resource/en/product_presentation/microcontrollers-stm32h7rs-lines-overview.pdf">https://www.st.com/resource/en/product_presentation/microcontrollers-stm32h7rs-lines-overview.pdf</a>
Brochures	<a href="https://www.st.com/resource/en/brochure/products-and-solutions-for-plcs-and-smart-i-os.pdf">https://www.st.com/resource/en/brochure/products-and-solutions-for-plcs-and-smart-i-os.pdf</a>
Flyers	<a href="https://www.st.com/resource/en/flyer/flstm32f4x1.pdf">https://www.st.com/resource/en/flyer/flstm32f4x1.pdf</a>
Flyers	<a href="https://www.st.com/resource/en/flyer/flstm32nucleo.pdf">https://www.st.com/resource/en/flyer/flstm32nucleo.pdf</a>
Flyers	<a href="https://www.st.com/resource/en/flyer/flstmcsuite.pdf">https://www.st.com/resource/en/flyer/flstmcsuite.pdf</a>
Flyers	<a href="https://www.st.com/resource/en/flyer/flstm32trust.pdf">https://www.st.com/resource/en/flyer/flstm32trust.pdf</a>
Product Certifications	<a href="https://www.st.com/resource/en/certification_document/stm32_authentication_can.pdf">https://www.st.com/resource/en/certification_document/stm32_authentication_can.pdf</a>
Application Notes	<a href="https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf</a>
Application Notes	<a href="https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf">https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf</a>
Application Notes	<a href="https://www.st.com/resource/en/application_note/an2639-soldering-">https://www.st.com/resource/en/application_note/an2639-soldering-</a>



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Application Notes [https://www.st.com/resource/en/application\\_note/an2945-stm8s-and-stm32-mcus-a-consistent-832bit-product-line-for-painless-migration-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an2945-stm8s-and-stm32-mcus-a-consistent-832bit-product-line-for-painless-migration-stmicroelectronics.pdf)

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Application Notes [https://www.st.com/resource/en/application\\_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3126-audio-and-waveform-generation-using-the-dac-in-stm32-products-stmicroelectronics.pdf)

Application Notes [https://www.st.com/resource/en/application\\_note/an3154-can-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an3154-can-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf)

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- Application Notes [https://www.st.com/resource/en/application\\_note/an4989-stm32-microcontroller-debug-toolbox-stmicroelectronics.pdf](https://www.st.com/resource/en/application_note/an4989-stm32-microcontroller-debug-toolbox-stmicroelectronics.pdf)
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